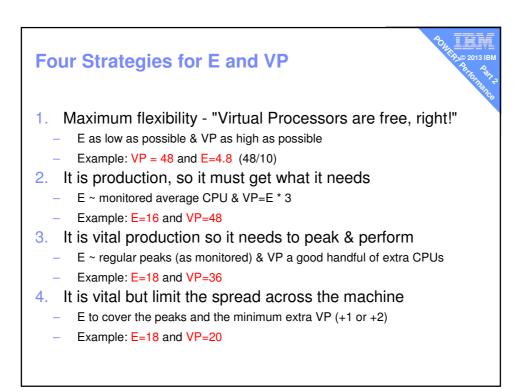
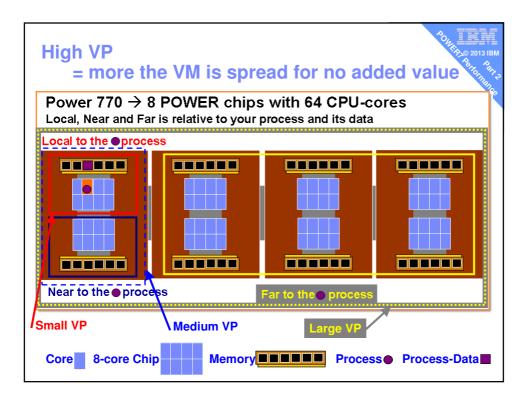
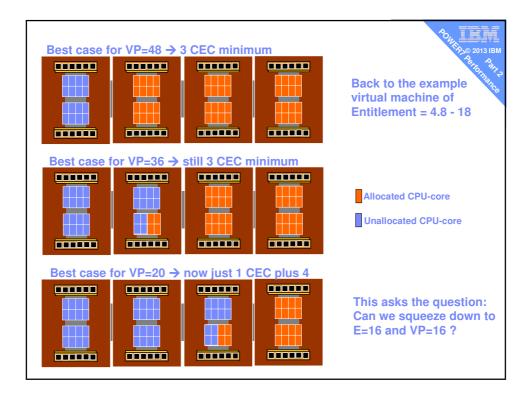


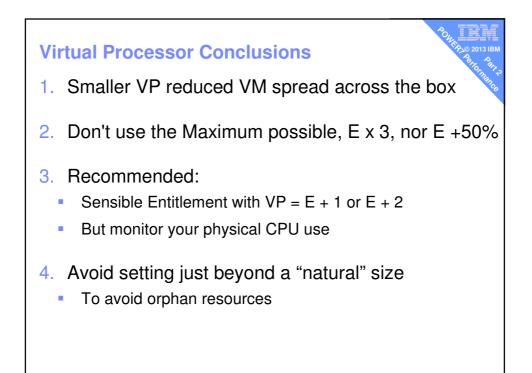
## If Entitlement=16 and unused VP's are folded ...

Is there an difference between VP=20, 32, 48, ... ?

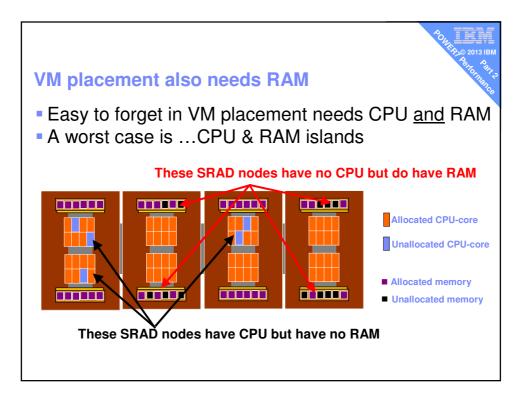


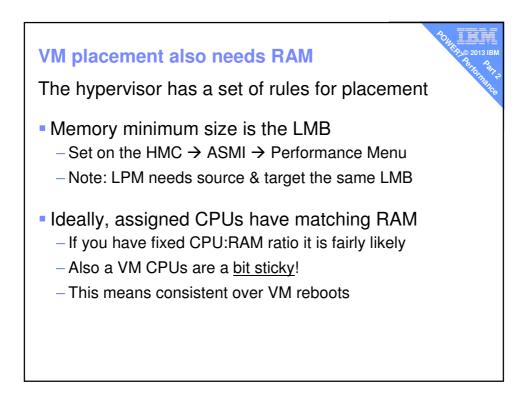


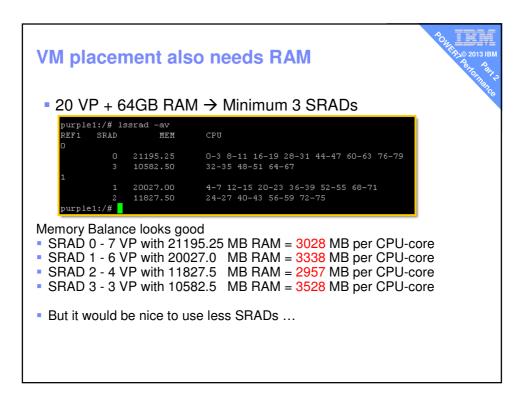


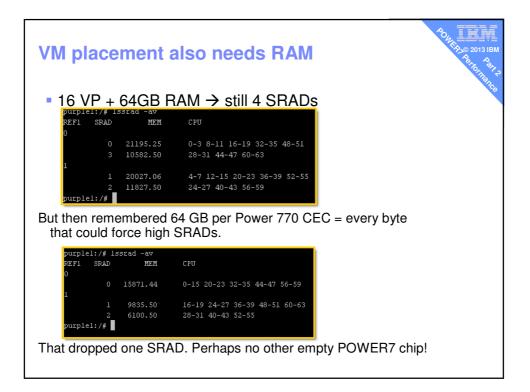


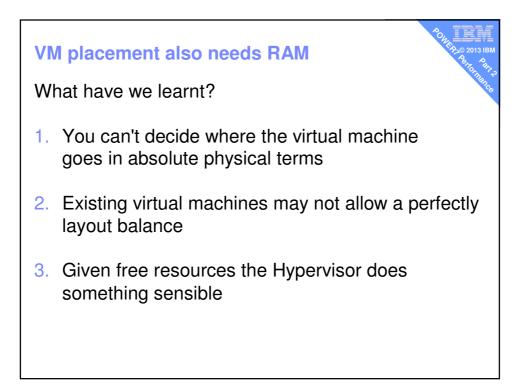


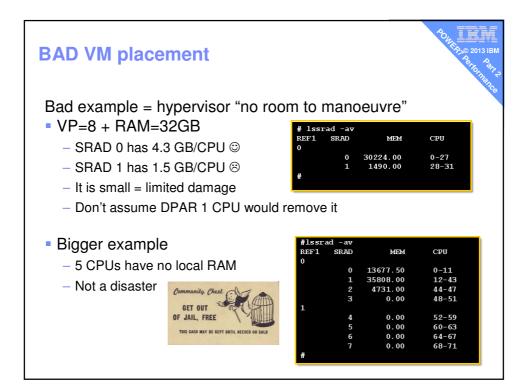


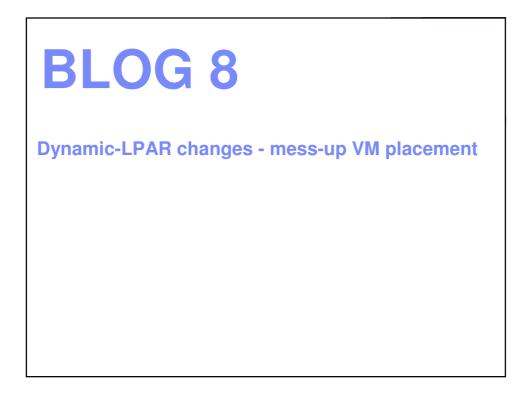


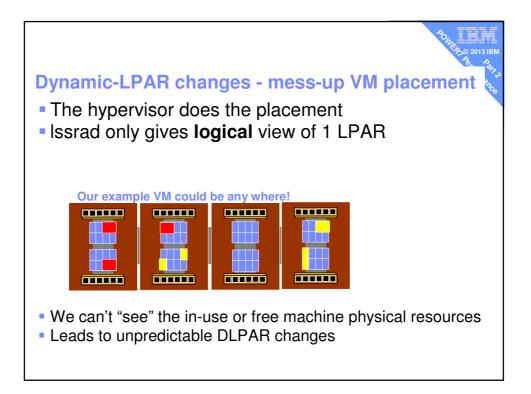


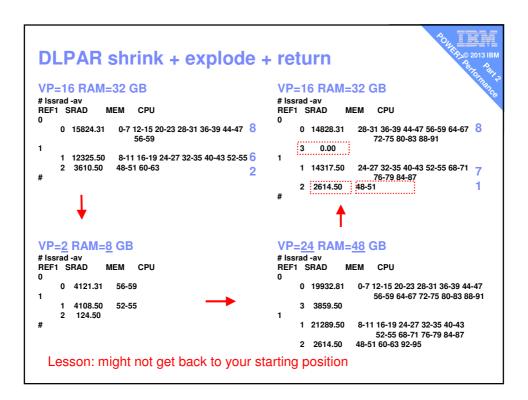


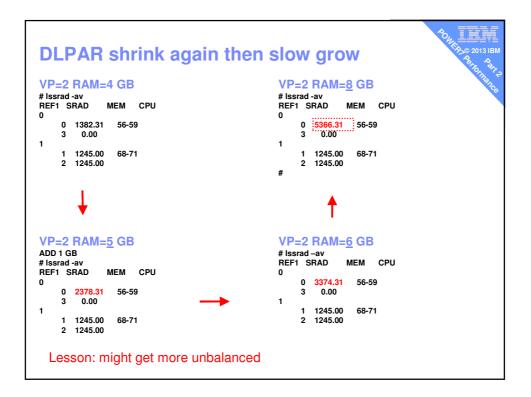


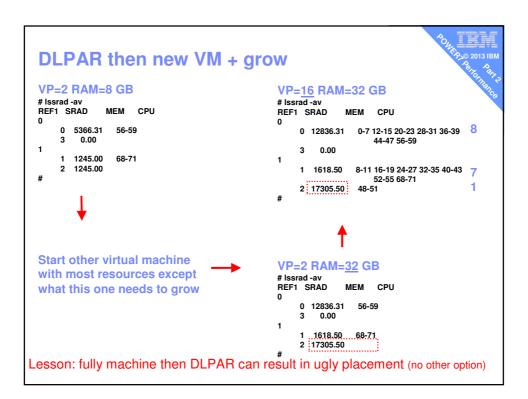


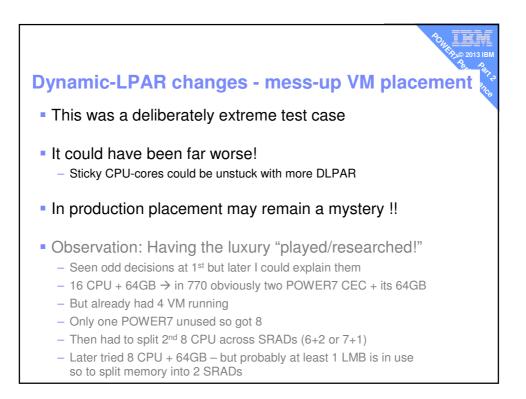


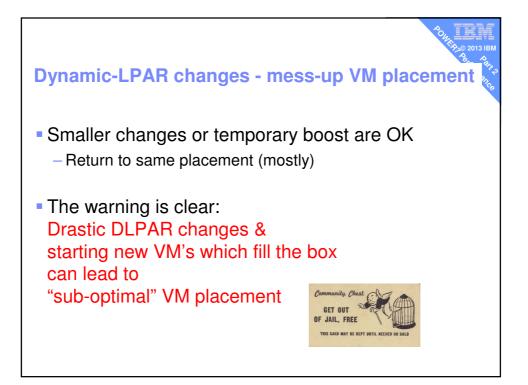




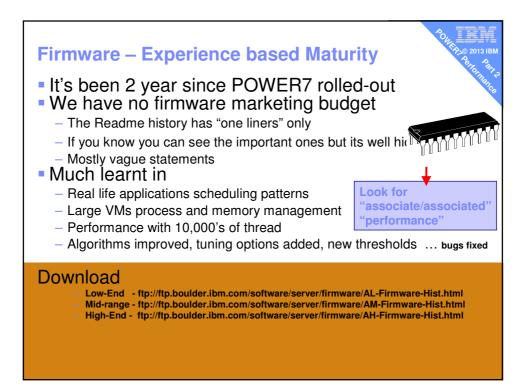


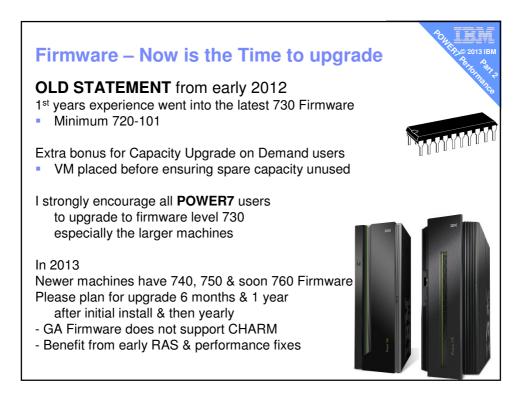


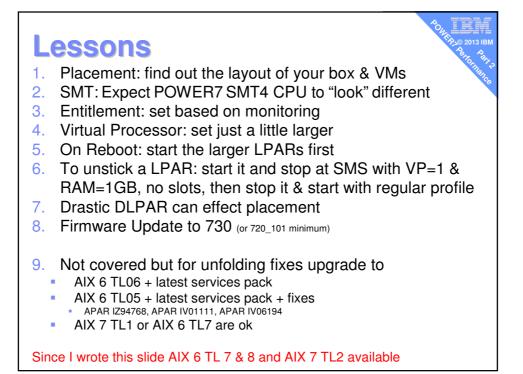




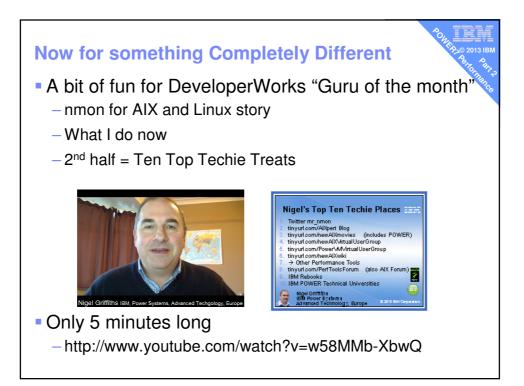




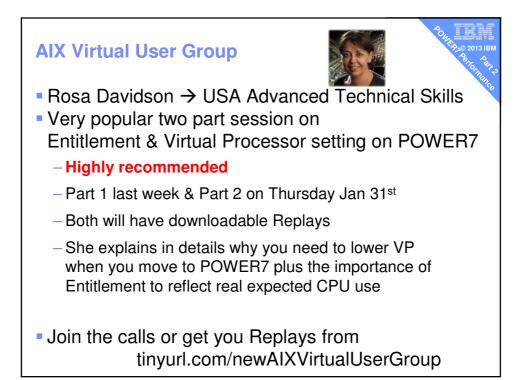


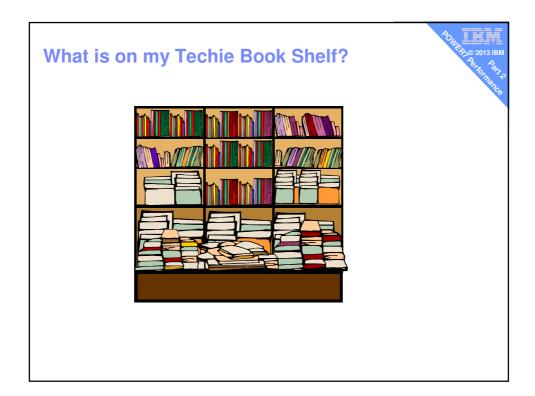




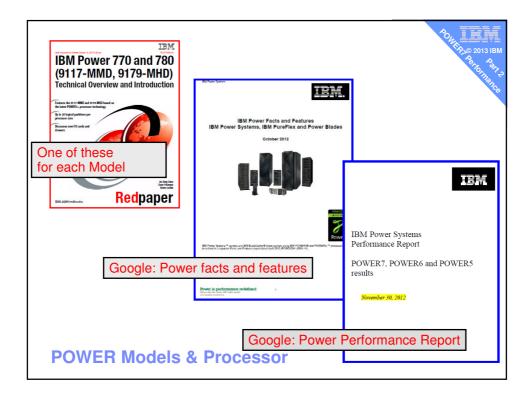




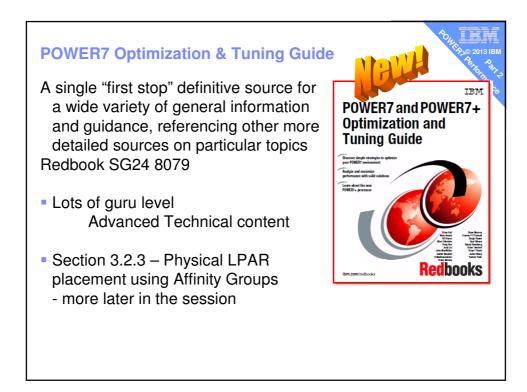


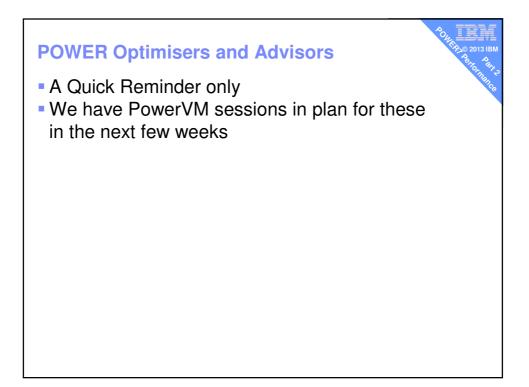




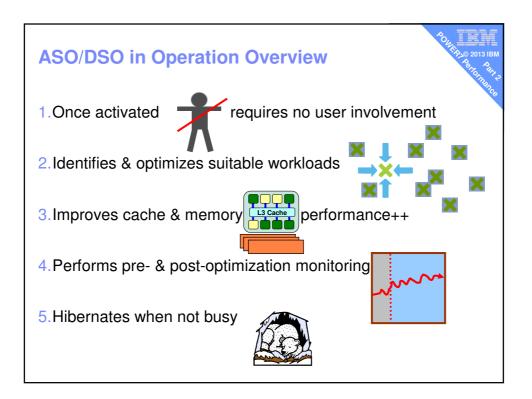


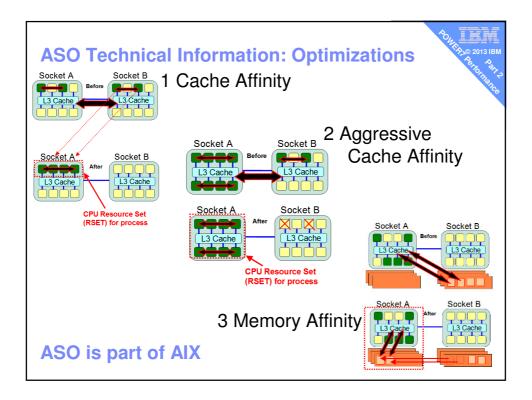


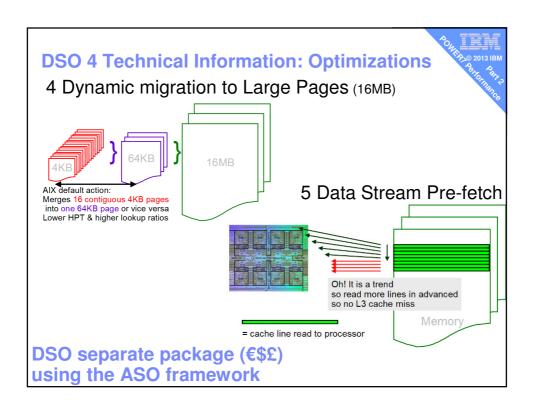


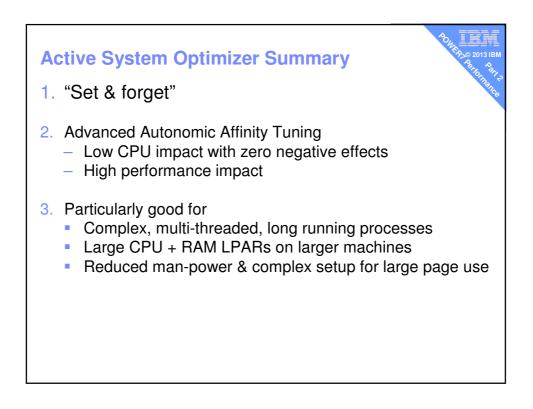


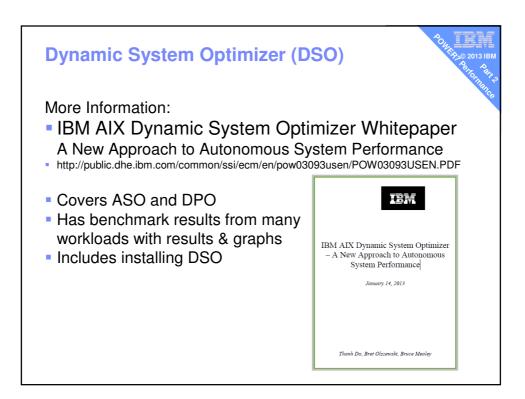




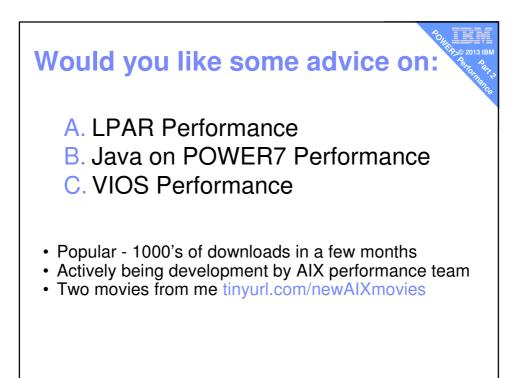


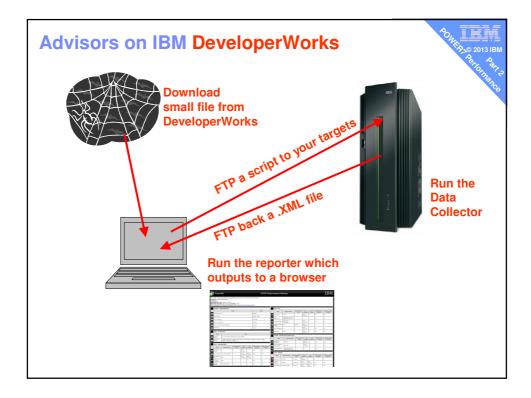






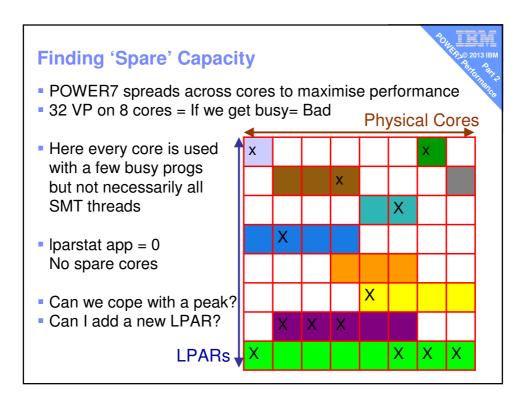


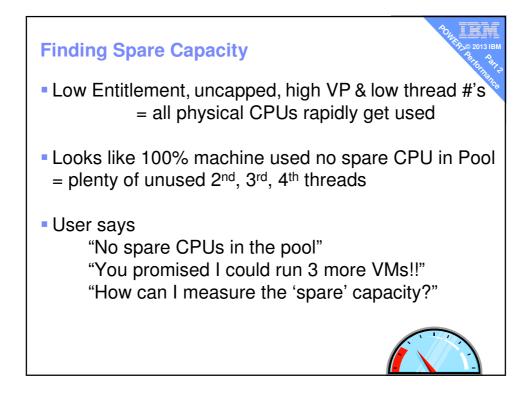


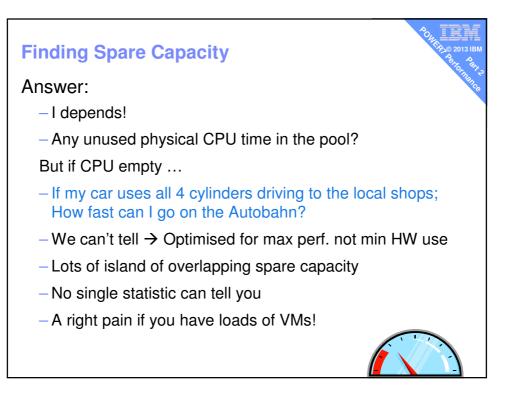


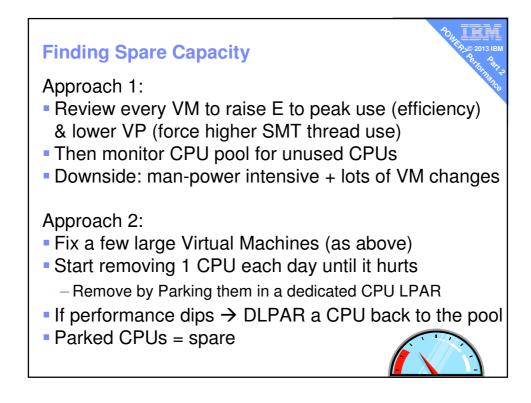
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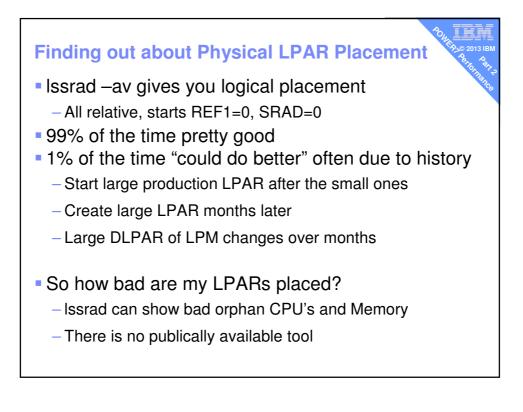


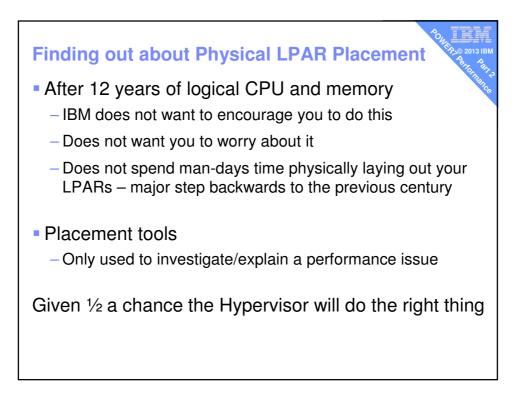


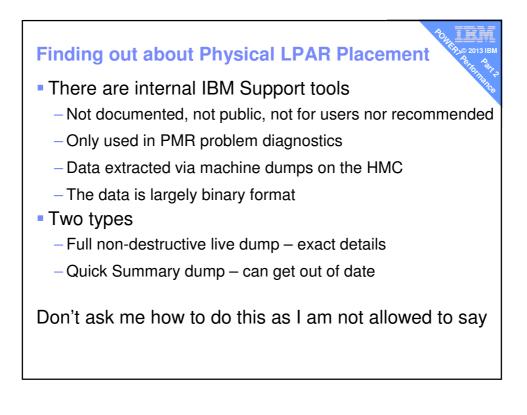




## Physical LPAR Placement



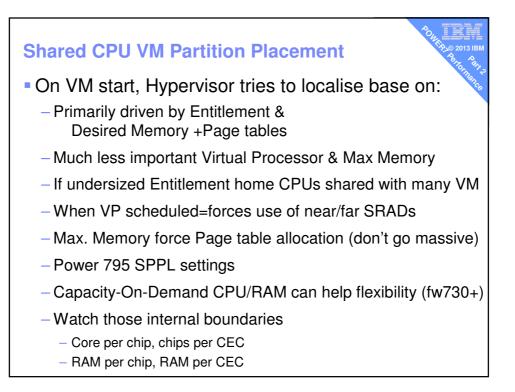


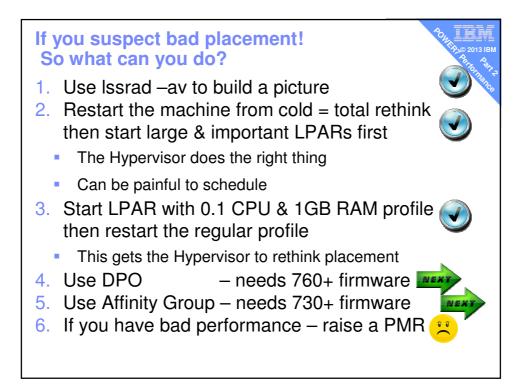


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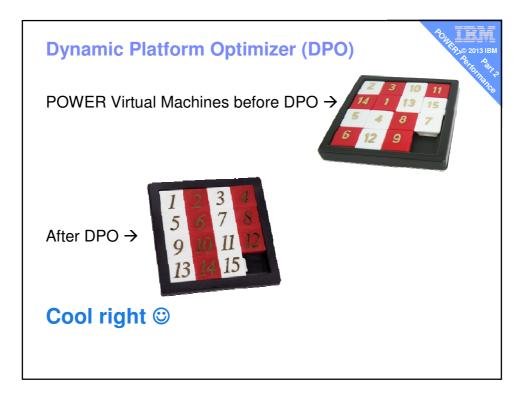
This machine has 128 GB of memory
 "LP" is the LPAR number as seen on the HMC

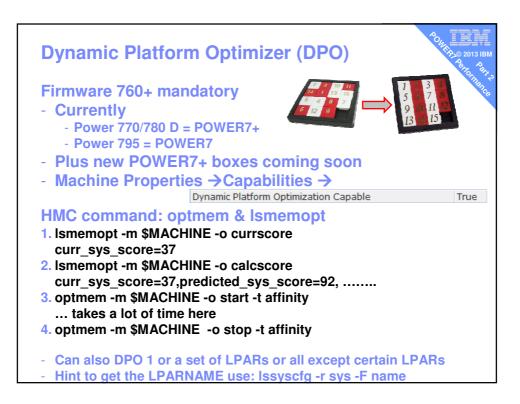


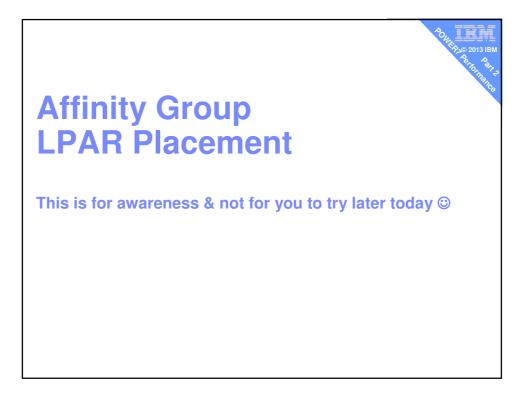


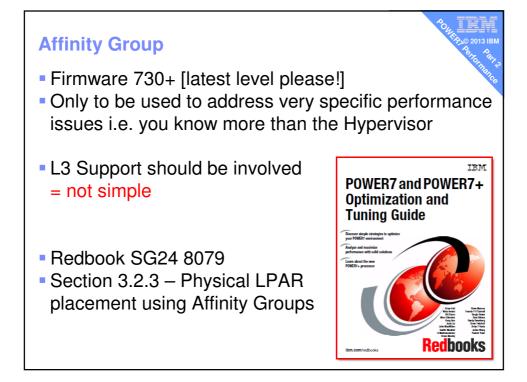


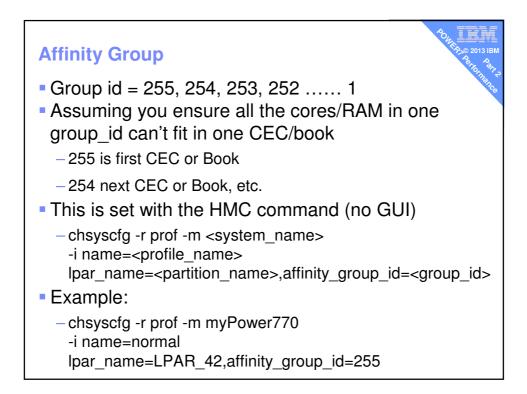












## **Affinity Group**

Treat each group\_id = CEC/Book as a bucket

- You need to assign LPAR(s) with
  - Entitlement & VP
  - and memory sizes
  - that fit and you have to allow for memory for
  - Page tables 1/64th
  - Hypervisor tricky System Planning tool can help
  - DMA buffers for adapters ditto
- You end up with a spreadsheet to write HMC script

## Affinity Group

- Apply the HMC script to the LPAR profiles
   Can do this in advance with the LPARs running
- Shutdown the machine
- Cold start the machine
- Hypervisor places them the way you like
- Then get the Physical LPAR placement
   With L3 Support (of course)

