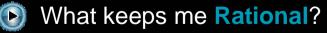
### WIND RIVER

## Integrated Device and Systems Development with IBM Rational and WindRiver

Optimizing device software and systems development

IBM Rational Software Development Conference UK 2007







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TD3



# Agenda

- Complexities of systems and device development
  - Customer challenges & trends
- The Wind River and IBM Rational solution
  - Why are Wind River and IBM Rational teaming up?
- Joint solution: Integrated Device and Systems Development
- Resources to Learn More!



#### The leader in Device Software Optimization

Wind River enables companies to develop, run, and manage device software better, faster, at lower cost, and more reliably.





#### Leaders in Every Industry Rely on Wind River



30%

**Alcatel-Lucent** Cisco EMC Ericsson Hewlett-Packard Huawei Intel Juniper LG Electronics Marconi Motorola **Nokia-Siemens** Nortel Oki **UT Starcom** ZTE



20%

ABB Aailent **BMW Bosch/Blaupunkt Continental** DaimlerChrysler Delphi GF **General Motors** Honda Honevwell Hyundai Magneti Marelli Mitsubishi National Instruments Nissan **Rockwell Automation** Siemens Yasukawa



25%

**BAE Systems** Boeina EADS **General Dynamics Finmeccanica** Harris Honeywell ITT L3 Communications Lockheed Martin MHI NASA NEC **Northrop Grumman** Raytheon **Rockwell Collins Smiths Aerospace** Thales US Navy





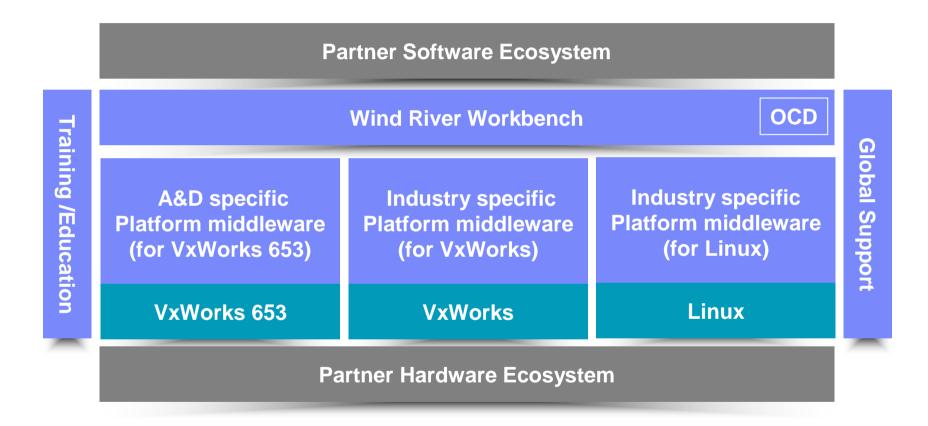
25%

Apple Datung Epson Ericsson Hewlett-Packard lwatsu LG Electronics Motorola Philips Ricoh Samsung Sanyo Sony Thomson Toshiba Verizon

350+ million devices worldwide use Wind River technology



### Wind River Market Specific Platforms





## Wind River's Real-Time Solutions

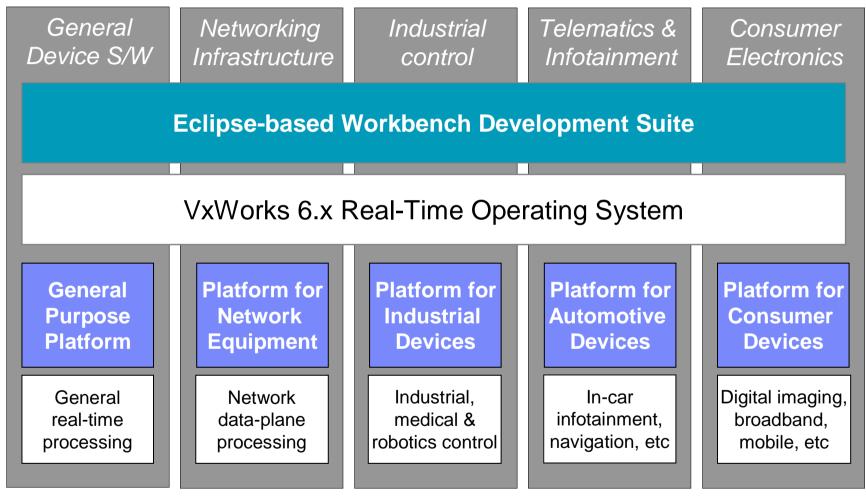
- VxWorks
  - Designed from the ground up to be a real-time operating system giving guaranteed\* real-time response times
- Wind River Linux using the PREEMPT kernel routines
  - Standard Linux provides a level of real-time response suitable—but not guaranteed—for a broad range of applications using widely adopted open-source technology
- Wind River Linux plus Wind River Real-Time Core
  - Linux plus Real-Time Core provides guaranteed real-time for applications that have more stringent Linux requirements

\* Guaranteed: Meets required response 100% of the time, regardless of system load.

Wind River delivers the widest choice of real-time solutions

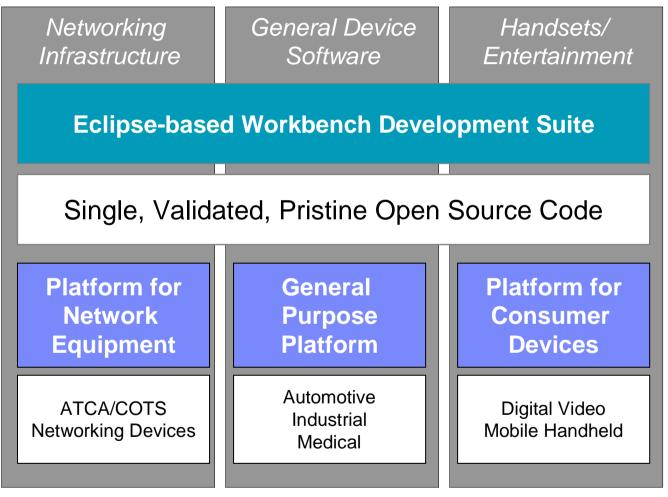


### Wind River VxWorks Platforms





### Wind River Linux Platforms





# Why are Wind River and IBM teaming up?

- Customers deal with a variety of solutions from many different sources, and often bear the cost of ensuring their tools work together
  - Cost of training, maintenance and customization of tools
  - Hidden cost of overlapping technologies (e.g. multiple editors, debuggers, build systems)
- An integrated lifecycle systems solution is valuable to our customers, to allow them to use one solution to develop both serverside and client-side applications
  - Links enterprise/host side development directly to device software development
  - Better utilizes development tools across a broader set of applications
  - Realize higher productivity gains from better workflow integration
- Many customers have asked that we work together!
  - IBM Rational's top customers in the Systems Development space are the same as Wind River's in the Device Software Optimization space
- Govern system of systems that are using Multicore Technology
  - > Help customers design, build and govern complex systems in a multicore environment



#### **Open Platform** *A new route to collaboration and innovation*

#### Open standards

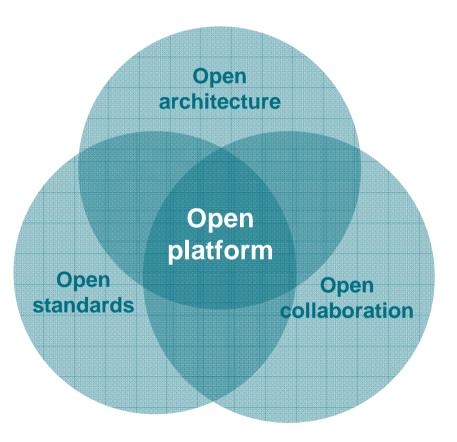
 Improve data sharing by simplifying integration of disparate technologies

#### Open architecture

 Increase collaboration by easily extending enterprise processes

#### Open collaboration

 Drive integrations and artifact traceability throughout the systems dev. lifecycle





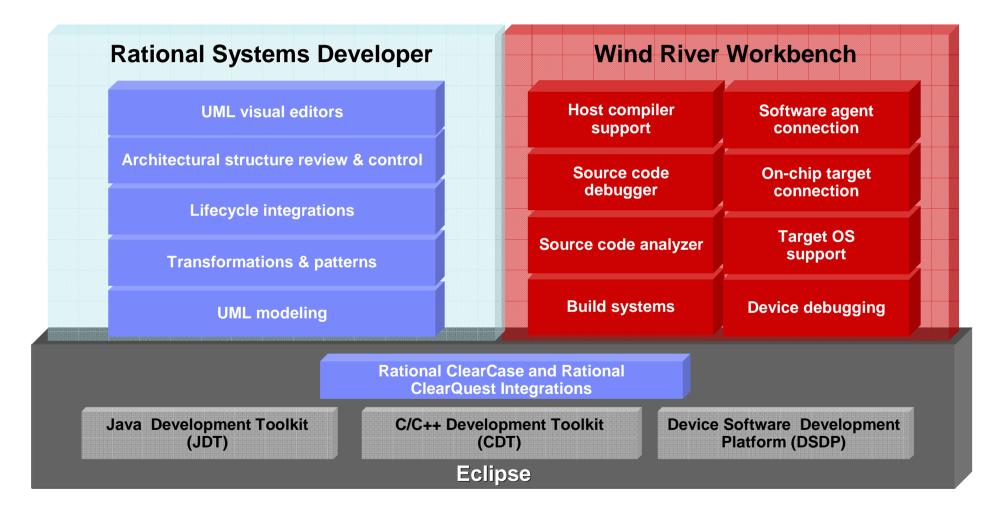
# Current complimentary integrated device and systems development solution

Process and Portfolio Management	Design and Construction	Software Quality
<ul> <li>IBM Rational Portfolio Manager</li> <li>IBM RUP-SE</li> <li>Change Management</li> <li>IBM Rational ClearCase</li> <li>IBM Rational ClearQuest</li> <li>IBM Rational BuildForge</li> </ul>	<ul> <li>IBM Rational RequisitePro</li> <li>IBM Rational Systems Developer</li> <li>IBM Rational Rose Technical Developer</li> <li>IBM Rational Ada Developer</li> <li>IBM Rational Ada Developer</li> <li>Wind River Workbench</li> <li>Wind River ScopeTools</li> <li>Wind River System Viewer</li> </ul>	<ul> <li>IBM Rational Functional Tester</li> <li>IBM Rational Manual Tester</li> <li>IBM Rational Test Real Time</li> <li>IBM Rational PurifyPlus</li> <li>Workbench Unit Tester</li> <li>Workbench Diagnostics</li> </ul>
Partner Ecosysten	n & Open Computing (Eclipse,	Linux, UML, SysML)
IBM Websphere	Wind River Linux	Platforms
IBM Tivoli	Wind River VxWc	orks Platforms
BM DB2		

**Middleware and Runtime Environment** 



#### Integrated Device & Systems Development Two industry leaders teaming to help customers and missions be successful





### **IBM Rational Systems Developer Capabilities**

#### <u>"Architectural</u> <u>Discovery</u>"

- Automatic anti-pattern and pattern detection
- Architectural discovery, analysis, metrics, and stability reporting
- Implementation level architectural rules

#### <u>"Modeler"</u>

- UML 2.0 Diagrams DoDAF
- OCL Support
- Pattern content
- Extensive open API
- Java-based "scripting" for extensibility
- HTML and XML based data extraction and reporting
- RAS tools
- Rose/XDE Model
   Import
- Traceability Analysis
- Visual Compare/Merge

Lifecycle Integrations

Java Structural Analysis

UML Language Transforms

Software Modeler

C/C++ Development Tools

Java Development Tools

ClearCase LT

#### **Operating Environments**

eclipse V3.1



#### "Lifecycle Integrations"

- ClearCase
- ClearQuest
- Requisite Pro
- SoDA
- RUP

#### "UML Language Transforms"

- Pattern/Transform authoring framework and services
- UML-to-code transforms for Java and C++
- Selective language to UML harvesting
- UML-to-CORBA IDL transformations

#### "C/C++ Development Tools"

- C/C++ editors and build management
- Compiler and debugger integration
- UML code editors for C/C++

#### "Java Development Tools"

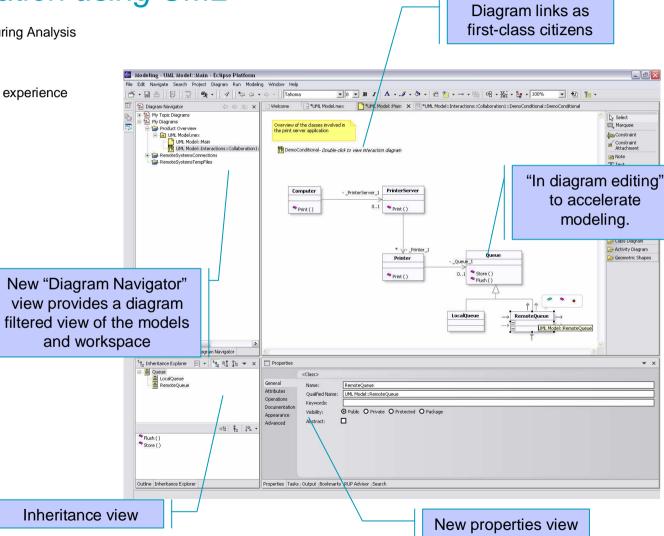
- Advanced Java tooling editors, projects, refactoring, etc.
- UML code editors for Java
- Code Review

#### IBM Rational Software Development Conference UK 2007



#### Model Driven Development: Model your application using UML

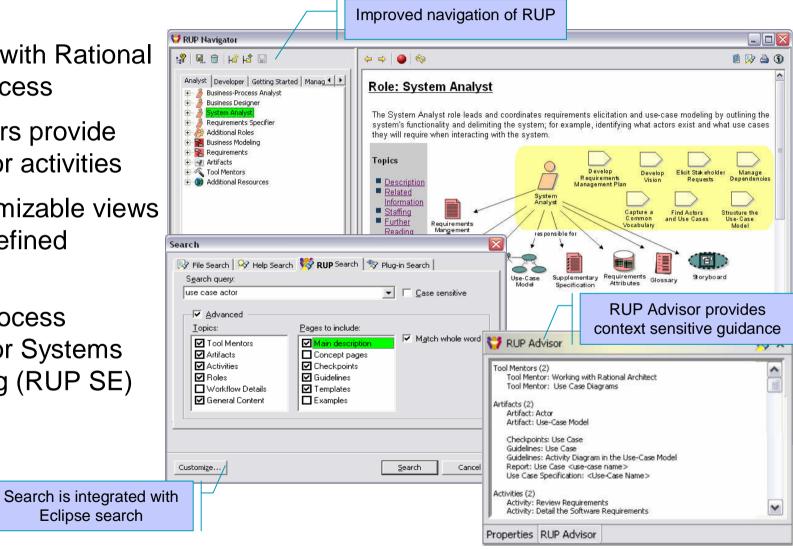
- Simplify the capture of UML models during Analysis and Design
- Pattern content & authoring support
- New custom views improve the editing experience
- Support for UML 2 diagrams:
  - Activity
  - Class
  - Communication
  - Component
  - Composite Structu
  - Deployment
  - Sequence
  - Use Case





### **Process Guidance in Rational Systems Developer**

- Integration with Rational Unified Process
- **Tool Mentors provide** guidance for activities
- User customizable views with user defined content
- Includes process guidance for Systems Engineering (RUP SE)

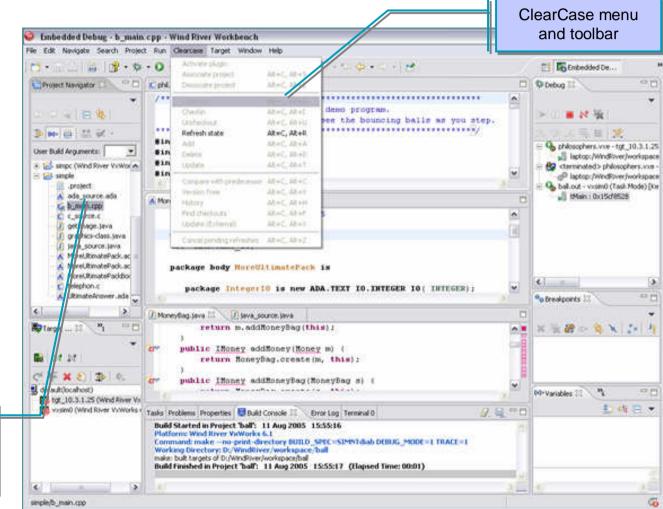




#### Rational ClearCase Integration with WindRiver Workbench

- Store and manage all of your software development assets, including your DSO source code
- Easily check in, check out, compare and merge your artifacts from within Workbench
- Integrates with both native ClearCase client and the new ClearCase Remote Client

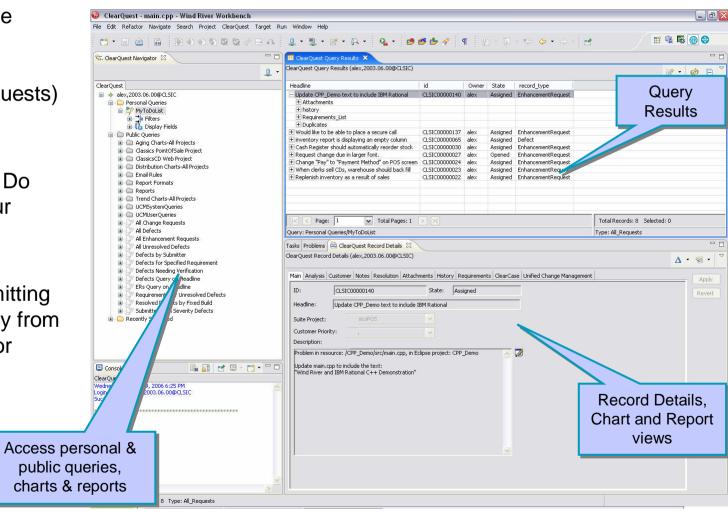
Access ClearCase context-sensitive menu directly from Workbench resources





# Rational ClearQuest Integration with WindRiver Workbench

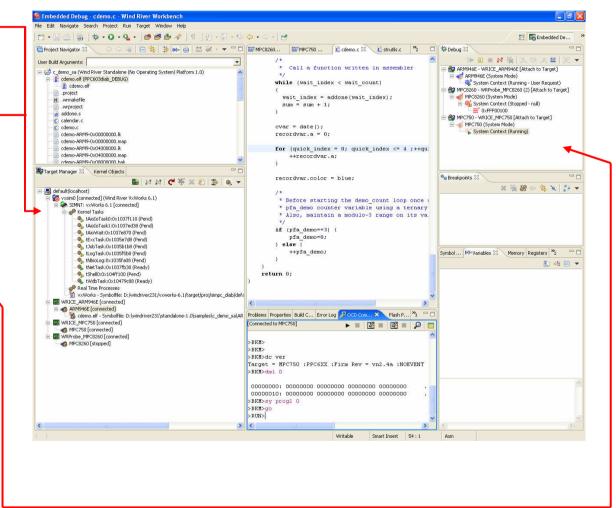
- Create and manage change requests (including defects, enhancements requests) in Workbench.
- Stay up to date by accessing your To Do Lists within the your development environment
- Save time by submitting new defects directly from application errors or other Workbench resources





### Workbench Multiple Session and Multiple Core

- Multiple Sessions/ Connections:
  - Software Agent Based
  - Wind River ICE
  - Wind River Probe
- Multiple CPUs or Boards:
- Multiple Contexts
  - System
  - Processes
  - Threads
- Linux Debugging
  - KGDB over Ethernet
  - KGDB over Serial
  - Usermode over Ethernet
  - Usermode over USB
- VxWorks Debugging
  - System mode debug
  - Task mode debug
  - Multiple connection types (ethernet, serial, JTAG)





# Wind River On-Chip Debugging Tools

#### Debugging enhanced with the addition of On-Chip Debugging support

- Support for Board and Operating System Bring-up
- CPU and Board initialization
- Analyze and debug system crashes
- Program Flash devices in-circuit
- Built-in diagnostics for board testing
- Kernel mode debugging with OS Awareness
  - VxWorks
  - Linux
  - Express Logic ThreadX

#### Hardware supported

Wind River ICE

- High Speed Ethernet connectivity
- Wind River JTAGServer<sup>™</sup> support for multiple JTAG/EJTAG/BDM devices
- Target Console Interface for backhaul of serial data from target to host development environment
- Easy migration from one processor family to another



#### Wind River Probe

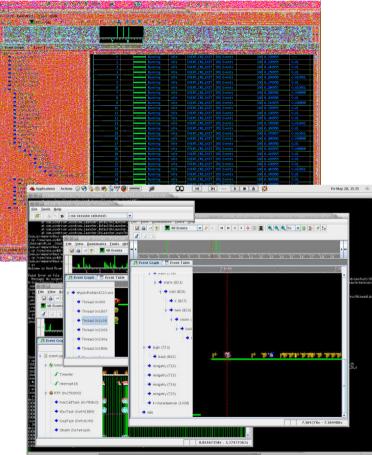
- USB 2.0 Compatible
- USB Windows Device Driver Certified
- USB Powered, no additional power supplies required
- Easy migration from one processor family to another
- 100MHz JTAG Clock support



### Wind River System Viewer

#### Graphical visualization of all system activity over time

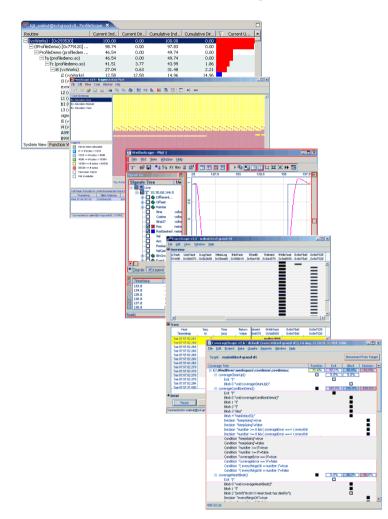
- Reveals complex interactions of tasks, threads, interrupts, and system objects
- Detect deadlocks, starvation, and race conditions
- Understand performance problems due to wrong priorities or resource contention



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#### Wind River ScopeTools



A set of dynamic visualization and analysis tools that help developers uncover software issues from memory leaks, to performance analysis, and execution trace

- Memory leak detection and visual analysis
- Execution flow tracing tool
- Statistical Profiling of threads and functions
- Graphical visualization of system variables over time



### **ProfileScope Finds the Hotspots**

Analyze CPU usage of all threads and functions executing on the processor

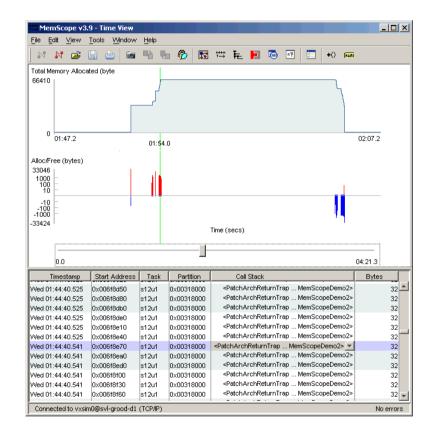
듣 tgt_walnut@svl-grood-d1, Profile	:Scope 🗙 🔪					
Routine	Current Ind	Current Dir	Cumulative Ind	Cumulative Dir	💎 Current U	. 🔺
<mark>-</mark> (vxWorks) : [0x293530]	100.00	0.00	100.00	0.00		
🖃 (tProfileDemo) [0x779120]	98.74	0.00	97.83	0.00		
🖃 ProfileDemo (profiledem	46.54	0.00	49.74	0.00		
🖃 fa (profiledemo.so)	46.54	0.00	49.74	0.00		
🖃 fc (profiledemo.so)	41.51	3.77	43.99	1.86		
🖃 i8 (v×Works)	27.04	0.63	31.48	2.21		
i2 (vxWorks)	12.58	12.58	14.96	14.96		
i3 (vxWorks)	5.66	5.66	3.33	3.33		
exmul (vxW	3.77	3.77	6.69	6.69		
L2 (vxWorks)	1.89	1.89	2.42	2.42		
L1 (v×Works)	0.63	0.63	0.16	0.16		
b1 (vxWorks)	0.63	0.63	0.26	0.26		
L3 (vxWorks)	0.63	0.63	0.07	0.07		
sigmax (v×V	0.63	0.63	0.27	0.27		
i5 (vxWorks)	0.00	0.00	0.66	0.66		
i4 (v×Works)	0.00	0.00	0.19	0.19		
A999 (v×Wc	0.00	0.00	0.19	0.19		
B999 (v×Wc	0.00	0.00	0.08	0.08		-
System View Function View Proces						



#### **MemScope Catches Memory Leaks**

#### Capture memory allocations and frees across ENTIRE system

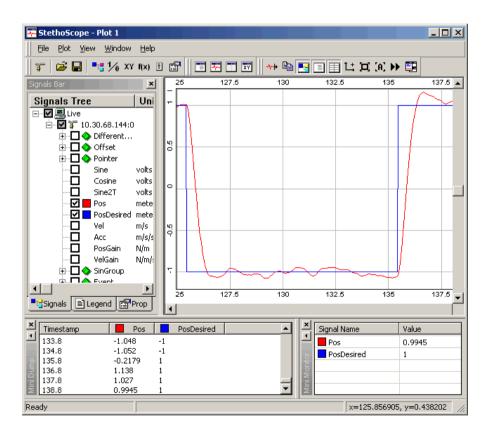
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By Allocation Size					
By Allocation Module					
By Allocation Task					
Legend					
Never been allocated					
0 <= # bytes < 1024					
1024 <= # bytes < 4096					
4096 <= # bytes < 16384					
16384 <= # bytes < 65536					
65536 <= # bytes					
Has been free'd					-1
Not Available					
	Top Address: 0x02000	0000 Bottom	Address: 0x000000		
				1 32	1K 32K
Individual Allocations under the selec	ted block (0x006ebd38,	0x006ebd31)			
Timestamp Start Addre	ess Task	Partition		Call Stack	Current Bytes
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a	(8.65.10)				
Connected to vxsim0@svl-grood-d1	(TCP/IP)				No errors

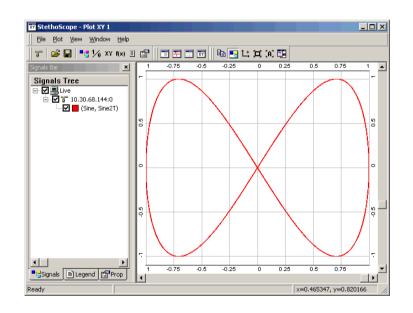




#### StethoScope Reveals System Behavior

Variables graphed in real time help you tune your system







#### TraceScope Unmasks Integration Issues

Chart function call sequences and entry & exit parameter values

<b>File</b> Edit		- walnut@s ndow Help	vl-grood-d1								×
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onnected	to walnut@	svl-grood-d1		T	arget initi:	alized				No overflo	ws



### CoverageScope Highlights Untested Code

Helps complete your test suite by showing what's been tested and what hasn't

ille <u>E</u> dit <u>P</u> roject <u>D</u> ata <u>G</u> raphs <u>R</u> eports <u>W</u> indow <u>H</u> elp			· · · · · · · · · · · · · · · · · · ·			ageScope v2.6 - C:\WindRiver\workspace3\c	:ovDemo\covdemo.c	
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C:\WindRiver\workspace\covdemo\covdemo.c	71.4%	57.1%	90.0%	91.7%	165	1 /* contains a deliberate bug, sets everythingsOK t	to 0 here	
🖃 coverageCleanUp()		0.0%	0.0%		167	on the last loop if coverageError is not equal to (	o */	
Exit "}"					168 🗖 169 🗖	if(numIter >= 0 && (coverageError == 0    (ever)	/thingsOK = humIter)))	
Block 0 "void coverageCleanUp()"					170 🔳	keepGoing = 1;		
coverageConditionDemo()		100.0%	100.0%	100.0%	171 II 172 II	numIter;		
Exit "}"					173 🔳	else		
Block 0 "void coverageConditionDemo()"					174 ■ 175 ■	{ keepGoing = 0;		
Block 1 "{"					175	}		
Block 2 "{"					177	taskDelay(5);		
Block 3 "else"					178 <b>1</b> 79 <b>1</b> 79	8		
Block 4 "taskDelay(5);"					180			
Decision "keepGoing"=true					181 /*	coverageHeartBeat - a task that beats once a secon	d as long as	
Decision "keepGoing"=false					183 *	everything is ok.		
Decision "numIter >= 0 && ( coverageError == 11 ( everythin					184 */	id coverageHeartBeat()		
Decision "numIter >= 0 && ( coverageError == 11 ( everythin					186 🔳 {			
Condition "keepGoing"=true						while(everythingsOK)		
Condition "keepGoing"=false					188 <b>■</b> 189 <b>■</b>	1 /* Heart beat message here */		
Condition "numIter >= 0"=true					190 🔳	coverageSleep(1);		
Condition "numIter >= 0"=false					191	printf("Acck!!!! Heart beat has died!\n");		
Condition "coverageError == 0"=true					193 🗆 }	prince (income bode has allowing)		
Condition "coverageError == 0"=false					194 195 /*			
Condition "( everythingsOK = numIter )"=true					196 *	coverageSleep - a simple sleep function that delays	the task for	
Condition "( everythingsOK = numIter )"=false								
coverageHeartBeat()		0.0%	66.7%	50.0%	Highlight:	Function Coverage		
Exit "}"	_					🔽 Exit Coverage	FullyCovered	Custom
Block 0 "void coverageHeartBeat()"						Block Coverage	PartiallyCovered	Custom.
Block 1 "{"								
Block 2 "printf("Acck!!!! Heart beat has died!\n");						Condition Coverage	NotCovered	Custom.
Decision "everythingsOK"=true			_			i conación coverage		

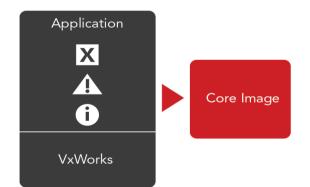


#### **Workbench Diagnostics**

A root-cause analysis tool set that enables development and test engineers to securely record, isolate, diagnose and correct device software defects in a running system

	• Sensor Point 1
Application	• Sensor Point 2
	• Sensor Point 3
	• Sensor Point 4
Operating System	• Sensor Point 5

Tag	Value	Time	Time Delta	Task
parameter	"Zip" (parameter) =94501	2811.10444434	0	tShell
parameter	"Street" (parameter) =0x744eb8, *"Street" (parameter) ="500 Wind River Way"	2811.10444434	0	tShell
parameter	"City" (parameter) =0x744ecc, *"City" (parameter) ="Alameda, CA"	2811.10444434	0	tShell
parameter	\$return =0	2811.10455498	0.00011064	tShell
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subprogram_entry	Entering SetRecord()	2811.1045959	0	tShell
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parameter	\$return =0	2811.10468164	0	tShell
subprogram_exit	Exiting demo()	2811.10469688	0.00001524	tShell
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parameter	"Zip" (parameter) =94501	2811.10472034	0.00001104	tShell
parameter	"Street" (parameter) =0x744eb8, *"Street" (parameter) ="500 Wind River Way"	2811.10472034	0	tShell
parameter	"City" (parameter) =0x744ecc, *"City" (parameter) ="Alameda, CA"	2811.10472034	0	tShell
subprogram_entry	Entering SetRecord()	2811.10472034	0	tShell
parameter	"mr" (parameter) =0x9327b8, *"mr" (parameter) =00744e9800744eac0000560e	2811.10472034	0	tShell
parameter	\$return =0	2811.10480662	0.00008628	tShell
subprogram exit	Exiting SetRecord()	2811.10480662	0	tShell





# Benefits for Systems Development Teams

#### Complete, end to end support for architecting, building and governing device and systems development

- Manage your requirements and link to model artifacts
- Model your DSO application with extensive UML 2.0 support
- UML-to-code transformations to Java & C++
- Visualize and edit existing C/C++ source code in UML diagrams
- Comprehensive source code editing, compilation and debug capabilities
- Target management, deployment and remote debugging support
- Faster, multi-language static code analysis
- Entirely data file driven Managed Build System
- Improve team productivity and collaboration
- Manage all development assets in Rational ClearCase, including RSD models and Workbench source code artifacts
- Stay up to date on current change requests & other activities in Rational ClearQuest
- Context sensitive process guidance and tool mentors for systems & device development



### Key Takeaways

IBM and Wind River provide a complete lifecycle solution for integrated device and systems development

- Build devices and systems faster with higher quality of products
- Address a broader set of applications with a single toolset
- Realize higher productivity gains from better workflow integration
- Allows for data integration across the enterprise and deployed devices
- Cuts out the sometimes hidden cost of overlapping technologies
- Drive improved program management and team productivity across systems development efforts



The power of Eclipse



## Resources to Learn More!

- IBM Rational Systems Development Solution on ibm.com
  - http://www-306.ibm.com/software/info/developer/solutions/systems/index.jsp
- IBM Rational Systems Development Solution eKit (for whitepapers and on demand webcasts)
  - http://www-306.ibm.com/software/info/sdp/systems/index.jsp
- IBM Rational Ready for Rational Partner Plug-ins:
  - http://www-128.ibm.com/developerworks/rational/downloads/ready.ht

# WindRiver WorkBench

http://www.windriver.com/products/development\_suite/



