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Bijan Davari received his M.S and Ph.D. degrees in Electrical Engineering from Rensselaer Polytechnic Institute, Troy, NY in 1984. He then joined IBM Research Division, Thomas J. Watson Research Center, Yorktown Heights, NY, where he worked on various aspects of high-performance CMOS technologies for IBM mainframe and UNIX systems . Dr. Davari was appointed IBM Fellow in 1996, and has been the Vice President of Next Generation Computing Systems and Technology since 2003. In this capacity, Dr. Davari leads the efforts for the definition and implementation of IBM's next generation systems, employing Massively Multi Threaded (MMT) architectures and Special function engines in Workload Optimized Systems. This activity integrates IBM's technical disciplines in Hardware and Software (including; silicon, packaging, cooling, system architecture, design tools, compilers, middleware, etc.) with the Client requirements in the new and emerging business applications in various fields. Dr. Davari received the J.J.Ebers award in 2005 and the Andrew S. Grove Award in 2010. He has authored and co-authored more than 70 publications on various aspects of semiconductor devices and technology. He is an IEEE fellow.