

z/TPF V1.1

2013 TPF Users Group

Title: z/TPF Debugger Education



AIM Enterprise Platform Software IBM z/Transaction Processing Facility Enterprise Edition 1.1

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Agenda

- What's new in the realm of debugger education?
 - Education resources and links
- Debugger education articles
 - Problem diagnosis
 - Custom communication packages
 - Determining code path
 - Hints and Tips
 - Starting the debugger effectively
- Q & A



What's new in the realm of debugger education?

- A new set of practical education articles have been written.
 - They focus on how to use debugger features together to solve problems.
 - They also focus on the lesser known or hard to find features.
 - A sample of this content will be the main focus this presentation.
 - The list of the new articles is available on the next slide.
- A new set of appendices have been added to the z/TPF Application Modernization using Standard and Open Middleware Redbook
 - They focus on step by step examples of how to use debugger features. These appendices are applicable to anyone new to the TPF Toolkit or wanting to learn about a variety of features.



Education resources and Links

- The following resources focus on how to use debugger features together to solve problems and on lesser known features.
- http://www-01.ibm.com/software/htp/tpf/. See the Fast links section on the lower left side. Select Tools -> z/TPF Debugger and then view the contents of the education material table.

developerworks.com article

 Debugging Entry Control Blocks created by custom communication packages on z/TPF

Debugger education articles

- Determining code path
- Starting the debugger effectively
- Problem diagnosis
- Hints and Tips



Education resources and Links

- These resources are a good source for seeing step by step usage:
- http://www-01.ibm.com/software/htp/tpf/. See the Fast links section on the lower left side. Select Tools -> z/TPF Debugger and then view the contents of the education material table.

z/TPF Application Modernization using Standard and Open Middleware Redbook

 There are several appendices with step by step demonstrations of building and loading an application in the TPF Toolkit, Web Services features, Debugger, Code Coverage Tool, Performance Analyzer, Dump viewer, Trace Log and etc.

Debugger Demo Movie

• This demo movie was created several years ago to highlight the function that was available at that time. Even though this movie is out of date, the education delivered in this format has been found to be very useful and the core function described continues to exist.

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Education resources and Links

- These existing resources are a good source learn what functionality exists.
 - http://www-01.ibm.com/software/htp/tpf/. See the Fast links section on the lower left side.
 - TPFUG presentations select TPFUG Presentations. A debugger and TPF Toolkit update is often provided at each TPFUG to announce new features, provide education and so on. These presentations are usually given in the TPF Toolkit Task Force or the Development Tools Subcommittee.
 - The Debugger User's Guide select TPF Family Libraries -> Open Current Information Center -> z/TPF PUT -> Library -> Debugger User's Guide.
 - TPF Toolkit help that is found in the Help menu also provides information regarding the features that are available. Select the Help menu -> Help contents. Then select Debugging TPF Applications, Analyzing Code Coverage of TPF Applications, or Analyzing Performance of TPF Applications.

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Problem diagnosis

- Topics
 - Dump viewer
 - Debugging stack corruption
 - Debugging heap corruption
 - Debugging infinite loops
 - Debugging memory leaks



Dump viewer

- The dump viewer is a debugger like interface to view the contents of a dump.
 - The dump viewer is especially useful for C/C++ code with the ability to use the variables view to see all C/C++ variables at a glance. You can click through the stack frames and see C/C++ variables on previous stack frames.
 - The dump viewer provides the ability to apply XML maps in the memory views of given data areas to make it easier to read the data in the memory of the application.
 - Most debugger views will work as normal such as the SW00SR view, DETAC view, DECB view, TPF malloc view and so on, which could be difficult or impossible to view in a traditional z/TPF dump.
- Enter ZASER DUMPON DBUG to collect dump viewer dumps.
- The user exit UDDC_debuggerDumpCaptureUserExit in cdbaux.cpp allows you to capture additional data areas.
- These dumps are portable for viewing from z/TPF system to z/TPF system because the program attribute table (PAT) entries, database definition (DBDEFs), and so on are completely copied to the dump file.



Dump viewer

 The ECB trace can tell you what the ECB was doing recently. It will show you the macros and functions called as well as parameters passed in and values returned. The ECB trace is available while viewing dumps through the debug console command ECBTrace. A variety of other debug console commands are available.



Dump viewer

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- The following techniques apply to both the debugger and dump viewer.
 - Click through the stack frames in the debug view and see what the local variable values are in the Variables view. You may notice that a character array containing a valid string appears to pour over into other variables in your stack. This can be an indication that your application is mishandling that string variable.
 - As you click through the stack frames, the properties view will show you details about that stack frame (size, address, etc).
 - You can also see the contents of the stack frame. Right click on a stack frame and choose map memory element to open an XML map of the stack frame in the memory. View picture on the next slide.

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- A couple things to take notice of in the stack frame:
 - Register 14 (R14) is the typical return address register in the z/TPF system. However, if R14 points into CPS0, it is likely a C/C++ cross module call and the return address is found in CRET.
 - A bad back chain pointer (BCH) often indicates that the application is overwriting the stack by way of a memcpy, MVC, and so on.
 - This tip works frequently. Look at the stack contents rendered in EBCDIC or ASCII for a text string. Try doing a grep for that string in your application code. sprintf and similar functions are often the cause of stack corruption and this approach has been used to solve many of these types of dumps.
 - Another approach is to examine the contents of the entry control block (ECB) trace for function and macro parameters and return values that point into the stack address range as they may be the cause of the stack corruption.

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Debugging stack corruption

 If you are using the debugger and know that a particular stack address will become corrupted (such as the back chain pointer or a variable such as i), you can use the watch breakpoint support to stop the debugger when the change occurs.



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Debugging stack corruption

- Enter an address as 0x1234, a pointer expression or & of the variable such as &port.
- The debugger will stop at the source line/instruction after the source line/instruction that modified the storage.





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< ק qdb0.cpp 🛙			
Line 55	Column 1 Insert	Browse	
		4891	10
	43 enum debugJavaOpt 44 int	testgroup; num parms = 0; // for saving IPRSE parse rc	
	45 int	i = 0;	
	46 int	testcase = 0;	
	47 int	childDbgCase = 0;	
	48 int	num total = NUM TOTAL;	
	49 unsigned short	port = 7999;	
	50 char	* block ptr; // Pointer to core block	
	51 char	* input_ptr; // pointer to message text	
	52 char	* ip_ptr;	
	53 char	* pgm_ptr;	
	54 char	* file_ptr;	
•	55 char	<pre>* kick = NULL;</pre>	
	56 char	* addr = NULL;	
	57 char	<pre>* file = NULL;</pre>	
	58 char	<pre>* threadParm = NULL;</pre>	No details to display for the current selection.
	59 char	<pre>* sys_state = (char *) cinfc_fast(CINFC_CMMSTI);</pre>	
	60		
	61 /* Parser grammer. */		
	62 char	* grammar_ptr =	
	63	IPRSE_STRICT_GRAMMAR IPRSE_MIXED_CASE_GRAMMAR	
	64	"{"	
	65	"{"	
	66	"{ ERR-d++"	
	67	" GO-d++"	
	68	" EXP-d++"	

- A CTL-75 is a dump indicating that heap (malloc) corruption has occurred.
- However, this detection for the CTL-75 dump occurs when the malloc block is freed.
- CTL-75 dumps occur in the control program and as a result, you can not run the debugger to the dumping location or use register by system error for these dumps.



- The TPF Malloc view can be used to locate corruption of malloc blocks.
- If the corruption column is shown in the Malloc view, the corruption detection will be performed. Malloc entries that are corrupted will appear in the changed pane at all times, as shown on the next slide.
- One thing to note, using corruption detection in the TPF Malloc view may impact debugger performance.



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3 3 • Debug Console 🛱 E nitors	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr SW005R @_DETAC #0 AL ecbpt: 0xF30000 <tex> 0 Address 000000011DA5C00</tex>	ASC Remote 3 - New Rend 0 - 3 D90003DC	Console C Data erings 4 - 7 00000000	8 - B 000002D8	C - F 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QI 90 QI 90 QI 90 QI 90 QI 80 QI 90 QI 90 QI 80 QI 90 QI	BO QDBO BO QDBO BO QDBO BO QDBO BO QDBO BO00000 <ebcd 0011DA5C0</ebcd 	0 – 3 0 R _Γ ^L ü			C - F	: : : : : : : : : : : : : : : : : : :
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edpt:0xF300000 <tex> 2 Address 0000000011DA5C00 000000011DA5C00</tex>	ASC Remote 3 P New Rend 0 - 3 D90003DC 00000000	Console C Data erings 4 - 7 00000000 0000000	8 - B 000002D8 00000000	C - F 00000000 11DA5C1C	11DA1200 11DA1400 11DA1500 11DA1600	90 QI 90 QI 90 QI 90 QI 90 QI 90 QI Address 000000 000000	B0 QDB0 B0	0 – 3 0 Rr ^L ü 0 rrrr	4 - 7	8 – B rrnQ rrrr	C - F	:
3 3 Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC0 AL edptr:0xF300000 2 Address 000000011DA5C10 000000011DA5C20	ASC Remote ASC Remote Comparison ASC Remote Comparison Comparison ASC Remote Comparison Compar	Console C Data erings 4 - 7 00000000 0000000 11DA500	8 - B 000002D8 0000000 11DA5E20	C - F 00000000 11DA5C1C 11DA5C2C	11DA1200 11DA1400 11DA1500 11DA1600	90 QL 90 QL 90 QL 90 QL 90 QL 90 QL Address 000000 000000 000000	B0 QDB0 B0	0 - 3 0 R _Γ ^L ü 0 ΓΓΓΓ 0 ◀ ¹ *	4 - 7	8 - 8 rmQ rrrr ∮≛;0	C - F [[]] []] []] []] []] []] []] [:)[∰⊈;)] = <u>0</u> - ⊽
3 3 Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edpt:0xF300000 <tex> 2 Address 0000000011DA5C00 000000011DA5C00</tex>	ASC Remote 3 P New Rend 0 - 3 D90003DC 00000000	Console C Data erings 4 - 7 00000000 0000000	8 - B 000002D8 00000000	C - F 00000000 11DA5C1C	11DA1200 11DA1400 11DA1500 11DA1600	90 QL 90 QL 90 QL 90 QL 90 QL 90 QL Address 000000 000000 000000	B0 QDB0 B0	0 - 3 0 Rr ^L ü 0 rrrr 0 4 ¹ *	4 - 7 rrrr 4 * Ø 4 * ; 0	8 – B rrnQ rrrr	C - F	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr swoosR DETAC = address 000000011DA5C00 000000011DA5C00 000000011DA5C30	ASC Remote ASC RE	Console C Date erings 4 - 7 00000000 0000000 11DA5C0 11DA5C0 00000000 11DA5200 11DA5200	8 - B 000002D8 0000000 11DA5E20 11DA5EB0	C - F 00000000 11DA5C1C 11DA5C2C 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QL 90 QL 90 QL 90 QL 90 QL edbptr: 0xF3 Address 000000 000000 000000 000000	B0 QDB0 B0	0 - 3 0 Rr ^L ü 0 rrrr 0 4 ² * 0 4 ² * 0 rrrr	4 - 7	8 - 8 rrn Q rrrr 4ª;0 4ª;^	C - F rrrr 4 ¹ * 4 ¹ *0 rrrr *000	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr swoosR @_pETAC #0 AL edptr:0xf30000 <ftex> 2 Address 0000000011DA5C00 0000000011DA5C20 0000000011DA5C20 0000000011DA5C20 0000000011DA5C30 0000000011DA5C50</ftex>	ASC Remote ASC Remote ASC New Rend 0 - 3 D90003DC 0000000 11DA5C1C 11DA5C1C 11DA5C2C 0000000 0F3BA000 C1F8BC85	Console C Data erings 4 - 7 00000000 0000000 11DA5C0 11DA5C0 00000000 11DA5C0 11DA	8 - B 000002D8 0000000 11DA5E20 11DA5EB0 0F382A20 FF00FF00 C2E2E240	C - F 00000000 11DA5C1C 11DA5C2C 00000000 0F382AA0 0300FC16 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QT 90 QT 90 QT 90 QT 90 QT Address 000000 000000 000000 000000 000000 0000	BD QDB0 SO011DASC3 SO011DASC4 SO011DASC5 SO011DASC4	0 - 3 0 R _Γ ^L ü 0 ΓΓΓΓ 0 4 ² * 0 4 ² * 0 ΓΓΓΓ 0 X ⁰ μ _Γ 0 A8 ⁻ e	4 - 7 rrrr 4 ² *Ø 4 ² ;0 rrrr 4 ² ° r IFSX	8 - B rrn Q rrrrr 4 ¹ ;0 4 ¹ ; ^	C - F rrrr 4 ² * 4 ² *0 rrrr *000 rrrr rrrr rrrr	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edptr:OxF300000 dtws> 2 Address 0000000011DASC00 0000000011DASC30 0000000011DASC30 0000000011DASC40 0000000011DASC40 0000000011DASC60 0000000011DASC60	ASC Remote ASC Remote ASC Remote Person Remote Person Remote ASC Remote Person Remote ASC Remote Person Remote Pers	Console □ Data erings 4 - 7 00000000 11DA5C00 11DA5C00 11DA5C00 11DA5C20 00000000 CT56227 0000000	8 - B 000002D8 00000000 11DA5E20 11DA5E20 0F382A20 FF00FF00 FF00FF00 C2E2E240 00000000	C - F 00000000 11DA5C1C 11DA5C2C 0000000 0F382AA0 0300FC16 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QT 90 QT 90 QT 90 QT 90 QT Address 000000 000000 000000 000000 000000 0000	B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 CD11DA5C0 0011DA5C1 0011DA5C2 0011DA5C5 0011DA5C5 0011DA5C5 0011DA5C5 0011DA5C5	0 - 3 0 R _Γ ^L ü 0 rrrr 0 4 ² * 0 4 ² *0 0 rrrr 0 ×0µr 0 AS ² e 0 1020	4 - 7 rrrr 4 ¹ *Ø 4 ² ;0 rrrr 4 ² ;7 IFSX rrrr	8 - B rrnQ rrrr 4 ² ;0 4 ² ;^ X00 ŸrŸr BSS rrrr	C - F rrrr 4 ² * 4 ¹ *0 rrrr *00 L _T 0 rrrr rrrr	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edpt:0xF300000 2 Address 0000000011DA5C00 0000000011DA5C00 0000000011DA5C20 0000000011DA5C40 0000000011DA5C50 0000000011DA5C50 0000000011DA5C70 0000000011DA5C70	ASC Remote ASC RE	Console Data erings 4 - 7 00000000 11DA5C0 11DA5C0 0000000 11743800 CC5E2E7 0000000 00000000	8 - B 000002D8 0000000 11DA5E20 11DA5E20 0F382A20 FF00FF00 C2E2E240 0000000 0000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 07382AA0 0300FC16 00000000 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QT 90 QT 90 QT 90 QT 90 QT Address 000000 000000 000000 000000 000000 0000	B0 QDB0 B0 D011DASC4 B011DASC4 B011DASC5 B011DASC5 B011DASC5	0 - 3 0 Rr ^L ü 0 rrrr 0 4 [±] * 0 4 [±] * 0 rrrr 0 X0µr 0 A8 [−] e 0 1000 0 rrrr	4 - 7 rrrr 4 ² *Ø 4 ² ;0 rrrr 4 ² r IFSX rrrr rrrr	8 - B rrnQ rrrr 4 ² ;0 4 ² ;^ X00 YrYr BSS rrrr rrrr	C - F rrrr 4 ² * rrrr * 0 rrrr * 0 rrrr rrrr rrrr rrrr	a (∰ 4 5) =0 - ~
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edptr:OxF300000 dtws> 2 Address 0000000011DASC00 0000000011DASC30 0000000011DASC30 0000000011DASC40 0000000011DASC40 0000000011DASC60 0000000011DASC60	ASC Remote ASC Remote ASC Remote Person Remote Person Remote ASC Remote Person Remote ASC Remote Person Remote Pers	Console □ Data erings 4 - 7 00000000 11DA5C00 11DA5C00 11DA5C00 11DA5C20 00000000 CT56227 0000000	8 - B 000002D8 0000000 11DA5E20 11DA5E20 0F382A20 FF00FF00 C2E2E240 0000000 0000000	C - F 00000000 11DA5C1C 11DA5C2C 0000000 0F382AA0 0300FC16 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QT 90 QT 90 QT 90 QT 90 QT Address 000000 000000 000000 000000 000000 0000	B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B0 CD11DA5C0 0011DA5C1 0011DA5C2 0011DA5C5 0011DA5C5 0011DA5C5 0011DA5C5 0011DA5C5	0 - 3 0 Rr ^L ü 0 rrrr 0 4 [±] * 0 4 [±] * 0 rrrr 0 X0µr 0 A8 [−] e 0 1000 0 rrrr	4 - 7 rrrr 4 ¹ *Ø 4 ² ;0 rrrr 4 ² ;7 IFSX rrrr	8 - B rrnQ rrrr 4 ² ;0 4 ² ;^ X00 ŸrŸr BSS rrrr	C - F rrrr 4 ² * 4 ¹ *0 rrrr *00 L _T 0 rrrr rrrr	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC =_0 AL cdpt:0xf300000 <ftex> 2 Address 0000000011DA5C00 0000000011DA5C00 0000000011DA5C30 0000000011DA5C40 0000000011DA5C50 0000000011DA5C70 0000000011DA5C70 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80</ftex>	ASC Remote ASC Remote ASC Remote ASC Remote ASC Remote D= New Rend 0 - 3 D90003DC 00000000 11DASC1C 11DASC2C 00000000 053BA000 C1F8BC85 181AB6FF 00000000 00000000 00000000 0000000	Console Data erings 4 - 7 00000000 11DA5C0 11DA5C0 0000000 11743800 CC5E2E7 0000000 00000000 00000000 00000000	8 - B 000002D8 00000000 11DA5E20 11DA5E20 0F382A20 FF00FF00 02222240 0000000 0000000 00000000 00000000	C - F 00000000 11DASC1C 11DASC2C 0000000 07382AA0 0000000 0000000 0000000 0000000 000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QT 90 QT 90 QT 90 QT Address 000000 000000 000000 000000 000000 0000	BD QDB0 BD QD1DA5CC BD DD1DA5CC BD DD1	0 - 3 0 Rr ^L ü 0 rrrr 0 4 ² * 0 4 ² * 0 rrrr 0 XUµr 0 AS ⁻ e 0 rrrr 0 rrrr 0 rrrr 0 rrrr	4 - 7 rrrr 4 * ∞ 4 * j0 rrrr 1FSX rrrr rrrr rrrr	8 - B rrrQ rrrr 4 ² ;0 4 ² ;^ X00 YrYr BSS rrrr rrrr rrrr	C - F rrrr 4:* 4:* rrrr X012 rrrr rrrr rrrr rrrr rrrr	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr swoosR DETAC A edptr : 0xf30000 <ftx> 2 Address 0000000011DASC00 0000000011DASC20 0000000011DASC30 0000000011DASC40 0000000011DASC50 0000000011DASC50 0000000011DASC50 0000000011DASC50 0000000011DASC50 0000000011DASC50</ftx>	ASC Remote ASC RE	Console C Deta erings 4 - 7 00000000 11DA5C0 11DA5C0 11DA5C0 11DA5C0 11DA5C0 0000000 00000000 00000000 00000000	B B 000002D8 0000000 11DA5E20 11DA5E20 152220 0000000 00000000 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 07382AA0 0300FC16 00000000 00000000 00000000 00000000 0000	11DA1200 11DA1400 11DA1500 11DA1600	10 00 90 QI Address 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	BD QDB0 BD QD00 BD QD00 BD QD00 BD QD00 BD QD00 BD	0 - 3 0 R ₁ Lu 0 rrrr 0 4 [±] * 0 4 [±] * 0 4 [±] * 0 0 rrrr 0 XDµr 0 XDµr 0 XDµr 0 100 0 rrrr 0 100 0 rrrr 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	4 - 7 rrrr 4 ² *Ø 4 ² ;0 rrrr 4 ² " IFSX rrrr rrrr rrrr rrrr rrrr	8 - B rmQ rrrr 4::0 4::0 YrYr BSS rrrr rrrr rrrr rrrr	C - F rrrr 42* 4*0 rrrr X000 rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrr	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr swoosR @_DETAC #0 AL address 000000011DA5C00 000000011DA5C00 000000011DA5C00 000000011DA5C30 000000011DA5C40 000000011DA5C40 000000011DA5C60 000000011DA5C90 0000000011DA5C90 0000000011DA5C80 0000000011DA5C80	ASC Remote	Console C Data erings 4 - 7 00000000 11DA5C20 0000000 11DA5C20 0000000 0000000 0000000 0000000 000000	B B 000002D8 0000000 01DA5E20 11DA5E20 11DA5E20 07382A20 FF00FF00 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 0300FC16 00000000 00000000 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QL 90 QL 90 QL 90 QL 90 QL 90 QL 90 QL 90 QL 000000 000000 000000 000000 000000 0000	B0 QDB0 B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B0001DASC3 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5</eecd<></eecd<></eecd<></eecd<></eecd<>	0 - 0 0 R_1^Lit 0 0 rrrr 0 4** 0 - 10** 0 0 4** 0 10** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1** 0 1**	4 - 7 rrrr 4 × Ø 4 × Ø 4 × ĵ rrrr iFSX rrrr rrrr rrrr rrrr rrrr	8 - B rrn Q rrrr 4::0 4::^ WOO YrYr BSS rrrr rrrr rrrr rrrr rrrr	C - F rrrr 4 * 4 x0 rrrr x0 x0 x0 rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr x0 x0 x0 x0 x0 x0 x0 x0 x0 x0	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SRDETAC = AL edpt:0xf300000 dtex> 2 Address 0000000011DA5C00 0000000011DA5C00 0000000011DA5C00 0000000011DA5C00 0000000011DA5C00 0000000011DA5C70 0000000011DA5C70 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80 0000000011DA5C80	ASC Remote ASC R	Console Data erings 4 - 7 00000000 11DA5C10 11DA5C20 0000000 11CA3800 CC5E2E7 0000000 00000000 00000000 00000000 0000	8 - B 000002D8 00000000 11DA5E20 11DA5E80 0F382A20 0F382A20 0F382A20 0F382A20 0F382A20 0F382A20 0F382A20 0F382A20 0000000 00000000 00000000 00000000	C - F 00000000 11DASC1C 11DASC2C 0000000 07382AA0 0300FC16 0000000 00000000 00000000 00000000 0000	11DA1200 11DA1400 11DA1500 11DA1600	10 00 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 00000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	B0 QDB0 B0 QDB0 B0 QDB0 B0 QDB0 B00000 <ebcd< td=""> 0011DA5C0 0011DA5C2 0011DA5C2 0011DA5C3 0011DA5C4 0011DA5C4 0011DA5C5 0011DA5C6 0011DA5C6 0011DA5C6 0011DA5C7 0011DA5C6 0011DA5C7 0011DA5C6 0011DA5C8 0011DA5C6 0011DA5C9 0011DA5C6 0011DA5C6 0011DA5C6 0011DA5C7 0011DA5C6 0011DA5C8 0011DA5C8 0011DA5C9 001DA5C8 0011DA5C8 001DA5C8</ebcd<>	C O S O F O O T T O T T O T T O T T O T T O T T O T T O T T O T T O T T O T T O T T O T <tht< th=""> <tht< th=""> <tht< th=""> <tht< th=""></tht<></tht<></tht<></tht<>	4 - 7 rrrr 4 **Ø 4 *;0 rrrr 4 *;0 rrrr 1 FSX rrrr rrrr rrrr rrrr rrrr rrrr	8 - B rrn Q rrrr 4 ² ;0 4 ² ;^ WOD ÝrÝr BSS rrrr rrrr rrrr rrrr rrrr rrrr	۲ - 2 ۲۲۲۲ ۵۰۵ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr swoosR @_DETAC #0 AL address 000000011DA5C00 000000011DA5C00 000000011DA5C00 000000011DA5C30 000000011DA5C40 000000011DA5C40 000000011DA5C60 000000011DA5C90 0000000011DA5C90 0000000011DA5C80 0000000011DA5C80	ASC Remote ASC R	Console C Data erings 4 - 7 00000000 11DA5C20 0000000 11DA5C20 0000000 0000000 0000000 0000000 000000	B B 000002D8 0000000 01DA5E20 11DA5E20 11DA5E20 07382A20 FF00FF00 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 0300FC16 00000000 00000000 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	10 00 90 QI Address 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	B0 QDB0 B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B00000 <eecd< td=""> B0001DASC3 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5 B001DASC5</eecd<></eecd<></eecd<></eecd<></eecd<>	0 - 0 0 R_L ¹ ¹ ¹ 0 1 - - 0 - - 0 41* - 0 41* - 0 41* - 0 41* - 0 41* - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 - - 0 -	4 - 7 rrrr 4 ² ר 4 ² ° rrrr 4 ² ° rrrr 1FSX rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrrr rrr rrr rrr 1FSX rrrr rrrr rrr rrr rrr rrr rrr	8 - B rrnQ rrrr 4 × ;0 4 × ; ^ *00 YrYr BSS rrrrr rrrr rrrr rrrr rrrr rrrr rrrr	۲ - - - - ۲ -	a) (∰ ⊈) a) - ⊽ -
3 3 Debug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr SW00SR	ASC Remote	Console Data erings 4 - 7 00000000 11DA5C20 0000000 11DA5C20 0000000 11743800 C726227 0000000 0000000 0000000 0000000 000000	B B 000002D8 0000000 11DA5E20 11DA5E30 0F382A20 0000000 00000000 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 07382AA0 0300FC16 00000000 00000000 00000000 00000000 0000	11DA1200 11DA1400 11DA1500 11DA1600	90 QI Address 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	B0 QDB0 B0 QD1 B0 QD	8 - 0 171 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 111 111 0 1111 111 0 111	4 - 7 rrrr 4 **Ø 4 *;0 rrrr 4 *;0 rrrr 1 FSX rrrr rrrr rrrr rrrr rrrr rrrr	8 - B rrn Q rrrr 4 ² ;0 4 ² ;^ WOD ÝrÝr BSS rrrr rrrr rrrr rrrr rrrr rrrr	۲ - 2 ۲۲۲۲ ۵۰۵ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲ ۲۲۲۲	
3 3 Vebug Console	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr swoosR DETAC A A ecbpt: 0xf30000 <tex> 2 Address 0000000011DASC00 0000000011DASC00 0000000011DASC30 0000000011DASC40 00000000000000000000000000000000000</tex>	ASC Remote	Console Data rings 4 - 7 00000000 11DA5C00 11DA5C0 11DA5C0 11DA5C0 000000 0000000 0000000 0000000 0000	B B 000002D8 0000000 11DA5E20 11DA5E30 0F382A20 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 05382AA0 00000000 00000000 00000000 0000000	11DA1200 11DA1400 11DA1500 11DA1600	10 00 90 QI Address 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	BD QDB0 BD QD1 BD	0 - 3 0 - 7	4 - 7 rrrr 4 - 7 4 - 7 4 - 7 4 - 7 4 - 7 1 - 7 7 - 7 1	8 - B rrnº rrrr *:D *:D *:D *:D *:D *:D *:D *:D	۲ ۲ ۲ - ۲ - ۲ ۲ - ۲ - ۲ ۲ - ۲ ۲ ۲ - ۲ ۲ - ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲	
3 3 Cobug Console	873 874 ECB I DECB	malloc_ptr = mall memset(malloc_ptr swoosR	ASC Remote	Console Data erings 4 4 7 0000000 11DA5C/0 11DA5C/0 11DA5C/0 11DA5C/0 0000000 0000000 0000000 0000000 0000000 0000000 0000000 0000000 0000000 0000000 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	B B 000002D8 0000000 11DA5E20 11DA5E20 11DA5E20 FF00FF00 C2E2E240 0000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 0000000 0300FC16 0000000 00000000 00000000 00000000	11DA1200 11DA1400 11DA1500 11DA1600	90 QI Address 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000	B0 QDB0 B001DA5C5 D001DA5C5 D011DA5C6 D001DA5C5 D011DA5C6 D001DA5C5 D011DA5C5 D001DA5C5 D011DA5C5 D011DA5C5 D011DA5C5 <t< td=""><td>0 - 3 0 Rr^La 0 frrr 0 4²*0 0 4²*0 0 3 4²*0 0 4²*0 0 3 4²*0 0 4²*0 0 100 0 1</td><td>4 - 7 rrrr rrrr 4 × 0 4 × 0 rrrr</td><td>8 - B rrnº rrrr 4:10 4:20 YrYr BSS rrrr</td><td>Yes Yes Yes Yes Yes Yes</td><td></td></t<>	0 - 3 0 Rr ^L a 0 frrr 0 4 ² *0 0 4 ² *0 0 3 4 ² *0 0 4 ² *0 0 3 4 ² *0 0 4 ² *0 0 100 0 1	4 - 7 rrrr rrrr 4 × 0 4 × 0 rrrr	8 - B rrnº rrrr 4:10 4:20 YrYr BSS rrrr	Yes Yes Yes	
3 3 Cobug Console	873 874 ECB I DECB	malloc_ptr = mall memset (malloc_ptr swoosR DETAC A A ecbpt: 0xf30000 <tex> 2 Address 0000000011DASC00 0000000011DASC00 0000000011DASC30 0000000011DASC40 00000000000000000000000000000000000</tex>	ASC Remote	Console Data rings 4 - 7 00000000 11DA5C00 11DA5C0 11DA5C0 11DA5C0 000000 0000000 0000000 0000000 0000	B B 000002D8 0000000 11DA5E20 11DA5E80 0F382A20 0 0000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 0000000 00000000 00000000 00000000 00000000 00000000 00000000	C - F 00000000 11DA5C1C 11DA5C2C 00000000 05382AA0 00000000 00000000 00000000 0000000	11DA1200 11DA1400 11DA1500 11DA1600	10 00 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 02 90 00000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 000000 0000000 000000 0000000 000000	BD QDB0 BD QD1 BD	0 - 3 0 Rr ^L u 0 4 ¹ * 0 4 ¹ * 0 4 ¹ * 0 8 ¹ * 0 8 ¹ × 0 700 0	4 - 7 rrrr rrrr 4 * ∞0 rrrr 4 * ∞0 rrrr 1 * ∞ 1 *	8 - B rrnº rrrr *:D *:D *:D *:D *:D *:D *:D *:D	۲ ۲ ۲ - ۲ - ۲ ۲ - ۲ - ۲ ۲ - ۲ ۲ ۲ - ۲ ۲ - ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲ ۲	

IBM z/Transaction Processing Facility Enterprise Edition 1.1

AIM Enterprise Platform Software TPF Users Group – Spring 2013

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- While the TPF malloc view is a great way to learn about your malloc blocks and effectively shows you what corruption has occurred, it cannot indicate when that corruption occurred.
- The perform heap check on stop feature tells the debugger to detect any heap corruption whenever the execution of the application is stopped.
- When heap corruption is detected, a pop up window is displayed indicating that corruption has been detected.
- However, the user must step or run the application such that the application is periodically stopping.



• To turn on the **perform heap check on stop** feature, right click in the stack frame and choose **perform heap check on stop**.



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AIM Enterprise Platform Software TPF Users Group – Spring 2013

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10000		-		100	-

 When corruption is detected, as in this case where a step into each line occurred, a pop up appears like this:



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- CTL-75 dumps occur in the control program and as a result, you cannot run the debugger to the dumping location or use register by system error for these dumps.
- However, if your heap corruption is writing past the fence (a typical case) you can use the heapcheck system feature in conjunction with the debugger to quickly locate the problem code.
- Heapcheck mode causes every malloc to use at least one 4 K frame, the malloc area with the fence is located at the end of the 4 K frame, and the next 4 K frame is invalidated.
- When the application writes past the fence in corrupting the malloc buffer, the application will start to write over the invalid frame and an OPR-4 will occur. The application must write beyond the fence because overwriting the fence is not enough to cause the OPR-4.
- As a result, you can debug the application, clear the breakpoints, and run to the OPR-4. Or you can register the OPR-4 in the system error registration.



- CTL-10 dumps occur in the control program and as a result the debugger cannot stop the application at the location of the error.
- The debugger attempts to do infinite loop detection.
- However, the application must periodically stop in order for the debugger to perform its detection. This is because the debugger attempts to allow the application to run as fast as possible to provide the optimal debugging experience. As a result, the infinite loop detection cannot occur without you setting breakpoints or stepping of some sort.
- The debugger attempts to make you aware of dumps that occur when the application dumps.

-	-		-		
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 Use ZDMAP to determine as low of an address as possible and as high of an address as possible. Doing a ZDMAP a-XXXX where XXXX is the address in R15 may be a good way to narrow in on a module to create breakpoints around. Notice that the value in R15 in the figure above falls into the range of QDB0 in the figure below.



 This gives us an address range of: 409A1AC50 to 409A1AC50 + AAE4 (409A25734). Now start the debugger on your application and use these two addresses to create address breakpoints.

ebugging infinite loops	
ECB Summary 🖞 Modules 🖉 Variables 🤗 Breakpoints 🔀 🎼 TPF Malloc	🐵 Add an Address Breakpoint 📃 🗖 🔀
Co to File Add Breakpoint Edit Breakpoint Enable Disable Remove Remove All Select All Ctrl +A Copy Ctrl +A Copy Ctrl +V Support Breakpoints	Required information Sets a breakpoint to stop execution at a specific address Address or expression: 0x409A25734
	⑦ < Back Next > Finish Cancel



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- Infinite loop detection is controlled by a time out that you can set. The default setting is 30 seconds. You can use the TPFTimeout debug console command to shorten the time you will need to wait.
- Next push the resume button and wait the specified number of seconds. A pop up will appear indicating that a possible CTL-10 has been found as shown on the next slide.
- The debugger will show you the current stopping location for you to investigate. You can continue to debug as normal or press the resume button to run to the next possible infinite loop detection point.

-		-	-		
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Debugging memory leaks

- The z/TPF debugger provides a few features to help identify memory leaks in the application. However they do require that you do some investigating because the debugger cannot determine when a malloc block is no longer used.
- The ECBHEAP debug console command allows you to gather information regarding the use of heap by the application.
- The ECBHEAP STATS debug console command shows how much memory is in use and what types of memory is in use. In the slide that follows, notice that no 64 bit memory is in use.

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Debugging memory leaks



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Debugging memory leaks

 The ECBHEAP CNTS [sortcnt] debug console command provides the counts of all malloc entries based on size.
 It can sort based on size or based on the number of malloc entries of a given size.

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🕆 📳 Remote System	• •									
		<u> </u>					8		8	
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ecbheap cnts 🚽										
DBUG8095I ECB P					nts"					
DBUG8166I STARI	C OF EC	B HEAP	COUNTS I	DISPLAY.						
	-									
HEAP COUNT TABL			all memor	v types						
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0x0000002		1								
0x0000003		3								
0x0000004		1								
0x0000007		1							-	
0x000000		1								
0x0000013		1								
0x0000017		1								
0x000001E		1							1	
0x0000025		3						/		
0x0000021	8	1								
0x000085	58 📕	1								
0x0000200	08 🚽	3								
0x0000403		1			/					
fot = 0x0000B96		21								
DBUG8167I END C	OF ECB	TRACE (COUNTS DI	SPLAT						
chheap cnts so										
DBUG8095I ECB H	73A8000	proces	-	-	nts sort	cnt"				
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Debugging memory leaks

- One way to use this feature is to step over a function, perform some action, and so on and then look at the ECBHEap counts to see what has changed. Make note of what memory sizes are not getting freed. Use the TPF malloc view to choose a given size entry and use the selected block pane to know what code is allocating malloc of that size.
- Another thing to look at is which part of the application is using the largest blocks of memory. Use the malloc view to examine the malloc blocks further (for example sort the malloc view data by size and Look at largest blocks)



Custom communication packages

- Topics
 - Using tpf_flag_for_debug
 - Using CDBX_DebuggerTBTRegistrationTerminalUserExit
 - Using tpf_flag_for_debug and CDBX_DebuggerTBTRegistrationTerminalUserExit together
 - User defined registration: The ultimate solution



Custom communication packages: Intro

- When a user registers by LNIATA, IP address, or LU, TPF marks ECBs as candidates for trace by terminal debugging.
- If those candidate ECBs enter the registered program, function or etc, a debugger session is started.




Custom communication packages: Intro

 If you implemented a custom communication package (TN3270, inter-processor communications, etc), it is possible that the ECBs in your system will not be marked as candidates for trace by terminal debugging.



Using tpf_flag_for_debug

 tpf_flag_for_debug is a system service that allows your custom communication package to mark ECBs as candidates for trace by terminal debugging.





Using CDBX_DebuggerTBTRegistrationTerminalUserExit

 This user exit is in the routine that marks ECBs as candidates for trace by terminal debugging. It allows you to inspect the ECB and provide a custom terminal to the debugger.

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 For example, if you have implemented TN3270 support, this user exit could return an LNIATA for an ECB created by your package such that the debugger user can register for trace by terminal by LNIATA.





Using tpf_flag_for_debug and CDBX_DebuggerTBTRegistrationTerminalUserExit together

 Using these two features together allows your custom communications package to call to mark the ECB as a candidate for debugging and allows you to specify the terminal to use.





- This feature allows you to start the debugger virtually anywhere in your application under the conditions you define.
- For example, the user could register: their ID, a transaction type, a transaction identifier, and etc to debug only the ECB they need to debug.
- See the developerworks article, the redbook or the debugger user's guide for an example implementation.





Define conditions to test

- Modify the file <TPF Toolkit install dir>\Config\TPFSHARE\Debug Registration\customDebugRegTypes.xml to
 - define the names of the conditions (parameters) to be tested
 - define the name of the registration type
 - define the registration type id
- Restart the TPF Toolkit

```
<customRegistration>
    <id>101</id>
    </id>
    </orange>MyRegistration</name>
    <parameter>User Id</parameter>
    <parameter>Message Type</parameter>
    <parameter>EBROUT</parameter>
    <parameter>Value_of_i</parameter>
    </customRegistration>
```

User defined registration: The ultimate solution

 The names of the conditions will be shown to the user with a text box for the user to provide the comparison value.







- Define where to test the conditions: The next thing that you need to do is to modify your application to call the test program with the conditions in your application to be tested against the comparison values registered by the user. C/C++ and Assembler interfaces are provided.
 - The first line of this block of code uses the performance-sensitive macro tpf_UserDefRegTypPerfCheck to see whether a given userdefined registration type is actively registered on the system. Because the user-defined registration code is contained within a block that is encapsulated by the performance-sensitive macro, this code can be left in your production-level code for test points that can be used in the future.
 - Now define an instance of the tpf_UserDefRegTypStruct structure and populate it with the registration type ID, a resolving function (in this example, we'll just use the user exit provided), and the comparison values to be passed as parameters.
 - Lastly, you call tpf_UserDefRegTypHandler.



User defined registration: The ultimate solution

Build and load your application.

```
123
124
      num parms = IPRSE parse ( input ptr,grammar ptr,&parse results,
125
                                 IPRSE ALLOC | IPRSE PRINT , "DBUG");
126
127
      if(tpf UserDefReqTypPerfCheck(101))
128
      - {
129
         struct tpf UserDefReqTypStruct temp = {0};
130
         temp.udrt id = 101;
131
         temp.udrt funcptr = (tpf UserDefRegTypUserExit *)cdbxud user exit;
132
         temp.udrt parm2 = (void*)reqType;
133
         temp.udrt parm4 = (void*)&i;
         tpf UserDefRegTypHandler(&temp);
134
135
      }
136
137
      /* display help manual if parser error
                                                                      */
138
      if (num parms < 1)
```



Define how to test the conditions

- Implement the code that performs the test of the conditions, for example in the user exit code cdbxud.c. It can be defined in assembler, in other code locations and etc.
- The contents of the UDRT_ptr (the state of the executing ECB) are compared to the contents of tbu_entry (the comparison values registered by the user as stored in the debugger registration entries).
- Notice that you can compare the registered variable against the values in the ECB, system or etc.
- The parameters are passed as void pointers so that your code must know how to interpret the comparison values, such as using functions like atoi, sscanf, and etc.
- Set rc to true to tell the debugger to start.



Build and load your code that tests the conditions.

```
82 unsigned int cdbxud user exit(struct tpf UserDefRegTypStruct* UDRT ptr,
 83
                                   struct itbpentry* tbu entry)
 84 {
       unsigned rc = FALSE;
                             /* set default return to false
                                                                  */
 86
       if ((UDRT ptr == NULL) || (tbu entry ==NULL))
         £
 89
         return rc;
         3
       switch(UDRT ptr->udrt id)
         £
 94
         case 101:
 96
              if(0 != strncmp((char*)&ecbptr()->ebw000,
                               (char*)tbu entry->itbp udrt parmValue[0],8))
                 break:
              if(0 != strncmp((char*)UDRT ptr->udrt parm2,
                               (char*)tbu entry->itbp udrt parmValue[1],8))
101
                 break:
102
              unsigned int lniata = 0;
103
              if(1 != sscanf((char*)tbu entry->itbp udrt parmValue[2],"%x",&lniata))
104
                 break:
105
              if(ecbptr()->ebrout != lniata)
106
                 break:
107
              if(*((int *)UDRT ptr->udrt parm4) !=
108
                 atoi((char*)tbu entry->itbp udrt parmValue[3]))
109
                 break:
110
              rc = TRUE;
111
              break:
112
```

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Using user-defined registration

- Register your user-defined debugger registration entry as you would for other registration types and then run your application.
- When the debugger is notified by your condition-testing code (cdbxud.c) that a debugger session should be started, the debugger will stop the application at the next line of code following the code snippet in your application that passed in the state of the application.

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	CVZZ : cvzz.o -O3	-						
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बdb0.cpp छ								
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Line 138			Brows		+'	7+-	8+-	9
Line 138	1+2	+3+		+6	+	7+-	8+-	9
Line 138	1+2	+3+	4+5+	+6	+	7+-	8+-	9
Line 138	1+2 127 if	+3+ (tpf_UserDefReg struct tpf_Use	rDefRegTypStruct t	+6		7+-	8+-	9
Line 138	1+2 127 if 128 { 129 130	<pre>-+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id =</pre>	TypPerfCheck(101)) TypPerfCheck(101)) DefRegTypStruct t 101;	+6) temp = {	0};			
Line 138	1+2 127 if 128 { 129 130 131	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func</pre>	TypPerfCheck(101)) TopfRegTypStruct t 101; ptr = (tpf_UserDef	+6) temp = { fRegTypU	0};			
Line 138	1+2 127 if 128 { 129 130 131 132	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm</pre>	TypPerfCheck(101)) TopefRegTypStruct t 101; Toptr = (tpf_UserDef 2 = (void*)reqType	+6) temp = { fRegTypU	0};			
Line 138	1+2 127 if 128 { 129 130 131 132 133	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm</pre>	<pre>rDefRegTypStruct t t 101; ptr = (tpf_UserDef 2 = (void*)reqType 4 = (void*)&i</pre>	+6) temp = { fRegTypU e;	0};			
Line 138	1+2 127 if 128 { 129 130 131 132 133 134	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm</pre>	TypPerfCheck(101)) TopefRegTypStruct t 101; Toptr = (tpf_UserDef 2 = (void*)reqType	+6) temp = { fRegTypU e;	0};			
Line 138	1+2 127 if 128 { 129 130 131 132 133 134 135 }	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm</pre>	<pre>rDefRegTypStruct t t 101; ptr = (tpf_UserDef 2 = (void*)reqType 4 = (void*)&i</pre>	+6) temp = { fRegTypU e;	0};			
Line 138	1+2 127 if 128 { 129 130 131 132 133 134 135 } 136	<pre>-+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm tpf_UserDefReg</pre>	<pre>r-45+ fTypPerfCheck(101)) erDefRegTypStruct t = 101; eptr = (tpf_UserDef h2 = (void*)reqType h4 = (void*)&i fTypHandler(&temp);</pre>	+6) temp = { fRegTypU e; ;	0};		ud_user_ex	
Line 138	1+2 127 if 128 { 129 130 131 132 133 134 135 } 136 137 /*	<pre>+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm tpf_UserDefReg display help m</pre>	<pre>45+ TypPerfCheck(101)) erDefRegTypStruct t = 101; eptr = (tpf_UserDef 12 = (void*)reqType 14 = (void*)&i TypHandler(&temp);</pre>	+6) temp = { fRegTypU e; ;	0};			
Line 138	1+2 127 if 128 { 129 130 131 132 133 134 135 } 136 137 /*	<pre>-+3+ (tpf_UserDefReg struct tpf_Use temp.udrt_id = temp.udrt_func temp.udrt_parm temp.udrt_parm tpf_UserDefReg</pre>	<pre>45+ TypPerfCheck(101)) erDefRegTypStruct t = 101; eptr = (tpf_UserDef 12 = (void*)reqType 14 = (void*)&i TypHandler(&temp);</pre>	+6) temp = { fRegTypU e; ;	0};		ud_user_ex	

User defined registration: The ultimate solution

• Example Uses

- Thousands of ECBs might be started per second in a given program (CRETC, network traffic, etc), and you might need to debug only one specific ECB (for example, the one ECB out of a thousand with 0 on data level 1).
- Perhaps your system has a proprietary communication package that requires the user to register multiple pieces of information.
- Maybe you need to debug a particular location in code where a set of conditions occur, such as a single entry point transaction application where a query is performed on a particular account number.



- APAR PJ36059
- PUT6
- TPF Toolkit Level v3.4.3



Determining code path

- Topics
 - Using trace log and the code coverage tool together
 - Debugger: stop on all functions and high level breakpoints
 - Debugger: ECB Summary view, animated step into, execute shortcuts
 - Debugger: optimized debugging vs non-optimized debugging



- A trace log is an integrated macro and function trace that provides you parameter values, return values, macro call details and the path through the application code at a high level.
- The code coverage tool allows you to see what source lines, macros and instructions your application has executed. The code coverage tool gives you lower level detail allowing you to infer code path.
- Using trace log and the code coverage tool together can help you better understand the code path of your application.



Using trace log and the code coverage tool together

- 1. Register and start code coverage for your application.
- 2. Register the debugger for the entry point of your application.
- 3. If necessary, change the number of trace log sessions allowed on your system with ZASER TRLOG-X
- 4. Start your application, the debugger starts.
- 5. Turn on trace log



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Using trace log and the code coverage tool together

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- 6. Click the resume button to run your application to completion.
- Double click the report file created in the Files subsystem (GUI FTP interface to the file system on TPF)
- The report file opens in the editor window showing you the trace log contents. The default view shows you functions and macros called in an indented fashion to show the call stack. See next slide.





Using trace log and the code coverage tool together

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unction call or Macro				Object Name	_				
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49 return from strlen			QXHP			1	D9A2C8	Mar 12, 2012 08:28:05.751726	
memcmp		IBM_DEFT	QXHP			1	D9A204	Mar 12, 2012 08:28:05.751732	
4 ⁰ return from memcmp		_	QXHP			1	D9A25E	Mar 12, 2012 08:28:05.751740	
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 ✓² return from strlen 		_	QXHP			1	D9A2C8	Mar 12, 2012 08:28:05.751758	
IPRSE_parse		_	CTBX	crfb		1	24	Mar 12, 2012 08:28:05.751769	
• setGrammarOptions		_	CTBX	crfb		1	4FC	Mar 12, 2012 08:28:05.751770	
<pre>ctype_b_loc</pre>			CISO	ctype-info			24	Mar 12, 2012 08:28:05.751817	
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<pre> return fromctype_b_loc</pre>		_	CISO	ctype-info		1		Mar 12, 2012 08:28:05.751828	
		-	CISO	ctype-info		1		Mar 12, 2012 08:28:05.751829	
<pre># return fromctype_b_loc</pre>		IBM_DEFT	CISO	ctype-info		-	72	Mar 12, 2012 08:28:05.751830	
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<pre># return fromctype_b_loc</pre>		-	CISO	ctype-info		1		Mar 12, 2012 08:28:05.751833	
octype_b_loc		-	CISO	ctype-info		1	24	Mar 12, 2012 08:28:05.751834	
return fromctype_b_loc		IBM_DEFT	CISO	ctype-info	PSW	-		Mar 12, 2012 08:28:05.751835	
return from IPRSE_getToken		_	CTBX	crfc		1	CDC	Mar 12, 2012 08:28:05.751836	
e newString		_	CTBX	crfd		1	12B0	Mar 12, 2012 08:28:05.751836	
🖃 🔍 calloc		IBM_DEFT	CTIS	ccaloc	PSW	1	52	Mar 12, 2012 08:28:05.751839	
	Value								

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- 9. Examine the function/macro parameters and return values, search or filter the results in an LPEX editor view, press the analyze button to see statistics about what macros were used, memory allocation, segments entered and etc, and generally understand the overall path of execution of your application.
- 10. Stop and save the code coverage session and run source analysis.
- Use the code coverage view to navigate to modules, objects, functions and source files of interest. Examine the execution statistics.
- 12. Examine the source lines or instructions executed. See next slide.



C941B1ECDE46C351.report C qxhp.cpp 🔀	
}	<u></u>
<pre>free(hotel_parms);</pre>	
break;	
case FLIGHT_DB:	
<pre>struct qxhf_parms *flight_parms; // Create Parameter Str</pre>	ruct
<pre>flight_parms=(struct qxhf_parms*) malloc(4069);</pre>	
<pre>if (flight_parms == NULL) { printf("malloc failure\n"); } if (num names) 0)</pre>	
if (num_parms > 0)	
init flight parms(flight parms);	
do	
{	
<pre>pr_ptr = pr_ptr->IPRSE_next; // point to the first "</pre>	variable/value" paramete
process flight parms(flight parms, pr ptr->IPRSE parameter,	
<pre>pr_ptr->IPRSE_value);</pre>	1
<pre>} while (num_parms);</pre>	
}	
switch (cur_func) {	
case INIT_FUNC:	
<pre>qxhf_init(sub_func, DFED_PROMPT, "QXFI");</pre>	//D16691f_2
break;	
case BUILD_FUNC:	
<pre>qxhf_build(sub_func, DFED_PROMPT); </pre>	
break;	//D10420
<pre>case IBUILD_FUNC:</pre>	//D18438 //D18438
dxni_ibuiid(); break;	//D18438
case ADD FUNC:	77 510100
switch (cur sub func) { // Sub-Function Calls:	
Source Properties	, <u>•</u>



Use this methodology to...

- learn about the code path of an application you don't know.
- learn why your code fix did not work properly. For example, was your new code even executed?
- determine the best place to start a debugger session.
- understand deviations between two slightly different situations.
 For example, run both scenarios separately as described above, use the code coverage comparison tool to identify where the paths deviate, and then use trace log to see parameters and return values to understand why the deviation occurred.

Debugger: stop on all functions and high level breakpoints

- You can use debugger features such as stop on all functions and high level breakpoints to understand the execution path of your code.
 - 1. Register the debugger for the entry point of your application
 - 2. Set up stop on all functions and/or other high level breakpoints.
 - 3. Use the resume button to run from location to location to understand the path of execution of your application.



Debugger: stop on all functions, high level breakpoints, etc

 Stop on all function entries behaves as if you set a breakpoint at every C/C++ function entry point (including TMSPC and PRLGC) and BAL external entry points.

TPF Debug - \RemoteSystemsTempFiles\LINUXTPF.POK.IBM.COM\home\jwisnie\maint\sabre03292010\debug\qdb0.	cpp - TPF Toolkit Enterprise	
File Edit Navigate Search Project Run Window Help		
╡▐Ѯ╺╶╠╴Ѐ┋┫╴┇┫╴╝╸╴╴╴╴┊┩╴┊╝╴ѷ╸Ҿ╺╶╤╶		
😰 🔚 Remote System Explorer 🛛 🕸 TPF Debug 📲 TPF Toolkit		
🏂 Debug 🛛 🦉 🚱 🖉 🖓 🗸 🖓 🖉 🖓 🗸 🍸 🦓 🗸 🌄 🖓 🖉 🖉 🖓 🗸 🍸 🖓	🗣 Breakpoints 🖾 🗱 Variables 📳 TPF Ma	
 <u>P</u> 9.57.13.89.qdb0 [Incoming Remote Debug Session] <u>→</u> <u>P</u> Platform: ZTPF Connection: 9.57.13.89:1084 	ال 🍬 🗟 🎇 🗶	! ⓑ 🖻 🖻 🕏 ▽
Thread: TPF Thread 0F384000 (Stopped)		
Execution Pt. : : 0x0000000409A1B3E2	🖓 Go to File	
QDB0 : qdb0.o : QDB0	Add Breakpoint	Address
invokeDriver : cvzz.o : CVZZ	· · · · · · · · · · · · · · · · · · ·	Entry
CVZZ : cvzz.o : CVZZ		Line
Process: 0F384000 Program: QDB0	Enable	Load
	Disable	Macro
🗊 qdb0.cpp 🔀	X Remove	Watch
Line 139 Column 1 Insert Browse	💥 Remove All	Stop At All Function Entries
+1+2+3+4+5+6+7+8+	Select All Ctrl+A	
133 /* Call the parser */	Copy Chiro	
134	🛱 Paste 🛛 🛛 🖓 👘	
135 num_parms = IPRSE_parse (input_ptr,grammar_ptr,&parse_results,		
136 IPRSE_ALLOC IPRSE_PRINT , "DBUG"); 137	export Breakpoints	
137 — 137 138 /* display help manual if parser error */	• Import Breakpoints	
139 if (num parms < 1)		
140 {		
141 dispHelp();		
142 }		
143		
144 /***********************************		
145 /* */		
146 /* */		



Debugger: stop on all functions, high level breakpoints, etc

 Load breakpoints stop the execution of your ECB at the entry point of a module the first time it is called. Such as specifying * for the module.

Sreakpoints ☆ 1988 Registers ☆ Modules	🕸 Add a Load Breakpoint
× 💥 🧬 🗟 🔌 🕂 🗈 🖨 🔄 🏍	Required information Sets a breakpoint to stop execution when a dynamically loaded library is loaded
⊂ <mark>>•</mark> Go to File	Library name: * User label (optional):
Add Breakpoint Address Edit Breakpoint Entry	
✓ Enable Load Disable ✓ Macro	
Kemove Watch	
Select All Ctrl+A Stop At All Function Entries	
Copy Ctrl+C	
Paste Ctrl+V	
Import Breakpoints Export Breakpoints	Sack Next > Finish Cancel

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Debugger: stop on all functions, high level breakpoints, etc

 Macro and Macro group breakpoints stop on the execution of macros. Macro groups such as ENTER (all ENTxC and BACKC), DFALL (all TPFDF) and ALLSVC may be particularly useful in this capacity.

●⊖ Breakpoints ⊠ (>)= Variables ∰ TPF Malloc □	🖉 Add a Macro Breakpoint 📃 🗖 🗙
× ¾ 🔐 ⊴ 🔌 🧏 🗈 🖻 🕀 🔄 × ······ ✔ 🤊 Stop at all function entries	Required information Sets a macro breakpoint
Go to File Add Breakpoint Edit Breakpoint Edit Breakpoint Entry Entry Line Load Disable Macro Watch Watch Select All Ctrl+A Copy Ctrl+C Paste Ctrl+V Export Breakpoints Interpret Breakpoints	Executable (Optional) * Object (Optional) * Macro Macro Macro Macro Macro Macro Cancel * Cancel



Debugger: ECB Summary view, animated step into, execute shortcuts

- Suppose the code path of your application is less important to the debugging of a problem than the current state of the ECB, such as in debugging a recursive program.
 - 1. Minimize the editor view.
 - 2. Arrange other views to view the state of the application at a glance such as the ECB Summary view, SW00SR view, DECB view, variables view or etc. Ensure the debug view is visible.
 - 3. Use stop on all functions and high level breakpoints previously discussed and watch the state of the application change. Or use execute shortcut keys to execute the application manually. Or use animated step into to walk through your application step by step automatically. Or use step debug to debug a small set of applications.

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Debugger: ECB Summary view, animated step into, execute shortcuts

• ECB Summary and the animated step button for automatic stepping.

Debug 🔀		💥 🕩 🗉 🔳	M 💁 - R - R - R - R - R - R - R - R - R -	🗆 📋 ECB Su	ummary 🛛 බ්	Modules				E 1919		
2 9.57.13.89.qdb0 [Incomin	g Remote Debug Session]			A 1010 Regist								
🖃 🔐 Platform: ZTPF Conne				RO	000000000	0000000	R1	0000000	000000000	R2	00000000	0F334
😑 🛷 Thread:TPF Thread				R3	000000000				000000000		00000000	
	: 0x00000000932B0E8			R6	000000000	0000000	R7	0000000	011C6BE78	R8	00000000	0932B
qdb3 : qdb3.o				R9	000000000	F300000	R10	0000000	011C6BE70	R11	00000000	0932B
qdb2 : qdb2.o					000000000	0002000	R13	0000000	0D8C4C2F3	R14	00000000	00002
expCases : qdl				R15	00000000 0	A06090A						
QDB0 : qdb0.o				PSW	A 471510008	0000000	Δ	0000000	00932B0EC			
invokeDriver :	cv77.0 : CV77			2								
.listingqdb3 🛛				Work	Area							
Line 22 Column	1 Insert	Browse		WOOK	0102E4C7	004 0	0000000	008	851AB35	0 012	2 005D028	22
+1+2	+3+	-4+5+-	678	016	01022407		05E0282		0002010			
		4249 *	~	016	CF040C00		0000192		0100000			
		4250 * TPF deb	ugger driver program. 📃	032	00000020		2D4D7C2		0100000			
		4251 *		048			00000000		0000840			
			ion: ZTEST DBUG EXP-1 (QDB0 ->	001	00001104	000 0	0000000	072	0000040	0 070	, 0400000	
		4253 *		🔄 Miscel	laneous							
		4254 * Descrip		FAP	00000000	0000000	GLA	0240A00	0 HLD	00		
			t up DECBs in 2 E-type programs fines various type of assembler	ACN	0000003		SUI	00	SSU	FF00		
		4256 * 2. de 4257 *	fines various type of assembler	ISN	0001		CPD	в	GLY	024120	000	
		4258 *		IOC	0001		OUT	010000	DET	0F302E	84	
		4259		PAT	00000000	E8E88B8						
		4260 *		Data L	evel							
		4261 *	define various type of constant					-				
		4262 *		Name		CE1FMx	CE1C				SUD DCT	
	0E8 58F0 8BCC		L R15,=BL4'011000001001000010	DO	00000000	0000000		32E80		017D	00 00	
	0EC 58F0 8BE3		L R15,=CL11'HELLO'' WORLD'	D1 D2	00000000	0000000		34000 00000		0FFF 0000	00 00	
	0F0 58F0 8BB8	4265 LITTEST3	L R15,=FL8'1123343130003'	D2 D3	00000000	0000000		00000		0000	00 00	
	0F4 58F0 8BD0 0F8 58F0 8BEE	4266 LITTEST4 4267 LITTEST5	L R15,=HL4'12345' L R15,=AL3(LITTEST2-LITTEST1)	D3 D4	00000000	0000000		32A80		0000 017D	00 00	
	OFC 58F0 8BDE	4267 LITTESTS 4268 LITTEST6	L RIS,=AL3(LITTESI2-LITTESII) L RIS,=P'12.3'	D4 D5	00000000	0000000		32A80 A7000		017D 041F	00 01	
	0100 58F0 8BF1	4269 LITTEST7	L R15,-P'12.5' L R15,=Z'13.5'	DS D6	00000000	0000000		A7000 AF000		OFFF	00 01	
	104 58F0 8BD4	4270 LITTEST8	L R15,=E'13444.334'	D6	00000000	0000000		00000		0000	00 00	
	108 58F0 8BC0	4271 LITTEST9	L R15,=D'134432456.1'	D7	00000000	0000000		32C00		0000 017D	00 00	
	10C 58F0 8BA8		L R15,=L'343.21'	D8	00000000	0000000		00000		0000	00 00	
00000000932B110 0	110 58F0 8BE0	4273 LITTEST11	L R15,=S(*)	DA	00000000	0000000		00000		0000	00 00	
00000000932B114 0	114 58F0 8BD8	4274 LITTEST12	L R15,=A(*)	DB	00000000	0000000		00000		0000	00 00	
		4275		DD	00000000	0000000		00000		0000	00 00	
			IDECB REG=R3	DD	00000000	0000000		00000		0000	00 00	
00000000932B118 0	0118 0A3B	4337	DECBC FUNC=CREATE, DECB=(R3), NAM	DE	00000000	0000000		00000		0000	00 00	
				DF	00000000	0000000		00000		0000	00 00	



Debugger: ECB Summary view, animated step into, execute shortcuts

- TPF Toolkit provides short cut keys to issue execute actions without clicking buttons which can help you to focus on the state of your application:
 - F5 Step into
 - F6 Step over
 - F7 Step return
 - F8 Resume

Debugger: ECB Summary view, animated step into, execute shortcuts

- step debug is a feature that allows you to limit your debugging to a list of specified modules.
 - 1. In the debug console, use the step debug set command to set up the list of programs to limit the application stopping



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-	-	101	1000	-
	-			
3574	-	-	100 100	

Debugger: ECB Summary view, animated step into, execute shortcuts

2. Toggle on the step debug (step filters) button on



3. Now use the step into button. It will only stop in the modules listed in the step debug list or stop at any breakpoints that you've set.





Debugger: ECB Summary view, animated step into, execute shortcuts

 IMPORTANT NOTE: make sure you toggle off the step debug (step filters) button when you are finished. The setting of the step debug (step filters) button setting is saved. A pop up box warns you the first time you press the step into button and it is behaving as step debug. Do not ignore this warning! Many users have thought the debugger was broken when they simply forgot to turn this step debug feature off.



Debugger: optimized debugging vs non-optimized debugging

- The features used to determine code path can be used to debug optimized code with or without debug information loaded.
- Once you have a high level view of your application, you can begin to use other debugger features to narrow in on the source of your problem.
- As you narrow in on the area of the problem, rebuild those segments –O0 and load the code with debug information to have an ideal debugging experience with all available variables and linear code execution when stepping.
- Assembler code does not need to be rebuilt, just load debug information. You can also use the Remote Debug Information feature to have the debugger automatically load the debug information for you.

Hints and Tips

Topics

- What code am I debugging?
- Debugger performance

What code am I debugging?

- The stack view can be used to see the loadset for each module on the stack.
- The stack view also shows the compiler options for each object.

🏇 Debug 🖄 l 😹 🕪 💷 🛋 👌 🎭 ד 🧞 🕐 🖉 🔜 🛣 🏠 🍙 ד 🖓 🗖 🗖
9.57.13.89.qdb0 [Incoming Remote Debug Session]
Platform: ZTPF Connection: tpfosa1h89.pok.ibm.com: 2726
Thread:TPF Thread 10354000 (Stopped)
Execution Pt. : : 0x00000009DC98FC8
QDB0:qdb0.o-O0-g2 QDB0 DRIVER
invokeDriver : cvzz.o -O3 -g2 CVZZ BASE
CVZZ : cvzz.q -O3 -g2 CVZZ BASE
Englishing Process: 10354000 Program: QDB0


What code am I debugging?

 On the z/TPF system, use the ZDDBG DISPLAY DBGINFOprog command to see what debug information is available on the system for a specific module. The loadset name is provided so you can ensure that your code has debug information.

CSMP0097I	16.54.03	CPU-B SS	dbginfo-qd -BSS SSU-H fo for prog	PN IS-01
VERSION	LOADSET	DBUG	READABLE	DEBUG FILE
 QDB0 QDB0	BBBBB BASE	YES YES	YES YES	/tpfdbgelf/qd/qdb0/20130319162816.dbgftp /tpfdbgelf/qd/qdb0/20121102155807
	SPLAY +			



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What code am I debugging?

 Click on a stack frame and look at the properties view to see the compile time and other information.

🎋 Debug 🛛 l 💥 🕪 💷	🔳 🗤 🎐 • 🤉 🔊 🕫 🕾 🕸 🕼 • 🗸 – 🗖			
Thread:TPF Thread 10 Execution Pt. : : QDB0 : qdb0.o -O	on: tpfosa 1h89.pok.ibm.com: 1028 0333000 (Stopped) 0x00000009DD8FFC8 0 -g2 : QDB0 : BASE z.o -O3 -g2 : CVZZ : BASE 3 -g2 : CVZZ : BASE			
noperties 🛛	(대학 1987년 1887년 18			
Property	Value			
Call Statement	41			
Compile Date/Time	2012/11/02 13:55:11			
Entry Address	9DD8FF9C			
Function	QDB0			
Module	QDB0 : BASE			
Object	qdb0.o -O0 -g2			
Recursion	00			
Stack Frame	12C0F428			
Stack Size	624			



Debugger performance

- Set up your Edit Source Lookup to perform well:
 - Choose TPF project (limit the definition of project filters to a small set of files) or Remote folder since they are known to perform better.
 - Do not specify root directories (such as /ztpf/).
 - Specify directories as close to source as possible.
 - Specify as few paths as possible.
 - Do not search for multiple matches unless it is absolutely needed. This feature will search all directories and sub-directories on all paths for the matching file name and present a list of all matches to the user.
 - Do not search subfolders. Select the folders where your source exists explicitly.
 - If network performance is a drastic issue, copy source code to a local location on the hard drive, remove all network paths and set the path to this single local location. This will give the best performance in locating files but introduces source file synchronization issues.



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Debugger performance

- Open fewer debugger views.
- Give focus to views with static data (breakpoints, monitors, modules, etc) to hide more dynamic views.
- Hide complex or costly views until you need them.
 - Variables view if lots of variables are present.
 - SW00SR view
 - ECB Summary view
 - TPF Malloc view (hide the corruption detection column)
- Limit the use of labor intensive features such as perform heapcheck on stop.
- Turn off hover expression evaluation: from the preference option Window menu->Preferences->Run/Debug->Compiled Debug->Allow hover evaluation checkbox.



Debugger performance

Define remote debug information directories well.

- Specify as few paths as possible
- Specify a small timeout value. If FTP must timeout on each system and path and the timeout value is set significantly high, the user may need to wait a long time for the timeout to occur for each system and path (accumulating to a long wait time).
- If the network is performing poorly, load debug information by way of the loaders instead of relying on the remote debug information feature. Or use debugging techniques that do not require debug information to be loaded.

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1000		_	And these parts
1000	-	-	hits have seen
			100 H 10 H 100

Starting the debugger effectively

- Topics
 - Understand your application
 - Debugging the right ECB
 - Registration types
 - Tips for registering on shared test systems



Understand your application

- The answer to the following questions determines how you must register the debugger to debug the right ECB.
 - How is my ECB started? Is this ECB started by a CREMC, CRETC, TPF_fork, SWISC CREATE or so on? Is this ECB started by a pthread_create? Or is this ECB started from a communications terminal such as an incoming message on an LNIATA, IP or LU.
 - How does my application behave? Does it create ECBs such as CREMC and so on? Does it create threads? Are there events, LOCKCs, signals, waiting for user input (ZPAGE), waiting for responses from another system, and so on?
 - What part of my application do I need to debug? Does it call global constructors? Is a library malfunctioning or is the mainline path? Is a system function or macro not returning the expected result?
 - Where is the right spot in my application to start debugging such that I'm close to the cause of the problem?
 - Maybe you don't know your application in this level of detail. Do you know a main entry point name, a library used, or so on?



Debugging the right ECB

- The z/TPF debugger is an ECB centric debugger meaning that the ECB is debugged regardless of which code that ECB executes.
- As you think about starting the z/TPF debugger, always be thinking in terms of catching the right ECB that will execute the code you need to debug.
- Use the determining code path functionality to understand the application.

Registration types

- The second key to starting the debugger effectively is knowing what features the debugger has, how to use them, and what the limitations are in order to catch the right ECB.
 - Register by program name 4 char module name (wild cards are supported).
 - Register by function name First execution of a function (wild cards are supported).
 - **Register by SVC –** First execution of a macro.
 - Register by system error start the debugger on an application dump.
 - Register by CTEST start the debugger where ever CTEST is coded in your application.
 - Register by user defined registration start the debugger where ever you want under the conditions you define and register.

Registration types

- Most types of registration provide the following options.
 - TPF terminal acts as a filter in that only the ECBs with a matching terminal will be candidates for debugging. To debug created ECBs (CRETC, CREMC, etc), you must register with LNIATA as *.
 - **Conditional registration** acts as a filter in that only ECBs that meet the condition (register or ECB contents) will be candidates for debugging.
 - Trace created entries indicates to the debugger that you are interested in debugging ECBs that will be created from the initial parent ECB you debug in independent debugger sessions.
 - **Trace global variable initialization** allows you to debug global constructors and other initialization functions.
- Debugging threaded applications trace created entries is not necessary. All threads created are immediately stopped. Each thread is controlled independent of all other threads. Key is to click on a thread in the debug view and then perform your desired action.



Tips for registering on shared test systems

- Registering on a shared test system can present a number of challenges.
- You must define a registration entry such that you do not debug someone else's application.
- You must start your application such that it is not debugged by someone else's registration entry.



Tips for registering on shared test systems

- Use the trace by terminal feature by specifying a TPF Terminal instead of using * for the LNIATA when registering the debugger and have the traffic of each individual started from a different LNIATA.
- Use conditional registration to differentiate ECB from ECB. For example test against a unique value in the ECB such as EBROUT or so on.
- User Defined Registration can work well for these situations. You can define
 a field to test to be the ID of a user and as part of your application traffic
 embed the ID of the user in the ECB so that you can test against it.
- Every registration type allows you to pass in a user token. As part of your application traffic embed the ID of the user in the ECB. And every registration trace by program type of registration calls user exit UCCDBTS in cusr.cpy for verification that a debugger session can be started on that ECB. In UCCDBTS you can code a test to compare the user token in the IPROG entry to the ID of the user embedded in the application. Or instead of embedding the ID of the user in the application, you can equate the user token passed in on the registration entry with the IP address or another user unique feature in ECB. You can do a very similar sort of user token comparison for trace by terminal in the tpf_terminal_user_exit in cdbuxt.c.
- Use selective activation. The debugger will work in selectively activated programs without making any accommodations.



Tips for registering on shared test systems

 Another common problem in debugging on a shared test system is debugging code that someone else loaded. While the debugger cannot know if you are debugging the right code, it does show you which loadset your code was loaded in the debug view in each stack frame. If something is not behaving properly, confirm that you are debugging your code.



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Notes

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