

Introduction

- Some Post z/VM 5.2.0 News
 - PAV
 - OMEGAMON XE
- z/VM 5.3.0 Performance
 - Line Items that have an impact
- APARs of Interest
- z10 Performance

See Performance Report on web for details

http://www.vm.ibm.com/perf/reports/



- Previously only supported as dedicated disks.
- VM63855 (for z/VM 5.2.0, available May 2006):
 - CP uses PAV to potentially decrease response time on minidisk I/O
 - We tightened the rules about ATTACHing or DEDICATEing PAV devices
- VM63855 virtualizes PAV for minidisks.
- Useful for environments where queuing on I/O occurs for minidisk I/O.
- Sometimes referred to as SYSTEM-owned PAV volumes
- PAV Base and Alias volumes defined on the Storage CU
- Summary of Results
 - Varies depending on DASD CU Model
 - Varies depending on read-write mix
 - Helpful when I/O queuing occurs
 - Law of diminishing return; that is, defining more Alias than needed can lower performance

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PAV – Rules of Thumb

Symptom:

- I/O wait queue forming at real volume where minidisks are
- See Performance Toolkit FCX168 reports (or equivalent)

Remedy:

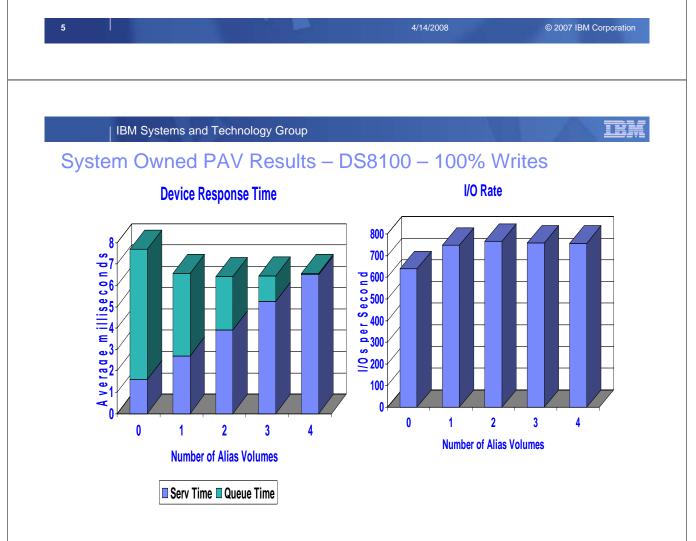
- Configure a PAV alias device in the storage controller
- Make sure the alias device is varied online
- Make sure the alias device is ATTACHed to SYSTEM

Measure:

- Re-run your workload
- Look again at those disk performance reports

Success criterion:

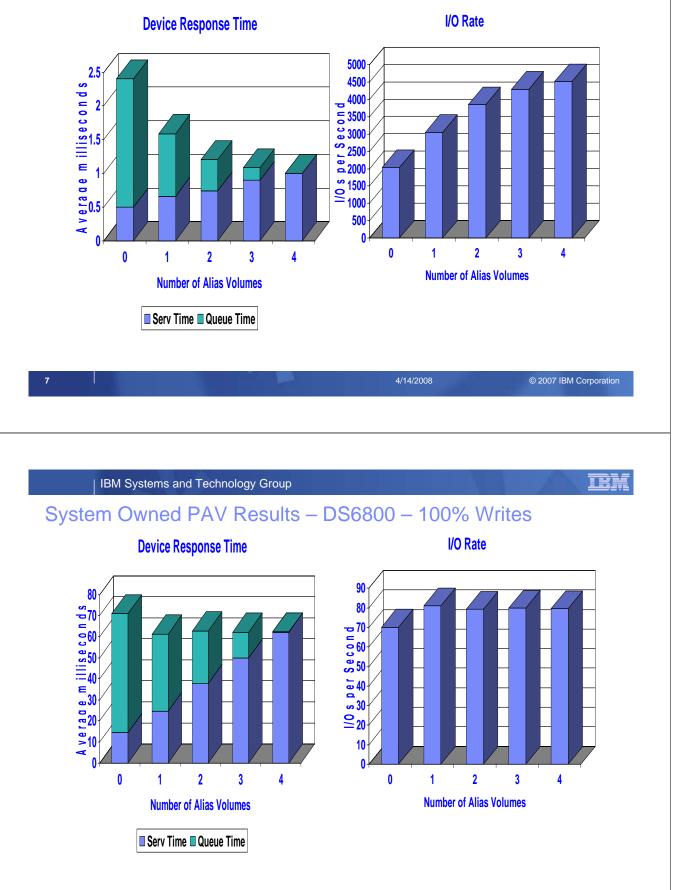
Response time equals service time (no wait queue)



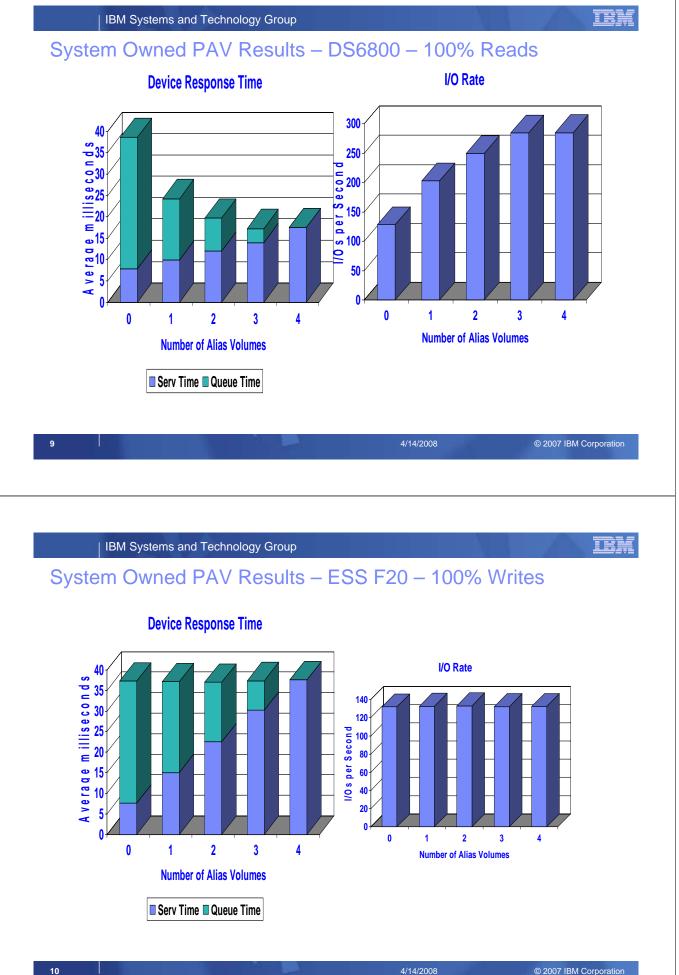
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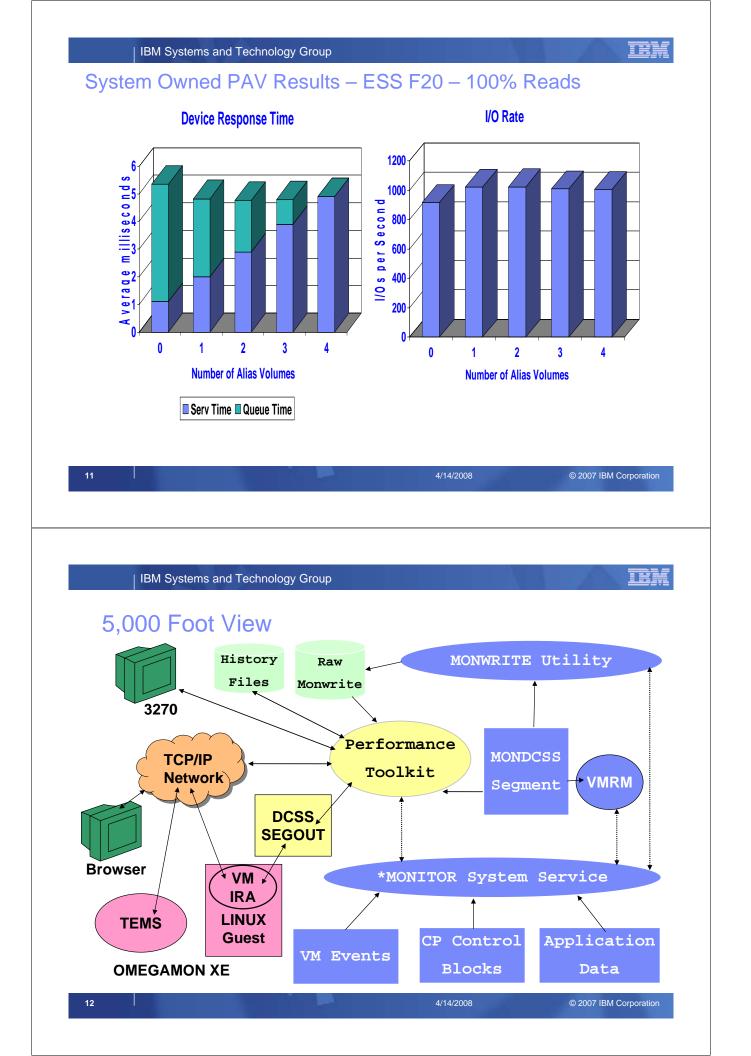




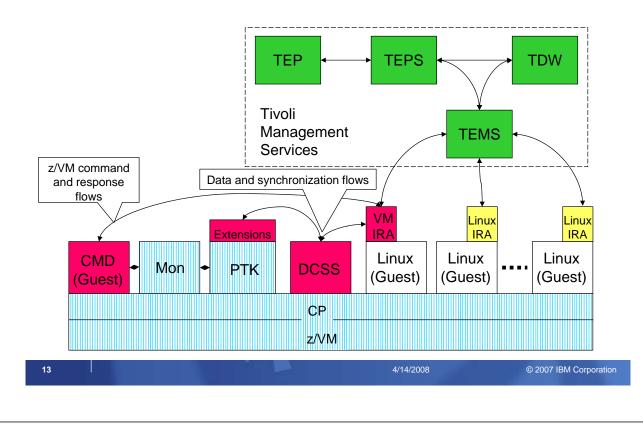


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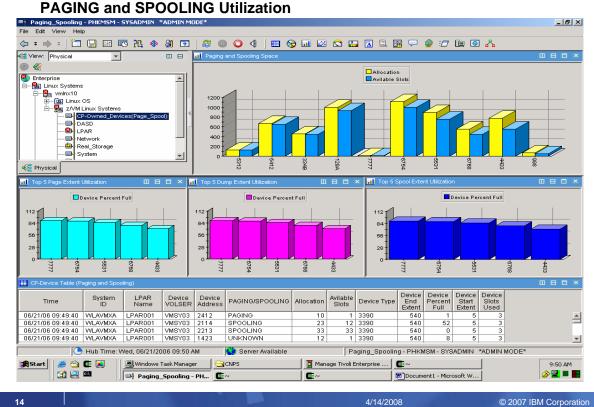


Basic Architecture



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z/VM 5.3.0

16

• GA June 29, 2007 - Many more details available in the z/VM Performance Report: http://www.vm.ibm.com/perf/reports/ Scalability and capability extended in several directions - Processors, Memory, I/O, Network -What were the old limits? -What are the new limits? Other Performance Enhancements 15 © 2007 IBM Corporation IBM Systems and Technology Group **Processor Scaling Number of Supported Processors** 36 32 28 24 20 16 12 8 4 0 1985 1990 1995 2000 2005 2010

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Greater than 24 CPU Support

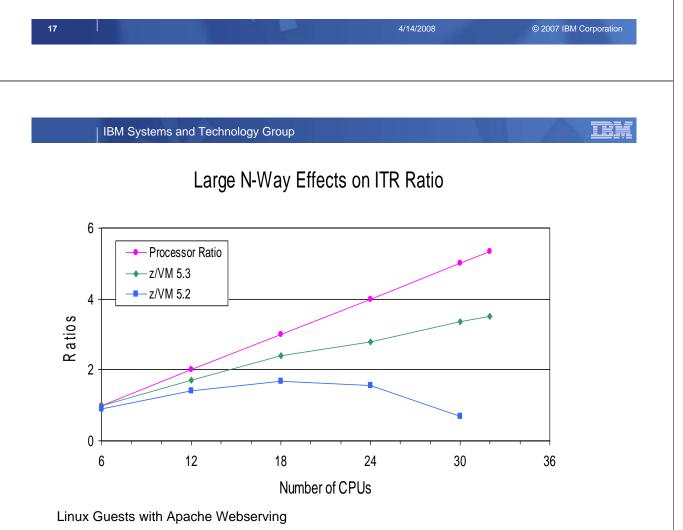
- While z/VM 5.2.0 would run on up to 31 processors, it only supported 24 due to performance limitations
- z/VM 5.3.0 supports 32 processors

Serialization Changes

- General support for exclusive and shared formal spin locks
- First to exploit is the Scheduler Lock (SRMSLOCK)
- New lock associated with each Processor Local Dispatch Vector (PLDV) for dispatching (DSVLOCK)

Performance is Workload Dependent

- Watch for Master Processor Limitations
 - · Tend to be more traditional workloads, not Linux environments
- Single non-MP virtual machine limits
 - Example: DB2 for z/VM & VSE can only use 1 processor



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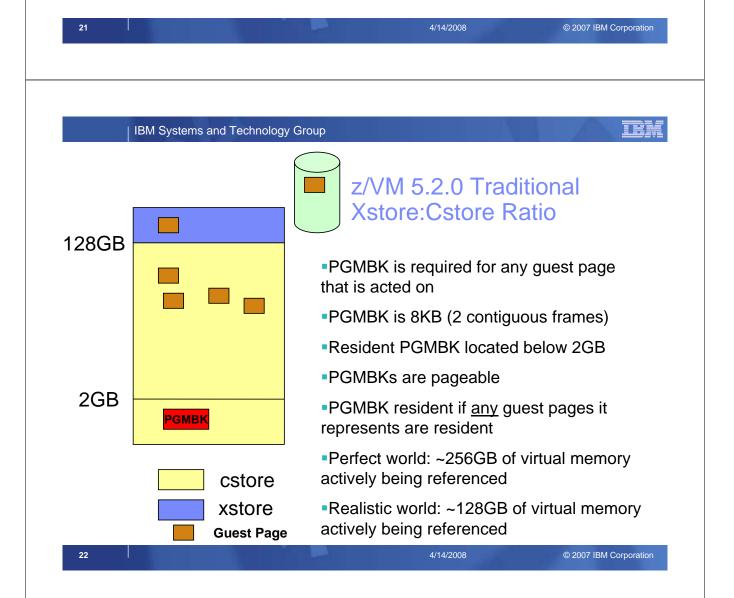
Metrics for Formal Spin Locks

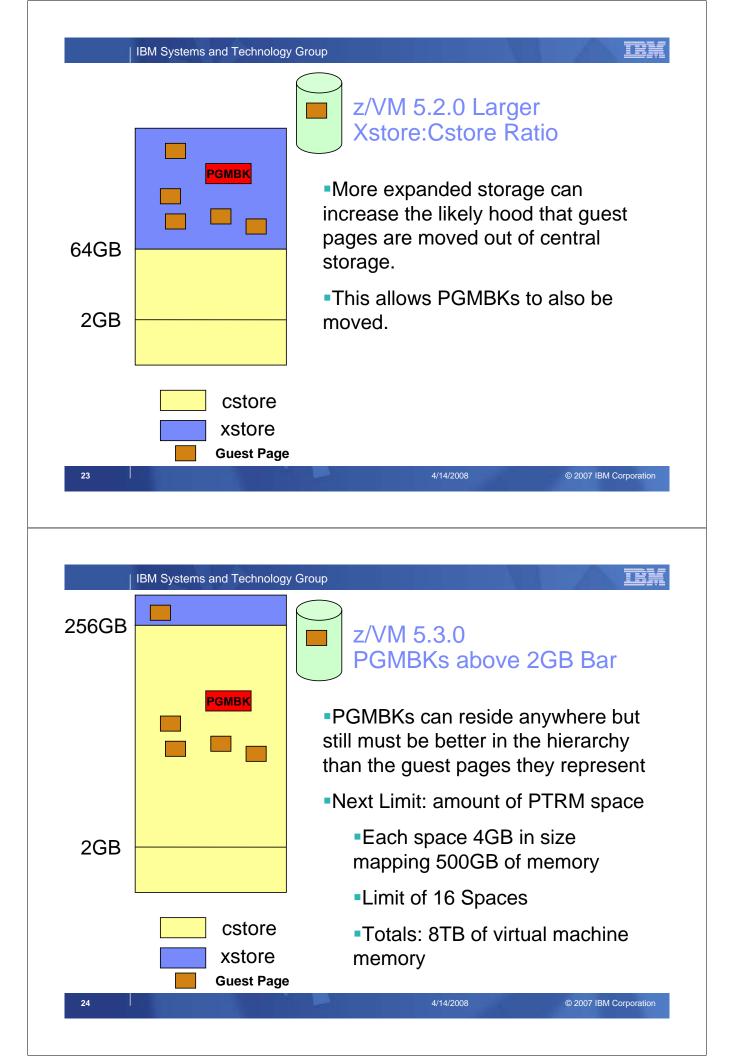
| E > | Interval | | | | | | | | | Μ7 | |
|----------------------------------|---|---------------|----------|---------|------|---------|-----------|--------|-------|------------|---------------|
| E > | Interval | | < | | | Spin L | ock Acti | vity - | | | > |
| E > | Interval | | < | Total - | > | < E: | xclusive | > | < | Shared | > |
| > | | | Locks . | Average | Pct | Locks 2 | Average | Pct | Locks | Average | Pct |
| | Ind Time | LockName | /sec | usec | Spin | /sec | usec | Spin | /sec | usec | Spin |
| > | >Mean>> | SRMATDLK | 1.9 | .539 | .000 | 1.9 | .539 | .000 | .0 | .000 | .000 |
| | >Mean>> | RSAAVCLK | .0 | 2.015 | .000 | .0 | 2.015 | .000 | .0 | .000 | .000 |
| > | >Mean>> | FSDVMLK | .0 | 24.97 | .000 | .0 | 24.97 | .000 | .0 | .000 | .000 |
| > | Mean>> | SRMALOCK | .0 | .000 | .000 | .0 | .000 | .000 | .0 | .000 | .000 |
| > | >Mean>> | HCPTRQLK | 4.1 | .195 | .000 | 4.1 | .195 | .000 | .0 | .000 | .000 |
| > | >Mean>> | SRMSLOCK | 34.0 | 1.096 | .001 | 32.7 | 1.037 | .001 | 1.3 | .001 | .000 |
| | | | | | | | | | | | |
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| Me | emory | / Scalin | g | | | | | | | | |
| Effective Real Memory Use Limits | | | | | | | | | | | |
| | | | | | | | | | , | | |
| | 200 | | | | | | | | • | | |
| | 288 - | | | | | | | | | | |
| | 256 - | | | | | | | | | | |
| | 256 - 224 - | | | | | | | | | | |
| | 256 - 224 - 192 - | | | | | | | | | | |
| | 256 - 224 - 192 - 160 - | | | | | | | | | | |
| | 256 - 224 - 192 - 160 - 128 - | | | | | | | | | | |
| | 256 - 224 - 192 - 160 - 128 - 96 - | | | | | | | | | | |
| | 256 - 224 - 192 - 160 - 128 - 96 - 64 - | | | | | | | | | | |
| | 256 - 224 - 192 - 160 - 128 - 96 - | | | | | | | | | | |

Greater than 128GB Memory Support (256GB)

z/VM 5.3 Improvements:

- PGMBKs allowed to be allocated above 2GB
 - Each PGMBK is 8KB (2 contiguous frames)
- Enhanced contiguous frame management
- Also seeing improvements to smaller configurations that are memory constrained
- Be careful with memory terminology
 - Try to define various terms when you use them or hear them
 - Examples:
 - Defined
 - Resident
 - Backed
 - Active
 - Actively Referenced
 - Addressable

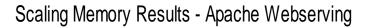


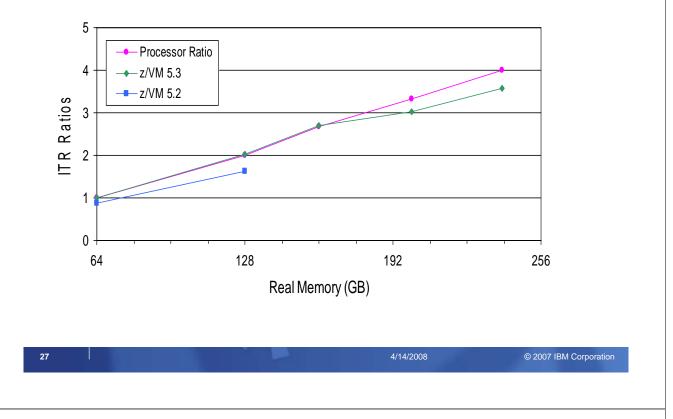


Performance Toolkit DSPACESH Report

| FCX134 CPU 2094 SER 19B9E Interval 13:04:01 - 13:09:01 GDLVM7 |
|---|
| <>Number of Pages> |
| Owning <resid> <-Locked> <-Aliases-></resid> |
| Userid Data Space Name Total Resid R<2GB Lock L<2GB Count Lockd XSTOR DASD |
| SYSTEM PTRM0000 1049k 35602 1104 0 0 0 980 7502 |
| Slightly edited FCX134 report PGMBKs live in PTRM0000, PTRM0001, PTRM000F Most systems will just have a PTRM0000 |
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| Limitations for Memory Memory limitations dependent on workload & configuration |
| – 256GB Real memory |
| 8TB of 'addressable' virtual machine memory – Limit of Page Tables |
| Paging Space (optimal when <50% full) 11.2TB for ECKD 15.9TB for Emulated FBA on FCP SCSI Virtual Machine Size (HW Dependent) |
| • 1TB on z9 |
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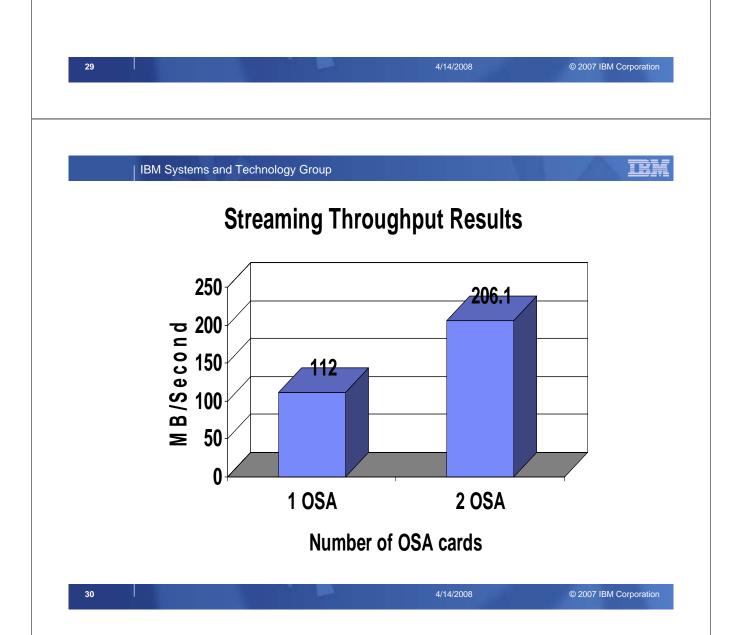
Improvement in Memory Constrained Environments

| | Contention | Delta | Delta |
|------------|-------------------------------|---------|--------------|
| Scenario | (Page Reqs per CPU-second) | Thruput | Total CPU/Tx |
| 3G/4G | 2159 | +10.3% | -9.5% |
| 2084 3-way | | | |
| 64G/2G | 0 | +1.0% | -0.9% |
| 2094 3-way | | | |
| 64G/2G | 352 | +15.5% | -12.7% |
| 2094 3-way | | | |
| 128G/2G | 291 | +21.6% | -19.4% |
| 2094 6-way | | | |

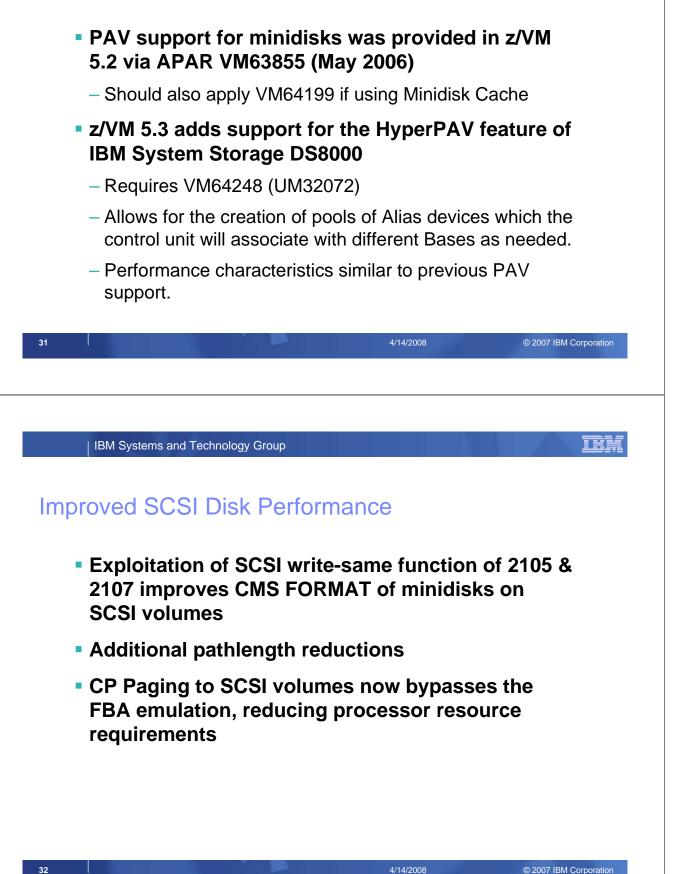
Results of various Linux Apache measurements comparing z/VM 5.3 to z/VM 5.2

Virtual Switch Link Aggregation

- Ability to attach multiple OSAs to a single virtual Switch
 - Aggregate bandwidth
 - Failover
- Requires:
 - z9 OSA-Express2 Support
 - Running in Layer 2 Mode
- Dynamic Load Balancing
 - Influenced by distribution of MAC addresses
 - Influenced by Physical Switch for inbound traffic
 - Cannot balance a single connection.
 - Example: a single data streaming connection will not get split across OSAs.



I/O Improvements



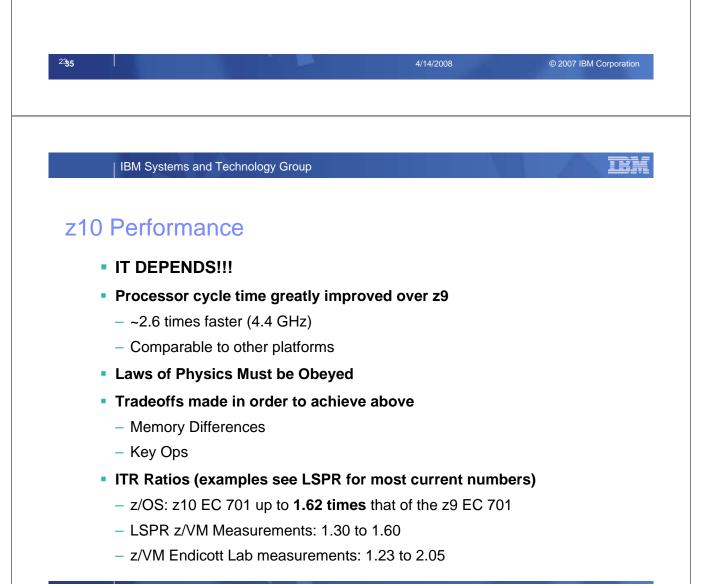
Monitoring Enhancements

- Lots of new fields in Monitor for new function:
 - specialty engines
 - Scheduler changes
 - HyperPAV support
 - Memory management
- New monitor Domain for Virtual Network Devices
- Additional flexibility in MONWRITE utility for starting/stopping
- Various changes in Performance Toolkit for VM

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| Service Must Ha | ves: R530 A | PARs | |
| VM64297 - PAG SYSTEM HANG | | IAND SCAN SHU | FS DOWN |
| – PTF UM32197 | ' Available and or | n RSU3 | |
| VM64269 - EXC SCAN | ESSIVE PAGING | GACTIVITY DURIN | IG DEMAND |
| – PTF UM32133 | B Available and or | n RSU2 | |
| | W PING RESPO NE VIRTUAL PR | NSE TIMES OVER OCESSOR | QDIO WITH |
| – PTF UM32158 | 8 available 10/18/ | 07 | |
| | | | |
| | | | |
| | | | |

Must Haves: R530 APARs

- VM64249 PEVM63853 LPAR CHECK STOP DURING EDM H/W UPGRADE
 - PTF UM32104 Available and on RSU2
 - HW Field Alert updated to include PTF numbers and additional information on z/VM 530 symptoms
 - Linux-only logical partitions when an Enhanced Driver Maintenance (EDM) occurs
- VM64323 PEVM63853 NO LPAR MONITOR RECORDS AFTER EDM UPGRADE
 - PTF UM32196 Available and on RSU3



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It Depends On....

- Number of Processors
 - Fewer processors, better ITRR
- Storage References
 - Smaller memory footprints, better ITRR
- Data Movement
 - Less data movement, better ITRR
- Virtual I/O to Real Devices
 - Less virtual I/O, better ITRR
- Storage Overcommitment
 - Less over commitment, better ITRR
- Amount of memory involved in long searches
 - Shorter & less frequent searches, better ITRR
- Exploitation of New Features
 - More exploitation of features, better ITRR

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Setting the proper expectations

- z10 is a great machine, with a number of excellent attributes.
- Care must be taken when sizing migrations from z9 to z10.
- Additional Information:
 - –LSPR Q & A (complete)
 - Discuss range and factors affecting
 - Pointer to z/VM Web Page
 - -z/VM Web Page
 - http://www.vm.ibm.com/perf/z10.html
 - "To MIPS or Not to MIPS, That is the Question!" by Gary King
 - http://shareew.prod.web.sba.com/proceedingmod/abstract.cfm?abstract_id=17583

Summary

- z/VM 5.2 Improvements via Service
- z/VM 5.3 significantly extends the capacity of:
 - Processor
 - Memory
 - I/O
- See z/VM Performance Report for more details
 - http://www.vm.ibm.com/perf/reports/
- Learn more about z/VM
 - http://www.vm.ibm.com/events/

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