# Networking in zEnterprise between z/OS and Linux on System z



# Enterprise2013

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# Reference Architecture for Networks with System z







# System z Networking - Operational Diagram





# Reference Architecture for Networks with System z



#### Note:

- All network Connections in System z and zEnterprise are realized via OSA (Open System Adapter) cards
- Different types of network possible with the same OSA card
- Hipersockets the network in the box, has no setup requirements from a HW perspective
- New: RoCE cards for ,remote Hipersockets' network communications





# OSA Express communication characteristics



**PCI** Card

**PCI** Card

PCI Bus

PCI Bus

Power

Power

CPU

CPU

- 'Integrated Power computer' with network card
- Shared between up to 640 OSA devices
- Three device numbers (ccw devices) per OSA device:
  - Read device (control data ← OSA)
  - Write device (control data  $\rightarrow$  OSA)
  - Data device (network traffic)
- OSA Address Table: which OS image has which IP address
- Network traffic OS (i.e. Linux) ↔ OSA
  - IP (layer3 mode)
  - Ethernet / data link layer level (layer2 mode)
  - OSA handles ARP- (Address Resolution Protocol)
  - One MAC address for all stacks

#### Note:

- Communication is asynchronous from an application perspective
- Communication is at OSA card clock speed (typically lower than Hipersockets)



# zEnterprise – What is INMN, IEDN and Customer network



Networking in zEnterprise between z/OS and Linux on System z

### OSA Express – Network types OSA Express 4s, OSA Express 3, OSA Express 2

- OSA Express supports various features such as:
  - 10 Gigabit Ethernet
  - Gigabit Ethernet
  - 1000BASE-T Ethernet

#### CHPID types

- OSCOSA-ICC (for emulation of TN3270E and non-SNA DFT 3270, IPL CPCs, and LPARs, OS system console operations)
- **OSD** Queue Direct Input/Output (QDIO) architecture
- OSE non-QDIO Mode (OSA-2, for SNA/APPN connections)
- OSN OSA-Express for NCP: Appears to z/OS and z/VSE as a device-

supporting channel data link control (CDLC) protocol.

- IQD The HiperSockets hardware device is represented by the IQD CHPID and associated subchannel devices. All LPARs that use the same IQD CHPID have internal connectivity and communicate using HiperSockets
- OSX OSA-Express for zBX. Provides connectivity and access control to the Intra-Ensemble Data Network (IEDN) from z196 and z114 to Unified Resource Manager functions.
- OSM OSA-Express for zEnterprise Ensemble management. OSM ports connect to the Intranode Management Network (INMN)
- IQDX Hipersockets Bridge to zBX





its

# Open Systems Adapter (OSA) performance



- OSA processor becomes more efficient as throughput increases
- Window size
  - TCP window determines amount of data that the sender can transmit to receiver without needing an acknowledgment from the receiver
  - Faster and longer networks require larger windows to keep data flowing smoothly
- Blocking
  - Performance is affected by the amount of data blocked together for transfer between OSA and TCP
- Frame size
  - Larger frames perform better
  - Larger frames reduce host and OSA processing costs
  - Size of frame depends on LAN type, MTU setting, size of data sent
- Measure: throughput, transaction response time, server utilization
  - Bulk data transfer and interactive transactions
- QDIO and jumbo frames (8992 byte MTUs) yield the highest streams
  - Ethernet II (DIX) MTU 1500 and jumbo frame MTU 9000 (most common)
  - IEEE802 MTU 1492 and jumbo frame MTU 8992



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# Summary: OSA-Express CHPID types to control operation



from zEC12, z196, or z114 to zBX



Linux on System z

zEC12, z196, z114





# Reference Architecture for Networks with System z





## System z Network alternatives between LPARs with OSA





# Network between LPARs on System z - Hipersockets network

- Connectivity within a central processor complex without physical cabling
- Licensed Internal Code (LIC) function

-emulating Data Link Layer of an OSA-device (internal LAN)

- Internal Queued Input/Output (IQDIO) at memory speed
- 4 different MTU sizes supported (Max. Transmission Unit):

-8KB, 16KB, 32KB, 56KB

- Support of
  - -Broadcast, VLAN, IPv6, Layer2 (starting with z10)
- UP to 32 different, isolated networks
- Synchronous communication
- Bi-directional CPU speed communication
   Note: Hipersockets needs n/2 defined buffers which should be at 32-128 for a good

performance





# New: Hipersockets Completion Queue between LPARs

# Avail since: 06/2012: Function for zEnterprise between LPARs

- •When the remote side can receive data volume then data is sent synchronously.
- •When the remote side cannot receive data volume then data is sent asynchronously.

**HiperSockets** transfers data synchronously if possible and asynchronously if necessary.

- Ultra-low latency with more tolerance for traffic peeks.
- Requires zEnterprise z196 or later hardware.
- Requires z/OS V1.13 or later software.



# Recommendations

![](_page_15_Picture_2.jpeg)

#### • Hipersockets and Hipersockets Completion Queue between LPARs

- for similar CPU / LPAR power, Hipersockets is recommended
- to increase stability and network speed use between 32- 64
  Hipersockets buffers and adjust MTU size

### Shared OSA network

 for LPARs with limited CPU speed (below 1/2 of max CPU speed), the shared OSA network might be faster

![](_page_15_Picture_8.jpeg)

![](_page_16_Picture_1.jpeg)

s included various data sizes

#### Optimize server to server networking – transparently – zEC12+zBC12 "HiperSockets<sup>™</sup>-like" capability across physical systems

Up to 50% CPU savings for FTP file transfers across z/OS systems versus standard TCP/IP \*\*

Up to **48%** reduction in response time and **10%** CPU savings for a sample CICS workload exploiting IPIC using SMC-R versus TCP/IP \*\*\*

Up to 40% reduction in overall transaction response time for WAS workload accessing z/OS DB2 \*\*\*\*

Up to *3X* increase in WebSphere MQ messages delivered across z/OS systems \*\*\*\*\*

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![](_page_16_Figure_7.jpeg)

#### Shared Memory Communications (SMC-R):

Exploit RDMA (Remote Direct Memory Access) over Converged Ethernet (RoCE) with qualities of service support for dynamic failover to redundant hardware

#### Typical Client Use Cases:

Help to reduce both latency and CPU resource consumption over traditional TCP/IP for communications across z/OS systems

Any z/OS TCP sockets based workload can **seamlessly** use SMC-R without requiring any application changes

![](_page_16_Picture_13.jpeg)

\*\* Based on internal IBM benchmarks in a controlled environment using z/OS V2R1 Communications Server FTP client and FTP server, transferring a 1.2GB binary file using SMC-R (10GbE RoCE Express feature) vs standard TCP/IP (10GbE OSA Express4 feature). The actual CPU savings any user will experience may vary.

\*\*\* Based on internal IBM benchmarks using a modeled CICS workload driving a CICS transaction that performs 5 DPL (Distributed Program Link) calls to a CICS region on a remote z/OS system via CICS IP interconnectivity (IPIC), using 32K input/output containers. Response times and CPU savings measured on z/OS system initiating the DPL calls. The actual response times and CPU savings any user will experience will vary.

\*\*\*\* Based on projections and measurements completed in a controlled environment. Results may vary by customer based on individual workload, configuration and software levels.

\*\*\*\*\* Based on internal IBM benchmarks using a modeled WebSphere MQ for z/OS workload driving non-persistent messages across z/OS

![](_page_17_Picture_1.jpeg)

# Shared Memory Communications – Remote Direct Memory Access (SMC-R) Definition

- Shared Memory Communications Remote Direct Memory Access is a new communication protocol aimed at providing transparent acceleration for sockets-based TCP/IP applications and middleware
  - Remote Direct Memory Access (RDMA) technology provides low latency, high bandwidth, high throughput, low processor utilization attachment between hosts
  - SMC-R utilizes RDMA over Converged Ethernet (RoCE) as the physical transport layer
- SMC-R is built on the following concepts:
  - RDMA enablement of the communications fabric
  - Partitioning a part of OS host real memory into buffers and using RDMA technology to access this memory
  - Establishing an 'out of band' connection over which data is passed to the partner peer using RMDA writes and signaling

![](_page_17_Picture_10.jpeg)

![](_page_18_Picture_1.jpeg)

# RDMA (Remote Direct Memory Access) Technology Overview

- Key attributes of RDMA
  - Enables a host to read or write directly from/to a remote host's memory *without* involving the remote host's CPU
    - By registering specific memory for RDMA partner use
    - Interrupts *still required* for notification (i.e. CPU cycles are not completely eliminated)
  - Reduced networking stack overhead by using streamlined, low level, RMDA interfaces
    - No requirement for TCP/IP protocols/stack, sockets, etc.
      - Low level APIs such as uDAPL, MPI or RDMA verbs allow optimized exploitation
        - > For applications/middleware willing to exploit these interfaces
  - Key requirements:
    - A reliable "lossless" network fabric (LAN for layer 2 data center network distance)
    - An RDMA capable NIC (RNIC) and ethernet fabric switches (recommended) or point to point

![](_page_18_Figure_14.jpeg)

![](_page_19_Picture_1.jpeg)

# Sysplex Distributor before RoCE

![](_page_19_Figure_3.jpeg)

- Traditional Sysplex Distributor
  - All traffic from the client to the target application goes through the Sysplex Distributor TCP/IP stack
  - All traffic from the target application goes directly back to the client using the TCP/IP routing table on the target TCP/IP stack.

![](_page_19_Picture_7.jpeg)

![](_page_20_Picture_1.jpeg)

# Sysplex Distributor after RoCE

![](_page_20_Figure_3.jpeg)

- RoCE Sysplex Distributor
  - The initial connection request goes through the Sysplex Distributor stack.
  - The session then flows directly between the client and the target over the RoCE features.

Note: As with all RoCE communication the session end also flows over the OSAs.

![](_page_20_Picture_8.jpeg)

![](_page_21_Picture_1.jpeg)

#### Impact of SMC-R on real z/OS workloads – early benchmark results

![](_page_21_Figure_3.jpeg)

\* Based on projections and measurements completed in a controlled environment. Results may vary by customer based on individual workload, configuration and software levels.

\*\* Based on internal IBM benchmarks in a controlled environment using z/OS V2R1 Communications Server FTP client and FTP server, transferring a 1.2GB binary file using SMC-R (10GbE RoCE Express feature) vs standard TCP/IP (10GbE OSA Express4 feature). The actual CPU savings any user will experience may vary.

\*\*\* Based on internal IBM benchmarks using a modeled CICS workload driving a CICS transaction that performs 5 DPL calls to a CICS region on a remote z/OS system, using 32K input/output containers. Response times and CPU savings measured on z/OS system initiating the DPL calls. The actual response times and CPU savings any user will experience will vary. 25 © 2013 IBM Corporation

## System z Network alternatives between z/VM LPARs

![](_page_22_Picture_2.jpeg)

![](_page_22_Figure_3.jpeg)

# z/VM Multi-zone Network VSWITCH (red - physical isolation

![](_page_23_Figure_2.jpeg)

With 2 VSWITCHes, 3 VLANs, and a multi-domain firewall

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![](_page_23_Picture_4.jpeg)

# Virtual Network Interface Card (vNIC)

- A simulated network adapter
  - OSA-Express QDIO
  - HiperSockets
  - Must match LAN type
- Usually 3 devices per NIC
- Provides access to Guest LAN or VSWITCH
- Created by directory or *CP DEFINE NIC*

![](_page_24_Figure_9.jpeg)

Guest LAN or virtual switch

#### z/VM guests (Linux, z/OS, z/VSE,... )

DEF NIC 600 TYPE QDIO COUPLE 600 SYSTEM VSWITCH1

![](_page_24_Picture_13.jpeg)

![](_page_25_Picture_1.jpeg)

# Virtual LAN (VLAN) Support

- IEEE Standard 802.1Q
- Reduce broadcast traffic
- Divide LANs logically into subnets to optimize bandwidth utilization
- Network devices supporting VLAN:
  - real OSA card, HiperSockets, z/VM GuestLAN, z/VM VSWITCH

![](_page_25_Figure_8.jpeg)

![](_page_26_Picture_1.jpeg)

# VSWITCH Definition for multiple VLANs

Prior to z/VM 6.2 if a Guest required access to multiple VLANs there Were two ways to define this connectivity.

- 1. Have the Guest connect to multiple VSWITCHes.
  - Each VSWITCH would provide the Guest an ACCESS Port
  - Each vNIC would have a unique vMAC.
- 2. Have the Guest GRANT to a Vswitch with a TRUNK Port.
  - The Guest would load the 8021Q module
  - Configure a VLAN with VCONFIG there is one vMAC.

For ease-of-use it would be desirable to have this connectivity with one VSWITCH where the Guest vNICs are GRANTed as ACCESS Ports

• Each vNIC would have a unique vMAC.

![](_page_26_Picture_12.jpeg)

![](_page_27_Figure_0.jpeg)

DEFINE VSWITCH VSWT1 RDEV 2A00 ETHERNET CONTROLLER \* VLAN 200 NAT 1 DEFINE VSWITCH VSWT2 REDV 2A03 ETHERNET CONTROLLER \* VLAN 300 NAT 1 SET VSWITCH VSWT1 GRANT GUEST SET VSWITCH VSWT2 GRANT GUEST

![](_page_27_Picture_2.jpeg)

![](_page_28_Figure_0.jpeg)

DEFINE VSWITCH VSWT1 RDEV 2A00 ETHERNET CONTROLLER \* VLAN AWARE PORTBASED NAT 1 SET VSWITCH VSWT1 PORTNUMBER 20 GRANT GUEST VLAN 200 SET VSWITCH VSWT1 PORTNUMBER 21 GRANT GUEST VLAN 300

![](_page_29_Figure_1.jpeg)

- Virtual router is required
- Different subnets
- External router awareness
- Guest-managed failover

- No virtual router
- Same subnets
- Transparent bridge
- CP-managed failover

![](_page_29_Picture_10.jpeg)

# Channel Bonding - for higher bandwidth

- The Linux bonding driver provides a
  - -method for aggregating multiple
  - -network interfaces into a single,
  - -logical "bonded" interface
- Provides failover and/or load balancing functionality
- Better performance depending on bonding mode
- Requires layer2 devices
- Further information
  - -http://sourceforge.net/projects/bonding

![](_page_30_Figure_11.jpeg)

![](_page_30_Picture_12.jpeg)

# Network bandwidth enhancement and Automated Failover

![](_page_31_Picture_2.jpeg)

#### Resource Virtualization: **OSA Channel Bonding** in Linux

![](_page_31_Figure_4.jpeg)

- Linux bonding driver enslaves multiple OSA connections to create a single logical network interface card (NIC)
- Detects loss of NIC connectivity and automatically fails over to surviving NIC
- Active/backup & aggregation modes
- Separately configured for each Linux

# Network Virtualization: z/VM Port aggregation

![](_page_31_Figure_10.jpeg)

- z/VM VSWITCH enslaves multiple OSA connections. Creates virtual NICs for each Linux guest
- Detects loss of physical NIC connectivity and automatically fails over to surviving NIC
- Active/backup & aggregation modes
- Centralized configuration benefits all guests

![](_page_31_Picture_15.jpeg)

TBM

![](_page_32_Figure_2.jpeg)

- Built-in failover and failback
- Bridge new IQDX chpid to OSX chpid
- Also works for IQD to OSD

- Same or different LPAR
- One active bridge per CEC
- PMTU simulation

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![](_page_32_Picture_10.jpeg)

# HIPERSOCKET Bridge – Network HA Overview

A HiperSockets channel is an intra-CEC communications path.

The HiperSockets Bridge provides inter-CEC Hipersockets LAN connection to combine many HiperSockets LANs into a Layer 2 broadcast domain. An ideal function for the SSI environment.

![](_page_33_Figure_4.jpeg)

The HiperSockets Bridge function is available in z/VM 6.2 with a z114 or z196 or zEC12 processor. Must be sure to have z/VM and processor maintenance levels..

![](_page_33_Picture_6.jpeg)

![](_page_34_Picture_1.jpeg)

# z/OS support for Hipersockets Bridge, CHPID = IQD

- z/OS does not support the bridging of non managed (zManager) networks, that is IQD configured as EXTERNAL\_BRIDGED. For a Bridge HiperSockets (IQD) channel an explicit OSD and or IQD interface must be configured.
- For the IEDN z/OS provides a "Converged IQDX Link". This support provides transparent IQDX connectivity through the AUTOIQDX (GLOBAL CONFIG) which dynamically and transparently configures an IQDX interface under its existing OSX interface.
  - Once configured, Communications Server transparently splits and converges network traffic to this interface.
  - For outbound traffic on this single OSX interface, Communication Server will decide based on the destination IP address to either send the packet on the IQDX link or the OSX link.
- Virtual switches (QDIO, OSX) deployed as HiperSockets bridges are Layer 2 transports only. z/OS does not support Layer 2 mode for OSD and OSX vNICs. Connectivity through a virtual switch to the bridged HiperSockets LAN is not possible.
- Attention: Do not configure z/OS or any other guest that has both HiperSockets and OSA-Express interfaces to forward traffic from one link to the other, this may cause duplicate packets to be generated and in some cases initiate a broadcast storm.

![](_page_34_Picture_9.jpeg)

VSWITCH Topology A typical Vswitch topology for multiple CECs. Active and Backup Uplink Ports to redundant Ethernet switches.

![](_page_35_Figure_4.jpeg)

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![](_page_35_Picture_5.jpeg)

![](_page_36_Picture_1.jpeg)

![](_page_36_Figure_2.jpeg)

![](_page_37_Picture_1.jpeg)

## Reference Architecture for Networks with System z

![](_page_37_Figure_3.jpeg)

![](_page_37_Picture_4.jpeg)

## System z Network alternatives from z/VM Guests

![](_page_38_Figure_2.jpeg)

![](_page_38_Picture_3.jpeg)

# z/OS Network Exploitation Support (z/OS 1.13 and higher)

- Provides same functionality as that on the IBM zEnterprise 196
- IBM zEnterprise Unified Resource Manager
- Network and Performance Management
- Intranode Management Network (INMN)
- Intra ensemble data network (IEDN)
- OSA-Express3 and OSA-Express4S Inbound Workload queuing (IWQ)
  - -Large Send for IPv6,
  - -Inbound Workload queuing (IWQ) for Enterprise Extender traffic
- OSA-Express4S checksum offload for IPv6
- OSA-Express4S checksum offload for LPAR to LPAR traffic (both IPv4 and IPv6)
- HiperSockets optimization for intraensemble data networks (IEDN)

![](_page_39_Picture_13.jpeg)

![](_page_39_Picture_14.jpeg)

# Linux on z network options and interfaces

![](_page_40_Figure_2.jpeg)

![](_page_40_Picture_3.jpeg)

for ouest OS or Virtual gues

![](_page_41_Picture_1.jpeg)

- A communications facility inside of z/VM
- A program running in a z/VM guest communicating
  - With another virtual machine within same z/VM
    - Running Linux on z/VM
    - Running other Operating System (for instance VSE)
  - With a CP system service
  - With itself
- IUCV interrupt control functions to
  - -establish and remove communication paths
  - -transfer messages

![](_page_41_Picture_12.jpeg)

![](_page_41_Picture_13.jpeg)

# A Terminal Server environment using z/VM IUCV without TCP

![](_page_42_Figure_2.jpeg)

![](_page_42_Picture_3.jpeg)

![](_page_43_Picture_1.jpeg)

# **Summary Network Recommendations**

- Which connectivity to use:
  - External connectivity:
    - LPAR: 10 GbE cards
    - z/VM: VSWITCH with 10GbE card(s) attached
    - For maximum throughput and minimal CPU utilization attach OSA directly to Linux guest
  - Internal connectivity:
    - LPAR:
      - HiperSockets for LPAR-LPAR communication (if fullsize CPUs used)
      - Hipersockest Completion Queue (CQ) for asynchronous communication
      - Hipersocket Bridge for SSI and multi CEC environments
      - Shared OSA for capped CPUs in one of the LPARs
    - z/VM:
      - VSWITCH for guest-guest communication
      - VLANs for network isolation
      - IUCV for inter guest communication and Linux access without TCP/IP
- For high network workload and cloud use:
  - z/VM VSWITCH with link aggregation
  - OSA channel bonding

Both include high availability and automatic failover.

![](_page_43_Picture_22.jpeg)

![](_page_44_Picture_1.jpeg)

# System z and zEnterprise Network value points

- Network Simplification ("Network in a Box")
- Central point of Management (Unified Resource Manager via the HMC/SE)
- Single physical network and zBX "package" (physical network integration)
- Reduced network path length; reduced number of hops
- Secured internal communications
- Physical security (internal / dedicated network equipment)
- Logical security (controlled access)
- Network Virtualization and Isolation
- High Availability network
- Redundant network hardware
- Logical failover
- Unique System z QoS
- Isolated / dedicated equipment with integrated HA
- Special purpose dedicated
  - data network & OSA-Express
  - potential for reduced network encryption and HW encryption support

![](_page_44_Picture_19.jpeg)

![](_page_45_Picture_1.jpeg)

### **Insurance Company Consolidated 292 Servers to a z10**

![](_page_45_Figure_3.jpeg)

![](_page_45_Picture_4.jpeg)

Distantistication of the

Data is based on real client opportunity and on internal standardized costing tools and methodologies. Client results will vary by process of workloads, technology level of consolidated servers, utilization factor, and other implemented by the second servers of the second second servers of the second second servers of the second second second second second second servers of the second sec

![](_page_46_Picture_1.jpeg)

# **Additional Documentation**

IBM System z Networking

http://www.ibm.com/systems/z/hardware/networking/

IBM System z Connectivity Handbook

http://www.redbooks.ibm.com/redpieces/abstracts/sg245444.html

VM Networking

http://www.vm.ibm.com/virtualnetwork/

Linux on System z documentation

http://www.ibm.com/developerworks/linux/linux390/documentation\_dev.html

Linux on System z - Tuning Hints & Tips

http://www.ibm.com/developerworks/linux/linux390/perf/index.html

- Linux on System z on developerWorks <u>http://www.ibm.com/developerworks/linux/linux390</u>
- Linux on System z Downloads

http://www.ibm.com/developerworks/linux/linux390/development\_recommended.html

![](_page_46_Picture_16.jpeg)

![](_page_47_Picture_1.jpeg)

# **Questions?**

![](_page_47_Picture_3.jpeg)

# Wilhelm Mild

IBM IT Architect

![](_page_47_Picture_6.jpeg)

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![](_page_47_Picture_10.jpeg)