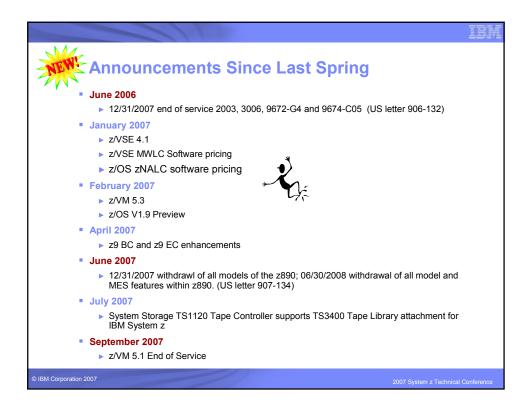
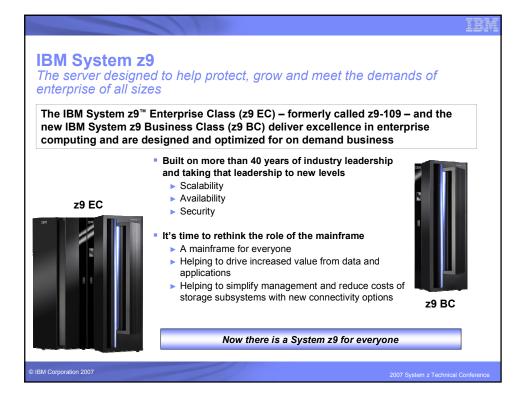
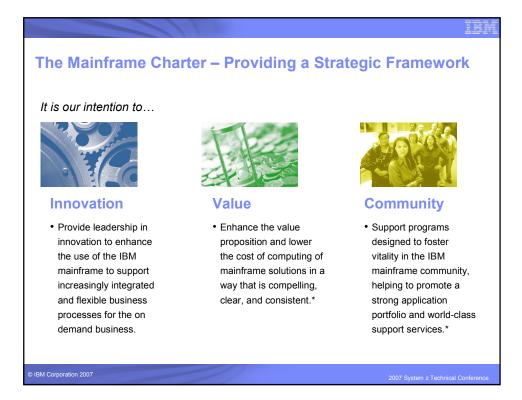


rademarks	5		
		Corporation in the United States and/	for other countries.
BookManager*	FICON*	Lotus*	Tivoli*
CICS*	FlashCopy*	MQSeries*	TotalStorage*
DB2*	GDDM*	Multiprise*	Virtualization Engine
DB2 Connect	GDPS*	OMEGAMON*	VisualAge*
DB2 Universal Database	geoManager*	OS/390*	VM/ESĂ*
DirMaint	HiperSockets	Parallel Sysplex*	VSE/ESA
Domino	HyperSwap	PR/SM	VTAM*
DRDA*	IBM*	QMF	WebSphere*
DS4000	IBM eServer	RACF*	z/Architecture*
DS6000	IBM logo*	Rational*	z/OS* z/VM*
DS8000	ImagePlus*	RMF	z/vm <sup>-</sup> z/VSE
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Enterprise Storage Server*		System z System z9	zSeries Entry License Charge
ESCON*	Intelligent Miner Language Environment*	System Storage	
Registered trademarks of IBM Co		-,	
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	ation in the United States, other countri		
		icrosystems, Inc., in the United States a	nd other countries
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	of Microsoft Corporation in the United		
	narks or registered trademarks of their		
Notes:	narks of registered trademarks of their i	espective companies.	
Performance is in Internal Throughp hat any user will experience will var	ry depending upon considerations such	as the amount of multiprogramming in	M benchmarks in a controlled environment. The actual throughput the user's job stream, the I/O configuration, the storage configuration at improvements equivalent to the performance ratios stated here.
		iceable used parts. Regardless, our wa	
		d as illustrations of the manner in which cs will vary depending on individual cust	a some customers have used IBM products and the results they may tomer configurations and conditions.
		products, services or features discusse for information on the product or service	d in this document in other countries, and the information may be as available in your area.
nformation about non-IBM products	s is obtained from the manufacturers of		epresent goals and objectives only. uncements. IBM has not tested those products and cannot confirm the IBM products should be addressed to the suppliers of those products
Prices subject to change without no	tice. Contact your IBM representative	or Business Partner for the most current	pricing in your geography.
	No. of Concession, Name of Con		

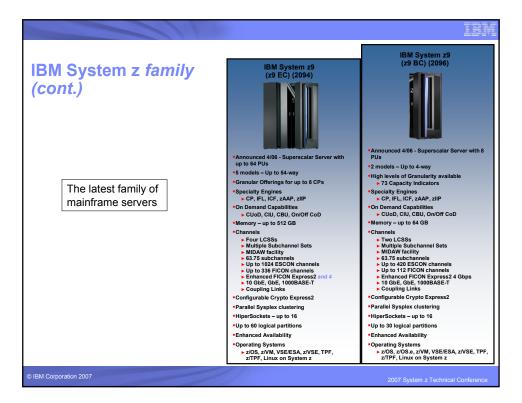


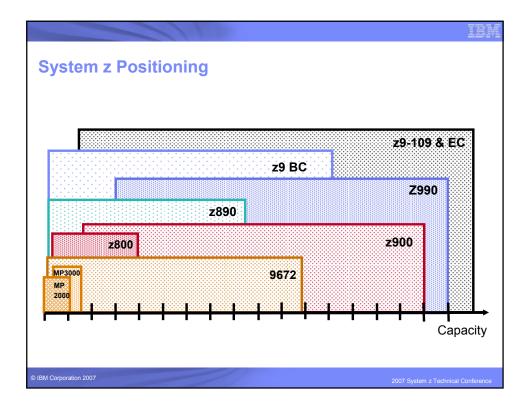


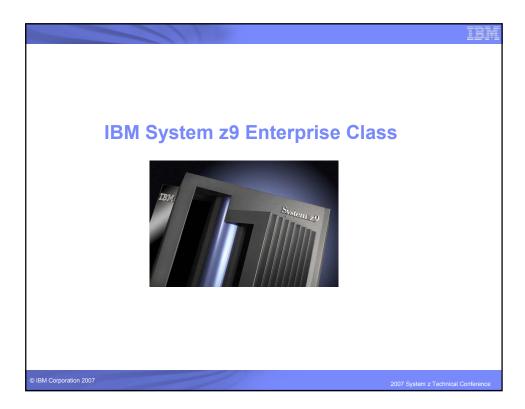




IBM System z Family	IBM eServer zSeries 990 – z990 (2084)	IBM eServer zSeries 890 – 2890 (2086)	IBM System z9 (29-109) (2094)
IBM eServer zSeries 800 – z800 (2066)		890 - z890 (2086)	
Announced 1000 – first 64-bit 254ries     42 models – Up to 16-way     *Specialty Engines     *CP, IFL, ICF     *On Damand Capabilities     *CUOD, CIU, CBU     *Memory – up to 32GB     *Channels     *Up to 256 ESCON channels     *Up to 256 ESCON channels     *Up to 256 ESCON channels     *Grypto caprocessors, accelerators     accelerators     *Crypto caprocessors, accelerators     *Parallel Sysplex clustering     *HiperSockets – up to 4 Supto X-     *Grypto caprocessors, accelerators     *Grypto caprocessors, accelerators     *Parallel Sysplex clustering     *HiperSockets – up to 4 Supto X-     *Grypto caprocessors, accelerators     *Grypto caprocessors, accelerators     *Grypto caprocessors, accelerators     *Grypto caprocessors     *Grypto caprocessors	nnounced 5/03 – first zSeries uperscalar Server models – Up to 32-way pocialty Engines - CP, FL, ICF, zAAP Demand Capabilities - CUdD, CLU, CBU, On/Off CoD emory – up to 256 GB hannels - Four LCSSs - Up to 246 FICON Express channels - TotenzRing, GbE, 1000BASE-T Ethernet - TokenzRing, GbE, 1000BASE-T Ethernet - Coupling Links rypto Express arailel Syspilox clustering liperSockets – up to 16 p to 30 logical partitions	<ul> <li>Announced 4/04 – 2Series Superscalar Server for mid market</li> <li>1 model – Up to 4-way</li> <li>28 capacity settings</li> <li>Spocialty Engines</li> <li>CP, IFL, ICF, ZAAP</li> <li>OD Demand Capabilities</li> <li>CUo, CUU, CBU, On/Off CoD</li> <li>Memory – up to 32 GB</li> <li>Channel</li> <li>Up to 420 ESCON channels</li> <li>Up to 420 ESCON channels</li> <li>Up to 420 ESCON channels</li> <li>Chydiagters (ISCA)</li> <li>Coupling Links</li> <li>Cryptographic Coprocessors</li> <li>Parallel Sysplox clustering</li> <li>HiperSockets – up to 18</li> <li>Up to 30 artitions</li> </ul>	<ul> <li>Announced 7/85, Superscalar Served</li> <li>Smedies – Up to 54-way</li> <li>Specialty Engines</li> <li>Co-Parand Capabilities</li> <li>Cuob, CiU, CBU, OnVoff CoD</li> <li>Wemory – up to 512 GB</li> <li>Other LCSS</li> <li>Mutbyle Subchannel Sets</li> <li>MDAW facility</li> <li>83.75 subchannels</li> <li>Up to 1024 ESCON channels</li> <li>10 GbE, GbE, 1000BASE-T</li> <li>Condjurable Crypto Express2</li> <li>Parallel Sysplex clustering</li> <li>Hiper Sockets – up to 16</li> <li>Up to 182 Filoxi Anamels</li> <li>Di GbE, GbE, 1000BASE-T</li> <li>Barlanel Sysplex clustering</li> <li>Hiper Sockets – up to 16</li> <li>Up to 69 partitions</li> <li>Enhanced Availability</li> <li>Operating Systems</li> </ul>
TPF, z/TPF, Linux on zSeries	perating Systems ► z/OS, z/VM, VSE/ESA, z/VSE, TPF, z/TPF, Linux on zSeries	<ul> <li>Operating Systems</li> <li>z/OS, z/VM, VSE/ESA, z/VSE, TPF, z/TPF, Linux on zSeries</li> </ul>	<ul> <li>z/OS, z/VM, VSE/ESA, z/VSE, TPF, z/TPF, Linux on System z</li> </ul>

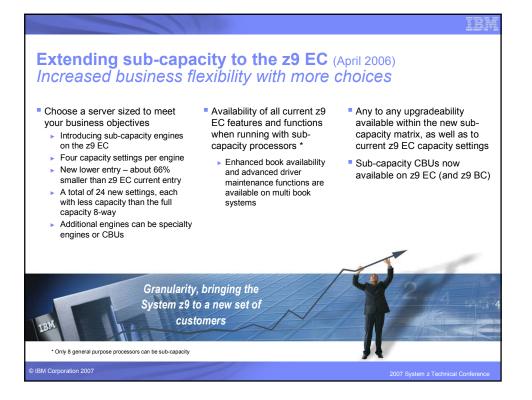


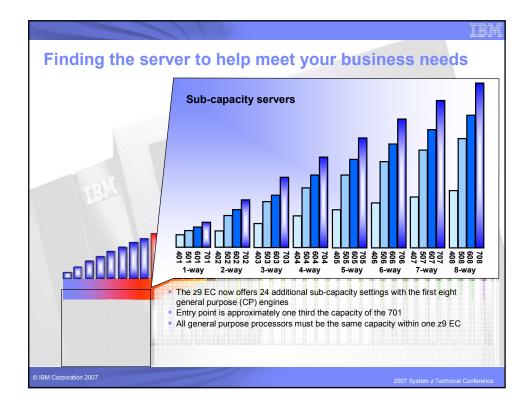


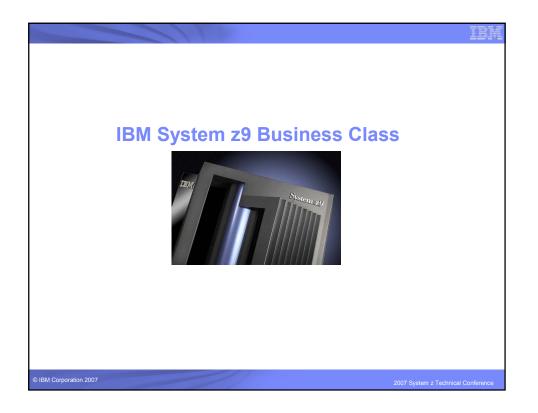


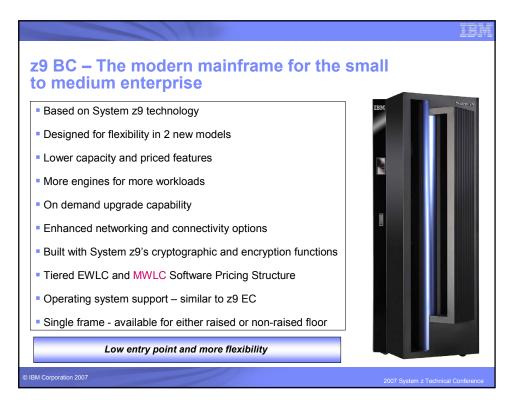


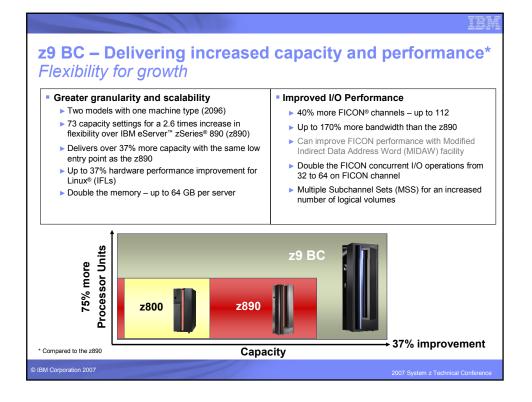
er indi	– 2094 – five cates PUs a	e models – SO		, S38, and S	54		
	cates PUs a	vailable for ab					
				ion			
		APs) per bool	ĸ				
are mo	dels						
				umber of PU c	haracterized as CF	s in the CEC	
xceeds (	08, then all CP	engines are full c	apacity				
MCMs	Available PUs	Max Available Sub-capacity CP PUs	Standard SAPs	Standard Spares	CP/IFL/ ICF/zAAP/zIIP	Max Memory	Max Channels
1	12	8	2	2	8	128 GB	960 **
2	24	8	4	2	18	256 GB	1024
3	36	8	6	2	28	384 GB	1024
-							
4	48	8	8	2	38	512 GB	1024
	are mo 0 408, 56 e n = the kceeds ( MCMs 1 2	n = the capacity setting       ceeds 08, then all CP       MCMs     Available PUs       1     12       2     24	Are models       0 408, 501 to 508, 601 to 608 and 701 to n = the capacity setting of the engine, a exceeds 08, then all CP engines are full of MCMs       Available PUs     Max Available Sub-capacity CP PUs       1     12       2     24	Max     Max     Standard       MCMs     Available PUs     Max     Standard       1     12     8     2       2     24     8     4	Max       0 408, 501 to 508, 601 to 608 and 701 to 754       n = the capacity setting of the engine, and xx = the number of PU cocceeds 08, then all CP engines are full capacity       MCMs     Available PUs     Max Available Sub-capacity     Standard SAPs     Standard Spares       1     12     8     2     2       2     24     8     4     2	Are models       0 408, 501 to 508, 601 to 608 and 701 to 754       n = the capacity setting of the engine, and xx = the number of PU characterized as CF exceeds 08, then all CP engines are full capacity       MCMs     Available PUs     Max Available Sub-capacity CP PUs     Standard SAPs     Standard Spares     CP/IFL/ ICF/zAAP/zIIP       1     12     8     2     2     8       2     24     8     4     2     18	Are models       0 408, 501 to 508, 601 to 608 and 701 to 754       n = the capacity setting of the engine, and xx = the number of PU characterized as CPs in the CEC exceeds 08, then all CP engines are full capacity       MCMs     Available PUS     Max Available Sub-capacity CP PUS     Standard SAPs     Standard Spares     CP/IFL/ ICF/zAAP/zIIP     Max Memory       1     12     8     2     2     8     128 GB       2     24     8     4     2     18     256 GB

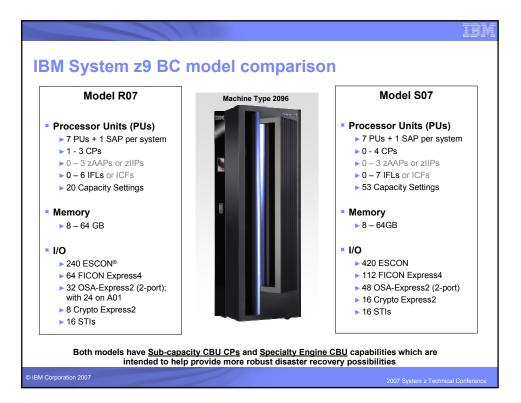


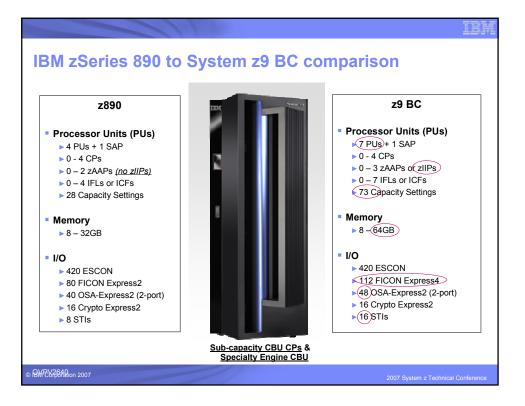




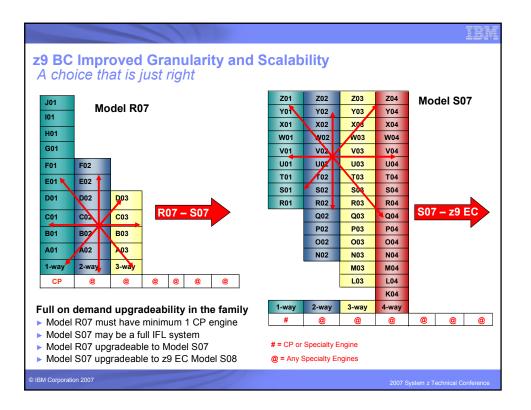


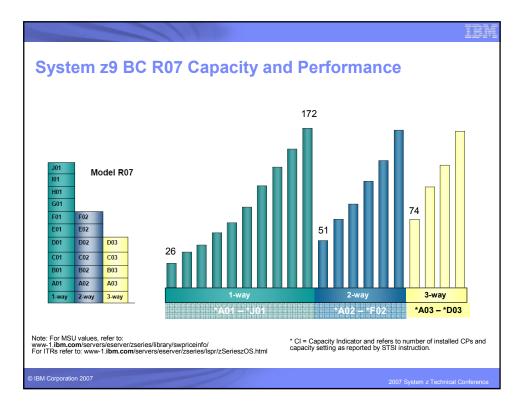


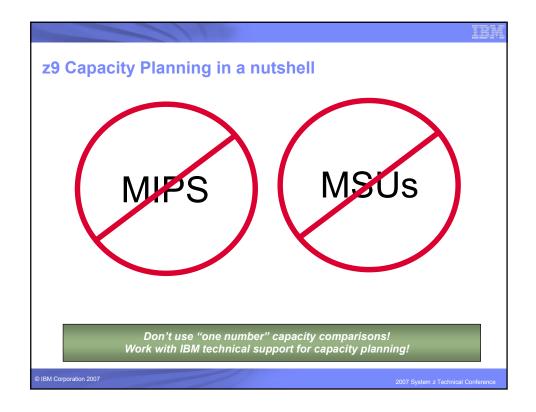




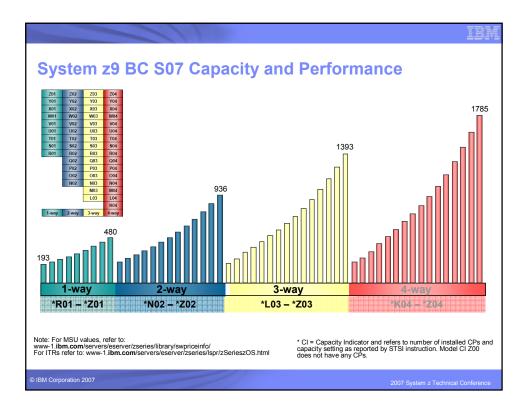
One m	achine t	ype – 2096 -	- two hardwar	e models, <u>I</u>	R07 and S0	<u>17</u>		
			Us available fo sors (SAPs) p		rization			
z9 BC	software	models						
► nxx	k, where r	n = subcapad	city engine size		umber of C	Ps		
			to J and xx =					
	iniouci O							
		pacity Indicat	ors for softward	e models				
		pacity Indicat	ors for softwar	e models				
		pacity Indicat	ors for softwar	e models	Standard Spares	CP/IFL/ ICF/zAAP/zIIP ****	Max Memory	Max Channels
•	20 for M	pacity Indicat odel R07 and Available	ors for softward d 53 for Model Max Available Subcapacity	e models S07 Standard		ICF/zAAP/zIIP		





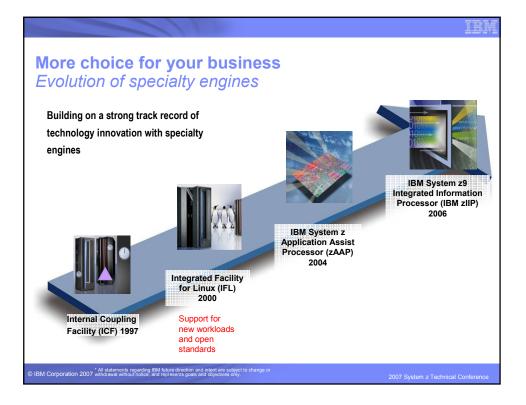


Features	Minimum # of features	Maximum # of features	Maximum connections	Increments per feature	Purchase increments
16-port ESCON	0 (1)	16	240 channels	16 channels 1 reserved as spare	4 channels
FICON Express4**	0 (1)	16	64 channels**	4 channels** 2 channels**	4 channels** 2 channels**
FICON Express2*	0 (1)	16	64 channels	4 channels	4 channels
FICON Express*	0 (1)	16	32 channels	2 channels	2 channels
OSA-Express2	0	16	32 ports (6)	2 or 1 (10 GbE has 1)	2 ports/1 port
OSA-Express*	0	16	32 ports (6)	2 ports	2 ports
Crypto Express2	0	4	8 PCI-X adapters	2 PCI-X adapters 1 PCI-X adapter <sup>(5)</sup>	2 PCI-X adapters <sup>(5)</sup> 1 PCI-X adapter <sup>(5)</sup>

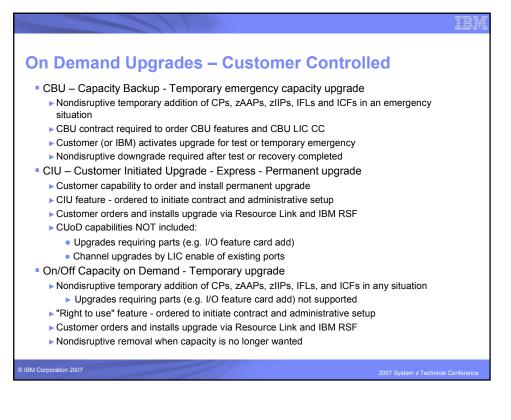


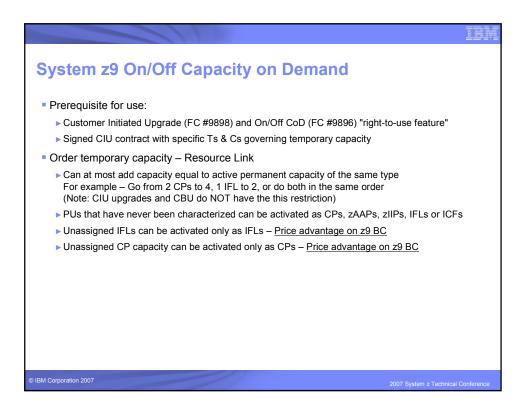
z9 BC Moo	del S07	I/O Fea	tures		
Features	Minimum # of features	Maximum # of features	Maximum connections	Increments per feature	Purchase increments
16-port ESCON	0 (1)	28	420 channels	16 channels 1 reserved as a spare	4 channels
FICON Express4**	0 (1)	28	112 channels**	4 channels** 2 channels **	4 channels** 2 channels**
FICON Express2*	0 (1)	20	80 channels	4 channels	4 channels
FICON Express*	0 (1)	20	40 channels	2 channels	2 channels
OSA-Express2	0	24	48 ports	2 or 1 (10 GbE has 1)	2 ports/1 port
OSA-Express*	0	20	40 ports	2 ports	2 ports
Crypto Express2	0	8	16 PCI-X adapters	2 PCI-X adapters 1 PCI-X adapter	2 PCI-X adapters ( 1 PCI-X adapter (5)

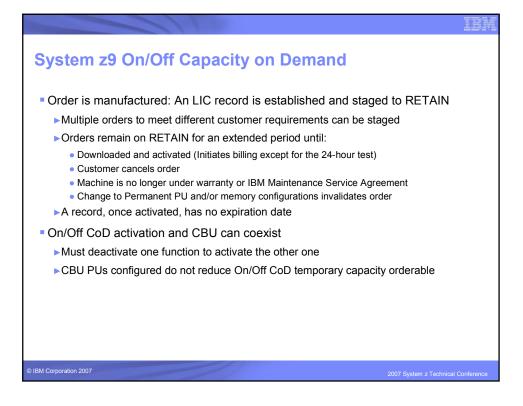
Minimum of one I/O feature (ESCON, FICON) or one Coupling Link (ICB, ISC-3) required.
 Each STI-3 distribution card occupies one I/O slot (supports ICB-3s).
 Maximum number of Coupling Links combined (ICs, ICB-3s, ICB-4s, and active ISC-3 links) cannot exceed 64 per server.
 ICB-4s are not included in the maximum feature count for I/O slots but are included in the CHPID count.
 Initial order of Crypto Express2 is two features. Each PCI-X adapter can be configured as either a coprocessor or an accelerator. Crypto Express2-1P has one PCI-X adapter.
 Carry forward on an upgrade only.
 FICON Express4-2C 4KM LX and SX have two channels per feature



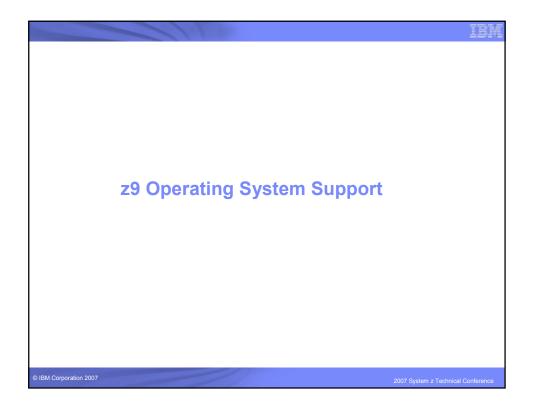
The type of Processor Units (PUs) that can be ordered on Syste	am 79.
<ul> <li>Central Processor (CP)</li> </ul>	
<ul> <li>Provides processing capacity for z/Architecture<sup>™</sup> and ES.</li> <li>Runs z/VSE, VSE/ESA, Linux for System z, z/OS, z/VM,</li> </ul>	
Integrated Facility for Linux (IFL)	
<ul> <li>Provides additional processing capacity for Linux workloa</li> </ul>	
Runs z/VM (with Linux for System z guests) or Linux for S	System z
► IBM System z Application Assist Processor (zAAP)	
• Under z/OS, the Java Virtual Machine (JVM) assists with	Java processing to a ZAAP
<ul> <li>IBM System z9 Integrated Information Processor (zIIP)</li> <li>Provides processing capacity for selected workloads e.g. SRB mode</li> </ul>	, DB2 for z/OS V8 workloads executing in
Internal Coupling Facility (ICF)	
<ul> <li>Provides additional processing capacity for the execution (CFCC) in a CF LPAR</li> </ul>	of the Coupling Facility Control Code
Optional System Assist Processors (SAP)	
<ul> <li>SAP manages the start and ending of I/O operations for a</li> </ul>	all Logical Partitions and all attached I/O











## IE

## System z9 Supported Operating Systems

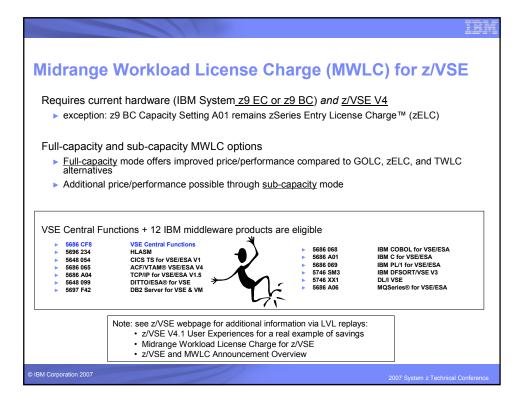
Operating System	ESA/390 (31-bit)	z/Architecture <sup>®</sup> (64-bit)
z/VSE <sup>™</sup> Version 3 Release 1 <sup>(3)</sup>	Yes	No
z/VSE Version 4 Release 1 <sup>(4)</sup>	No	Yes
$z/VM^{\ensuremath{\mathbb{R}}}$ Version 5 Release $1^{(2)},2$ and 3	No	Yes
Linux on System z, 64-bit distribution	No	Yes
Linux on System z, 31-bit distribution	Yes	No
z/OS Version 1 Release 9	No	Yes
z/OS.e <sup>(1)</sup> and z/OS Version 1 Releases 6, 7, 8	No	Yes
z/TPF Version 1 Release 1	No	Yes
TPF Version 4 Release 1 (ESA mode only)	Yes	No

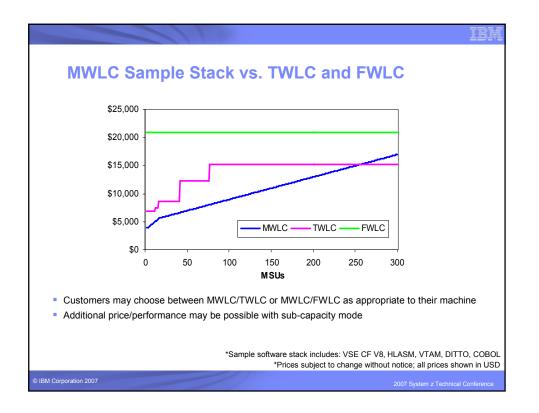
z/OS.e - z800, z890 and z9 BC only. Release 1.8 will be the last release of z/OS.e.
 Support for z/VM 5.1 will end September 30, 2007
 z/VSE v3. 31-bit mode only. It does not implement z/Architecture, and specifically does not implement 64-bit mode capabilities. z/VSE is designed to exploit select features of IBM System z9 and zSeries hardware.
 z/VSE V4 is designed to exploit 64 bit real memory addressing, but will not support 64-bit virtual memory addressing Note: Please refer to the latest PSP bucket for latest PTFs for new functions/features.

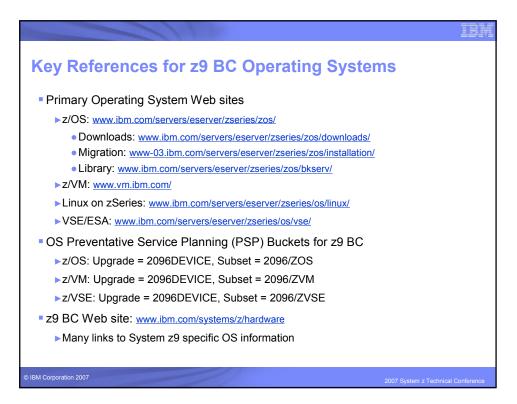
Functions	z/VSE V4.1	z/VSE V3.1 (Note 1)
z/Architecture mode only	Yes	No
64-bit real addressing (up to 8 GB proc storage)	Yes	No
Fibre Channel Protocol (FCP) for SCSI Disks	Yes	Yes
CP Assist for Cryptographic Function (CPACF)	Yes	Yes
Crypto Express2 (SSL RSA encryption assist)	Yes	Yes
HiperSockets <sup>™</sup> (including spanned HiperSockets)	Yes	Yes
FICON Express2 <sup>™</sup> & FICON Express4 <sup>™</sup>	Yes	Yes
OSA Express2 (incl Gb, 10Gb, 1000 Base-T, OSN)	Yes	Yes
OSA Integrated Console Controller (OSA-ICC)	Yes	Yes
Up to 60 LPARs and 4 LCSSs	Yes	Yes

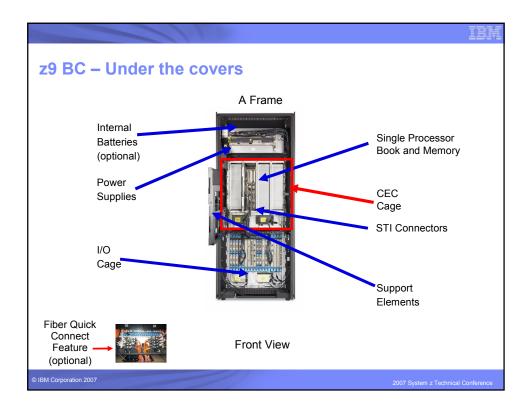
ted features of IBM Syste2027hard

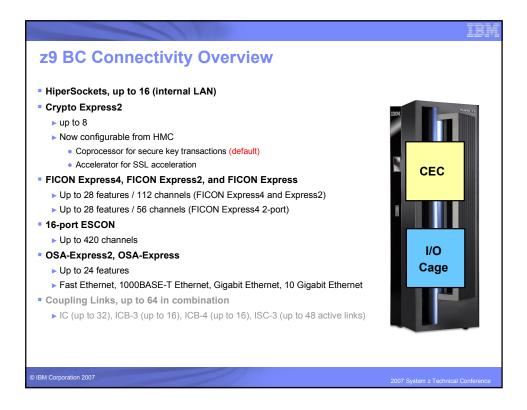
Note 1: z/VSE V3 can operate in 31-bit mode only. It does not in BM Corporation 2007 implement 64-bit mode capabilities. z/VSE V3 is designed to su

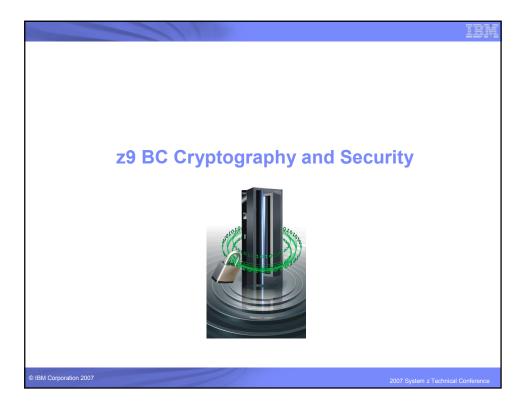


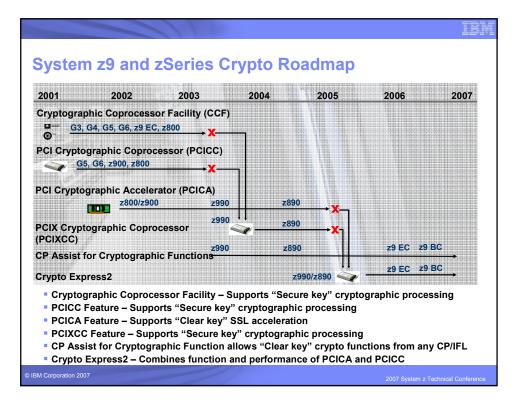


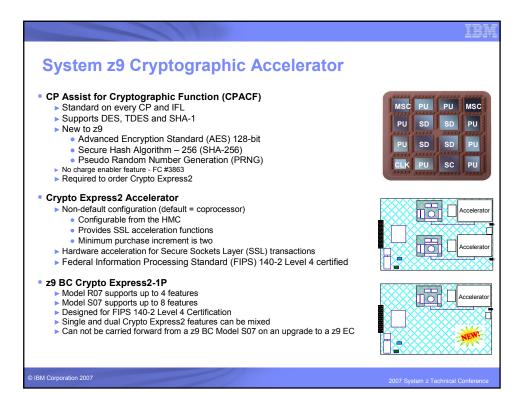




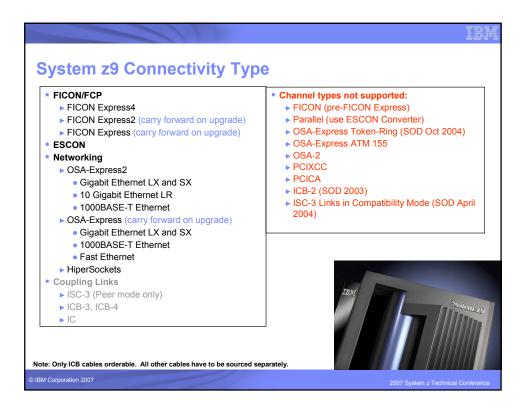


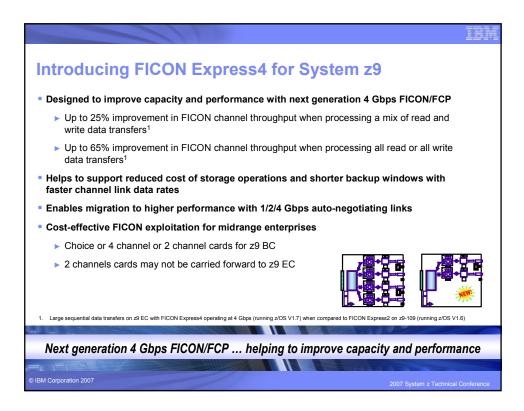


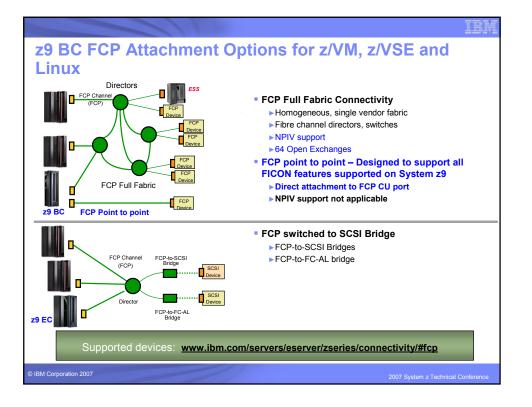


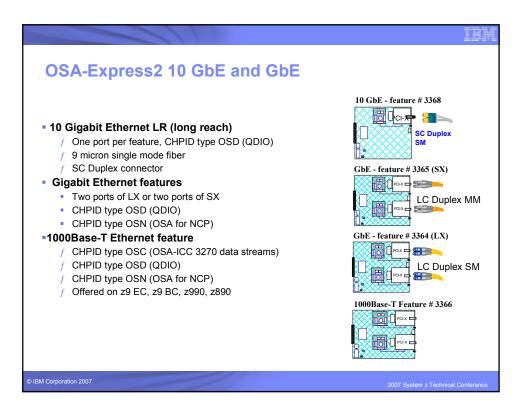




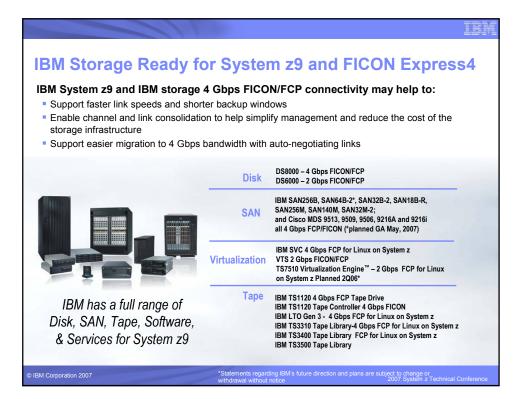


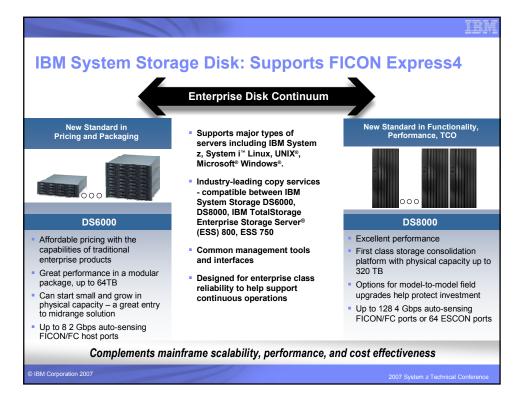


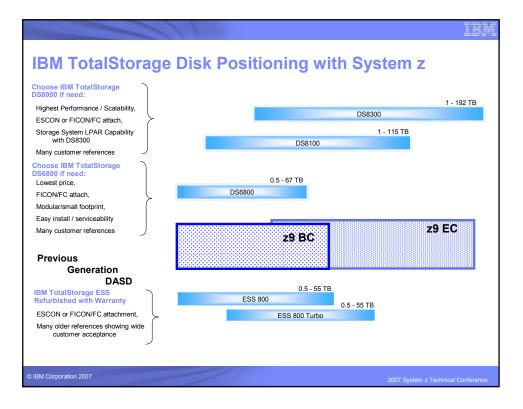












z/VSE s	support for	IBM Syster	n Storag	Je	
		New Standard in Pricing and Packaging		New Standard in Functionality, Performance, TCO	
	IBI	M TotalStorage DS6000	ESS 750 / 800	IBM TotalStorage DS8000	
	IBM System Storage	DS6000	ESS 750, 800, 800Turbo	DS8000. DS8000 Turbo	
	ESCON	Not Avail	Yes	Yes	
	200011				
	FICON	Yes	Yes	Yes	

