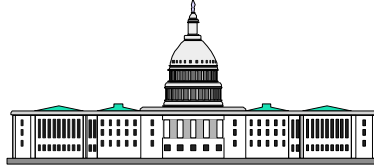


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October 6-10, 2000

S/390 Enterprise Server Hardware Update



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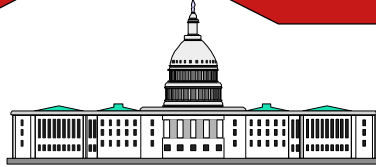
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eserver zSeries 900 Processor



Expanding addressing from 2 Gigabytes to 16 Exabytes

zSeries 900 Processor



★ New level of performance

- G6 CP + 25%
- G6 ZZ7 + 50%
- Balanced Structure

★ New 64-bit Architecture

- 64-bit Addressing (Real and Virtual)
- 64-bit Arithmetic/Logical
- 64-bit I/O architecture
- 64-bit SIE architecture
- 64-bit Cryptography
- 64-bit QDIO

★ Integrated Availability

- Transparent CP, SAP, ICF, IFL Sparing
- Memory sparing
- ESCON sparing
- New I/O - hot plug, hot swap
- Dual Power Service and Control Net
- SE Auto-switch

★ Non Disruptive Growth

- 26 Servers and a CF Model
- CUoD and CBU
- SOD: Memory Upgrade



★ IRD (Intelligent Resource Director)

- Dynamically balance system resources
- Integration of
 - PR/SM - CPU Management, I/O Priority
 - CSS - I/O Priority Queueing, DCM
 - Parallel Sysplex
 - Workload Manager

★ Workload Pricing

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Highlights



★ Introducing new I/O Subsystem

- 3X I/O Bandwidth
- New I/O Cage - 28 slots
- CHPID Mapping
- 16 port ESCON card, sparing
- 2 port FICON card, 96 FICON channels
- 2 port Gigabit Ethernet cards
- 2 port Fast Ethernet cards
- 2 port ATM 155 cards
- 4 port ISC-3 card
- Dynamic Channel Path Management (DCM)
- 1 GB STI



★ Sysplex Enhancements

- Model 100 CF
- ISC-3 (Internal System Channel) at 2 Gbits/s
- ICB-3 (Integrated Cluster Bus) at 1 GByte/s
- IC-3 (Internal Coupling Channel)
- Peer Coupling Link Mode
- 2 standard Sysplex Timer Cards

★ Enhanced Security Leadership

- Cryptographic Coprocessors
- Up to 16 PCICC processors
- ~2,000 SSL transactions/second

★ Investment Protection

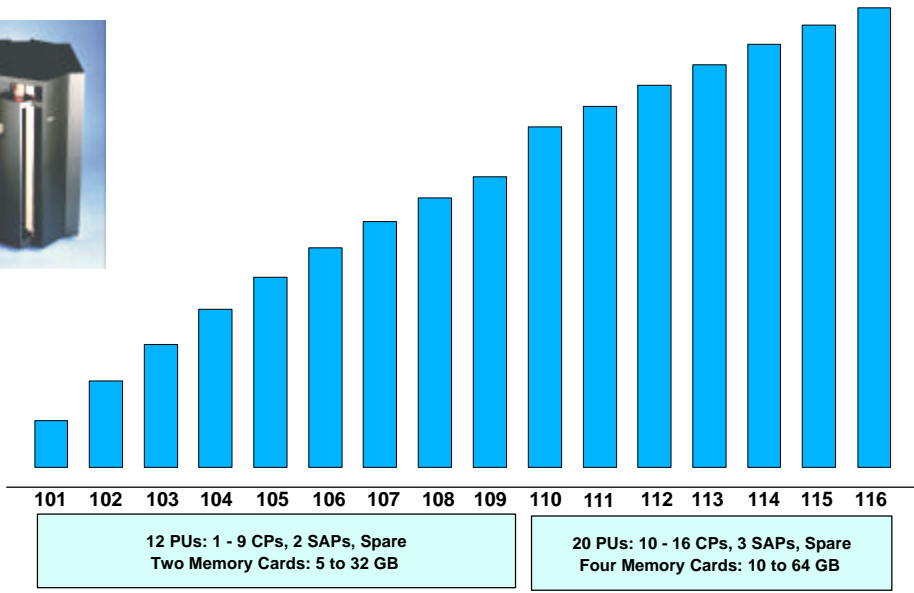
- G5/6 MES Upgrades
- Infrastructure reuse

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Models 101 - 116

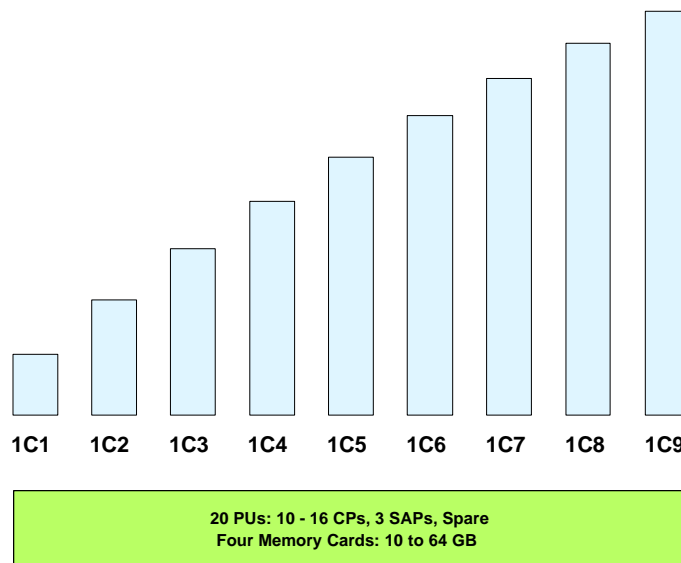


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Capacity Models 1C1 - 1C9



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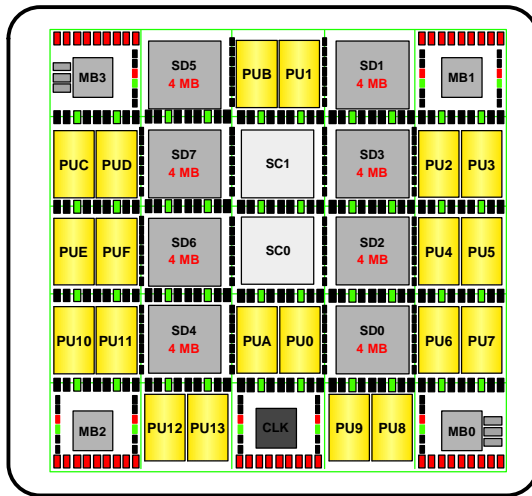
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Multichip Module (MCM)



Models 110 - 116 and 1C1 - 1C9



20 PU MCM

Technology Excellence

★ CMOS8S Technology

- Copper interconnections

★ 35 Chips

- 30 chips CMOS8S

★ MCM Packaging

- 127.5 mm x 127.5 mm
- Over 2.5 billion transistors
- 20 Processor Units (PUs)
 - 17.9 mm x 9.9 mm
 - 47 million transistors
 - L1 cache/CP
 - 256 KB I-cache
 - 256 KB D-cache
 - 1.3 ns Cycle Time
- 8 System Data (SD) cache chips
 - L2 cache
 - 234 million transistors
 - 4 MB/chip
 - 2 x16 MB/cluster per MCM
- 2 Storage Control (SC) chips
- 4 Memory Bus Adapter (MBA) chips
- 1 Clock (CLK) chip
- 101 Glass ceramic and 6 Thin Film layers
 - 1 km of wire

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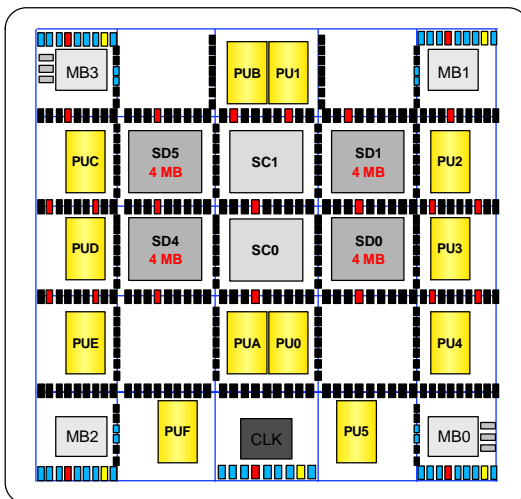
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Multichip Module (MCM)



Models 100, 101 - 109



12 PU MCM

Technology Excellence

★ CMOS8S Technology

- Copper interconnections

★ 23 Chips

- 18 chips CMOS8S

★ MCM Packaging

- 127.5 mm x 127.5 mm
- Over 1.5 billion transistors
- 12 Processor Units (PUs)
 - 17.9 mm x 9.9 mm
 - 47 million transistors
 - L1 cache/CP
 - 256 KB I-cache
 - 256 KB D-cache
 - 1.3 ns Cycle Time
- 4 System Data (SD) cache chips
 - L2 cache
 - 234 million transistors
 - 4 MB/chip
 - 2 x8 MB/cluster per MCM
- 2 Storage Control (SC) chips
- 4 Memory Bus Adapter (MBA) chips
- 1 Clock (CLK) chip
- 45 Alumina ceramic and 6 Thin Film layers
 - 0.4 km of wire

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Processor Features



- One spare is required
- CF LP: ICFs and/or CPs, dedicated and/or shared, CFCC only
- Linux LP: IFLs only, dedicated or shared, Linux or VIF only
- OS LP: CPs only, dedicated or shared
- Optional SAPs are almost never needed

	PU's	CP's	SAP's Standard	SAP's Opt. (up to)	IFL's/ICF's (up to)
101	12	1	2	3	8
102	12	2	2	3	7
103	12	3	2	3	6
104	12	4	2	3	5
105	12	5	2	3	4
106	12	6	2	3	3
107	12	7	2	2	2
108	12	8	2	1	1
109	12	9	2	0	0
1C1	20	1	3	5	15
1C2	20	2	3	5	14
1C3	20	3	3	5	13
1C4	20	4	3	5	12
1C5	20	5	3	5	11
1C6	20	6	3	5	10
1C7	20	7	3	5	9
1C8	20	8	3	5	8
1C9	20	9	3	5	7
110	20	10	3	5	6
111	20	11	3	5	5
112	20	12	3	4	4
113	20	13	3	3	3
114	20	14	3	2	2
115	20	15	3	1	1
116	20	16	3	0	0
100	12	0	2	0	9

IFL=Integrated Facility for Linux

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Memory Cards, Storage Sizes



- Memory Cards:
4, 8, or 16 GB
- LIC CC storage control:
"Dial-a-Gig"
- Cards Used:
 - ▶ 2 - Model 100 CF
 - ▶ 2 - Models 101 - 109
 - ▶ 4 - Models 110 - 116
 - ▶ 4 - Models 1C1 - 1C9
- SOD: Concurrent upgrade within card capacity

Storage	Models 100 - 109	Models 110 - 116	Models 1C1 - 1C9
5 GB	2 x 4 GB		
6 GB	2 x 4 GB		
7 GB	2 x 4 GB		
8 GB	2 x 4 GB		
10 GB	2 x 8 GB	4 x 4 GB	4 x 4 GB
12 GB	2 x 8 GB	4 x 4 GB	4 x 4 GB
14 GB	2 x 8 GB	4 x 4 GB	4 x 4 GB
16 GB	2 x 8 GB	4 x 4 GB	4 x 4 GB
18 GB	2 x 16 GB	4 x 8 GB	4 x 8 GB
20 GB	2 x 16 GB	4 x 8 GB	4 x 8 GB
24GB	2 x 16 GB	4 x 8 GB	4 x 8 GB
28 GB	2 x 16 GB	4 x 8 GB	4 x 8 GB
32 GB	2 x 16 GB	4 x 8 GB	4 x 8 GB
40 GB		4 x 16 GB	4 x 16 GB
48 GB		4 x 16 GB	4 x 16 GB
56 GB		4 x 16 GB	4 x 16 GB
64 GB		4 x 16 GB	4 x 16 GB

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Software Requirements

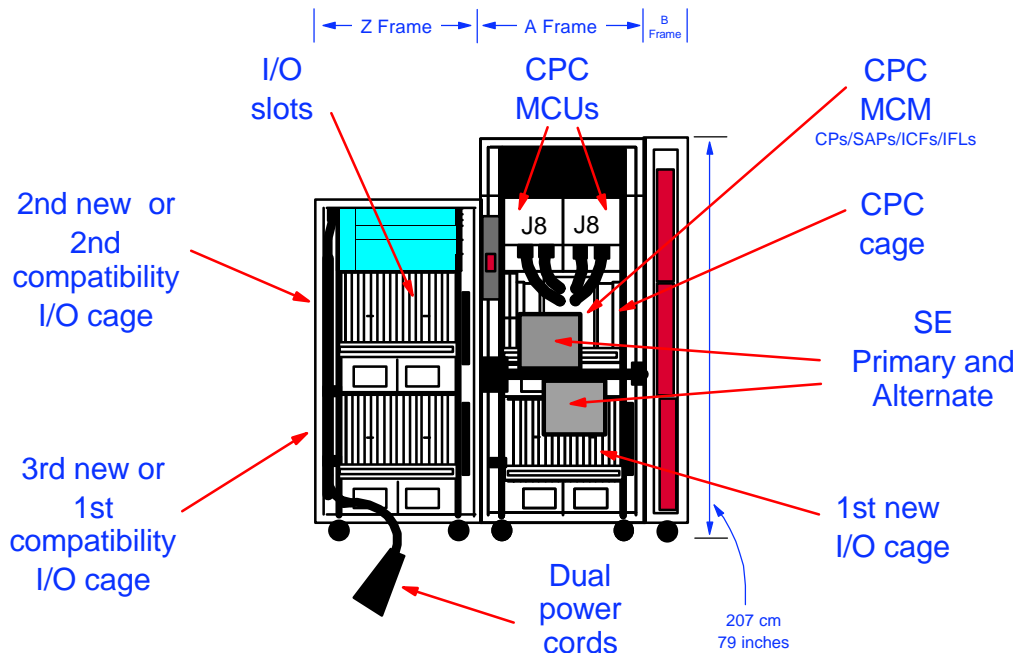


- z/OS
- OS/390 Version 2 Release 6
- VM
 - ▶ z/VM
 - ▶ VM/ESA Version 2 Release 4
 - ▶ VM/ESA Version 2 Release 3
 - ▶ VM/ESA Version 2 Release 2
- VSE
 - ▶ VSE/ESA Version 2 Release 3 and higher
- Transaction Processing Facility (TPF)
 - ▶ TPF Version 4 Release 1

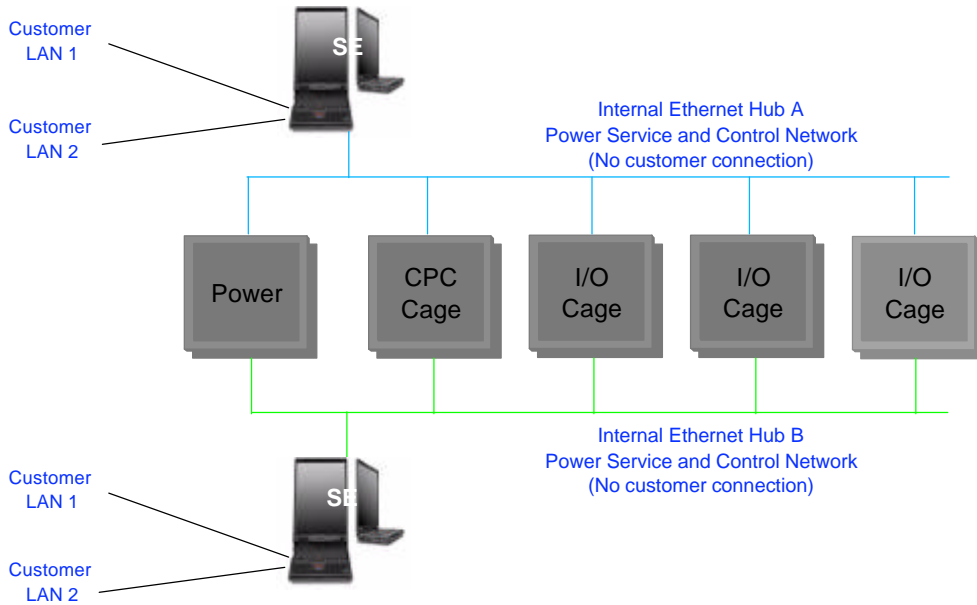
Notes:

- The zSeries 900 do not support S/370 mode operations
- The zSeries 900 do not support Version Codes

System Layout (CPC and I/O cages)



Power Service and Control Network

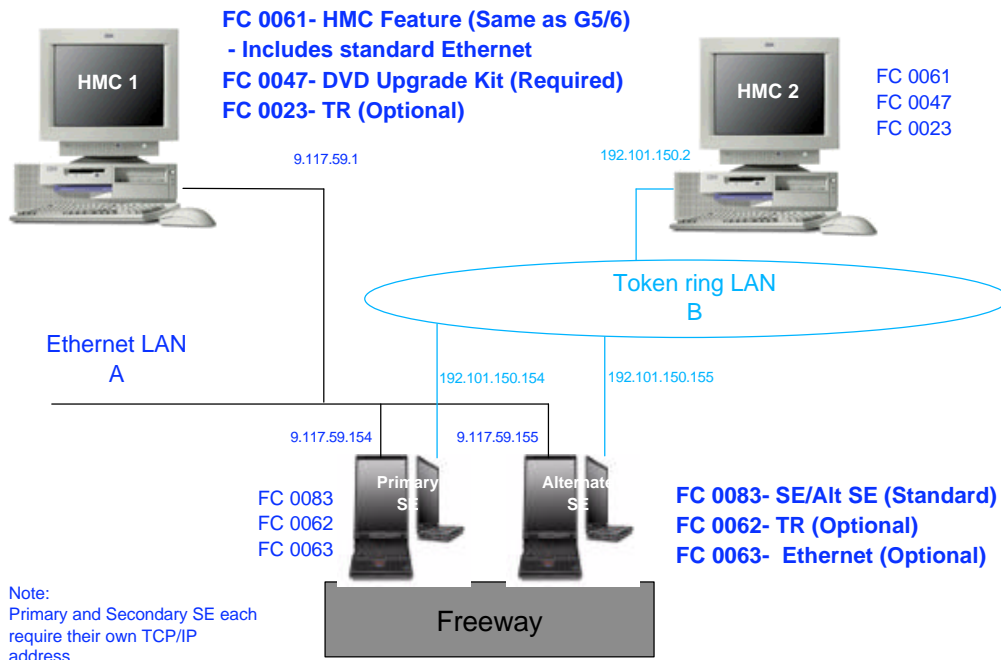


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HMC to SE TR and Ethernet

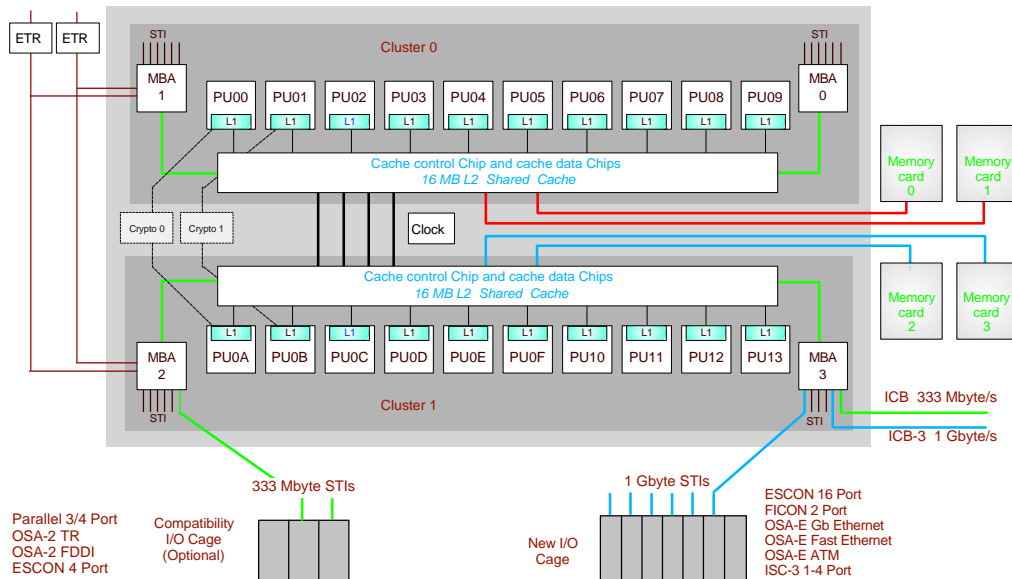


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System Structure (CP and I/O)



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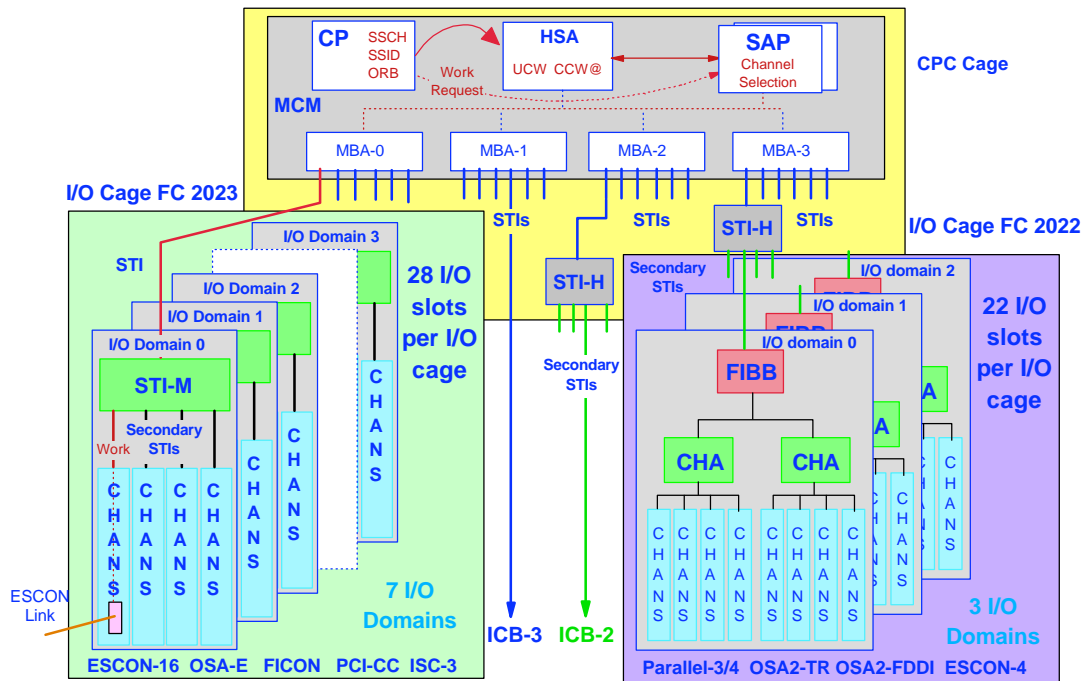
STIs



- The STI cables provide the connectivity for both internal CPC to channel I/O domains, as well as for external ICB links
- There are up to 24* 1 Gigabyte/second STI links
- One 1 Gigabyte STI link is required for each installed I/O domain in a new I/O cage (FC #2023), up to 7 per cage
- Up to 16 of the 24 1 Gigabyte STIs can be used for ICB-3 connections (uses the STIs directly from the rear of the MCM board)
- Some of the 1 Gigabyte STIs can be multiplexed down to 333MB STI connections to support:
 - ▶ The legacy I/O cages (FC #2022) if installed
 - ▶ ICB connections to G5/G6 processors

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Channel Subsystem I/O Operation

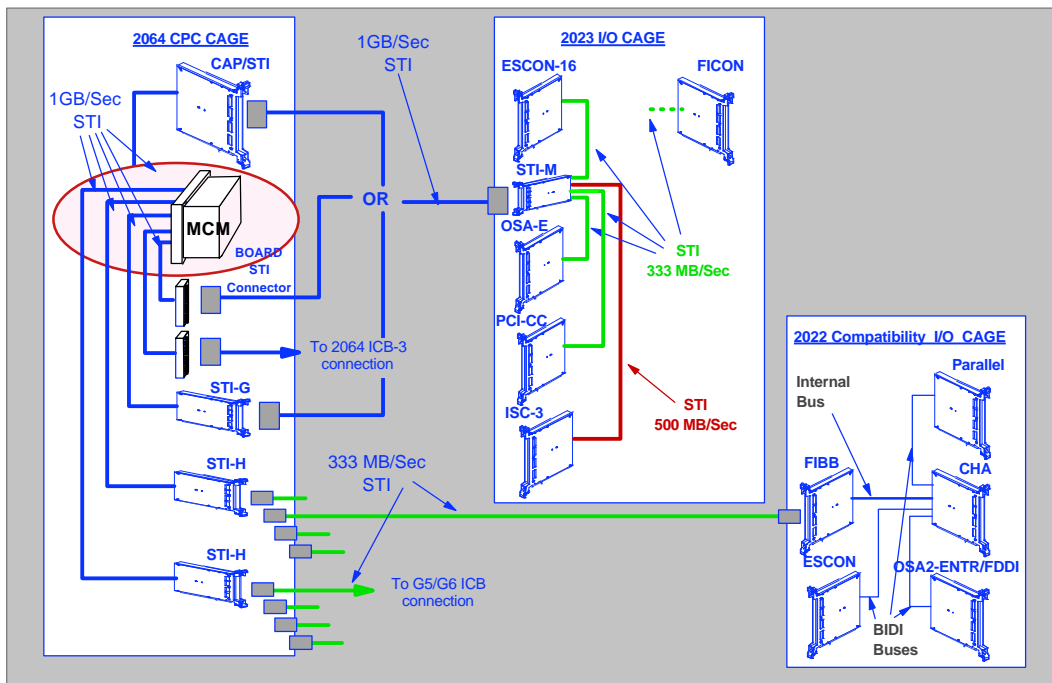


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Channel Connections - STIs



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Channel card plugging



- Channel cards in a new style I/O cage (FC 2023) are spread as evenly as possible across I/O domains in the I/O cage
- As the number domains increases to the maximum in a new style I/O cage, and more channels are required to be installed, another new style I/O cage is added to the configuration and the required channel cards are spread in domains in both I/O cages (new machine build process only)
- Domains (STI's) do NOT have affinity to an SAP. Channel cards are assigned SAP affinity at POR or "hot plug" activation. Assign paths to CUs to achieve maximum availability. SAP affinity should not be considered.

New I/O Cage(FC2023) Domain Plugging

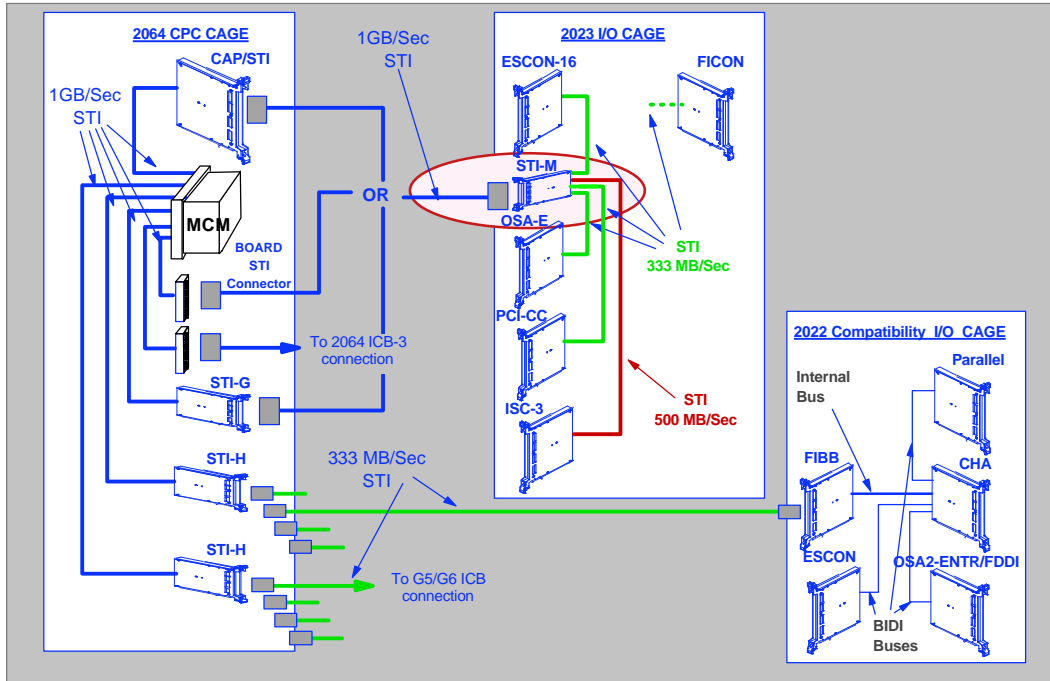


Number of Cards	Total Domains Plugged	2023 cage # 1 (bottom of A frame)	2023 cage # 2 (top of Z frame)	2023 cage # 3 (bottom of Z frame)
1 or 2 or 3	1 or 2 or 3	0 - 1 - 2		
4 - 16	4	0,1,2,3		
17 - 24	6	0,1,2,3,4,5		
25 - 28	7	0,1,2,3,4,5,6		
29 -32	8	0,1,2,3	0,1,2,3	
33 - 40	10	0,1,2,3,4	0,1,2,3,4	
41 - 48	12	0,1,2,3,4,5	0,1,2,3,4,5	
49 - 56	14	0,1,2,3,4,5,6 (All)	0,1,2,3,4,5,6 (All)	
57 - 72	18	0,1,2,3,4,5	0,1,2,3,4,5	0,1,2,3,4,5
73 - 80	20	0,1,2,3,4,5,6 (All)	0,1,2,3,4,5,6 (All)	0,1,2,3,4,5
81 - 84	21	0,1,2,3,4,5,6 (All)	0,1,2,3,4,5,6 (All)	0,1,2,3,4,5,6 (All)

[This chart shows the new I/O cage \(FC 2023\) I/O domain plugging sequence for a new 2064.](#)

[The above information is a guide and can change. It is always recommended to review the econfig output for details of the actual channel card placement.](#)

Channel Connections - I/O Cage



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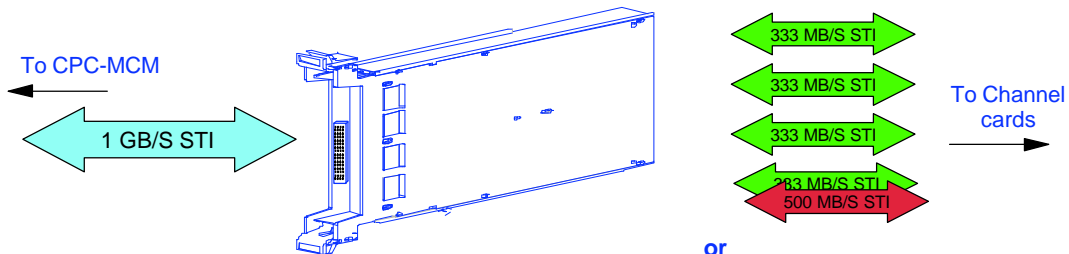
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STI-M Card



Four 333 MB/Sec STI internal board connections to ESCON-16, FICON, OSA-E or PCI-CC



- Installed in the new I/O cage in dedicated I/O slots (up to 7 in new I/O cage)
- Supports one I/O domain
- Half high card assembly
- Two cards per slot, one in the upper half and one in the lower half of the I/O slot
- Hotplug support

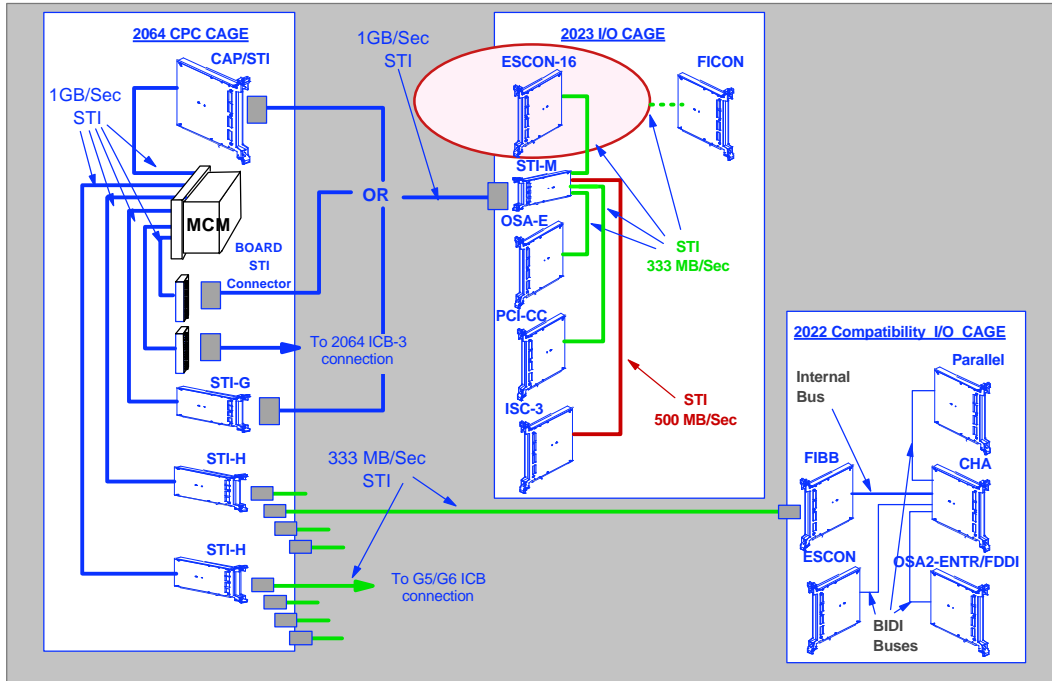
or
 Three (or two) 333 MB/Sec STI FC 2023 Internal Board connections to ESCON-16, FICON, OSA-E or PCI and
 One (or two) 500 MB/Sec STI FC 2023 Internal Board connection to ISC-3

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Channel Connections - ESCON

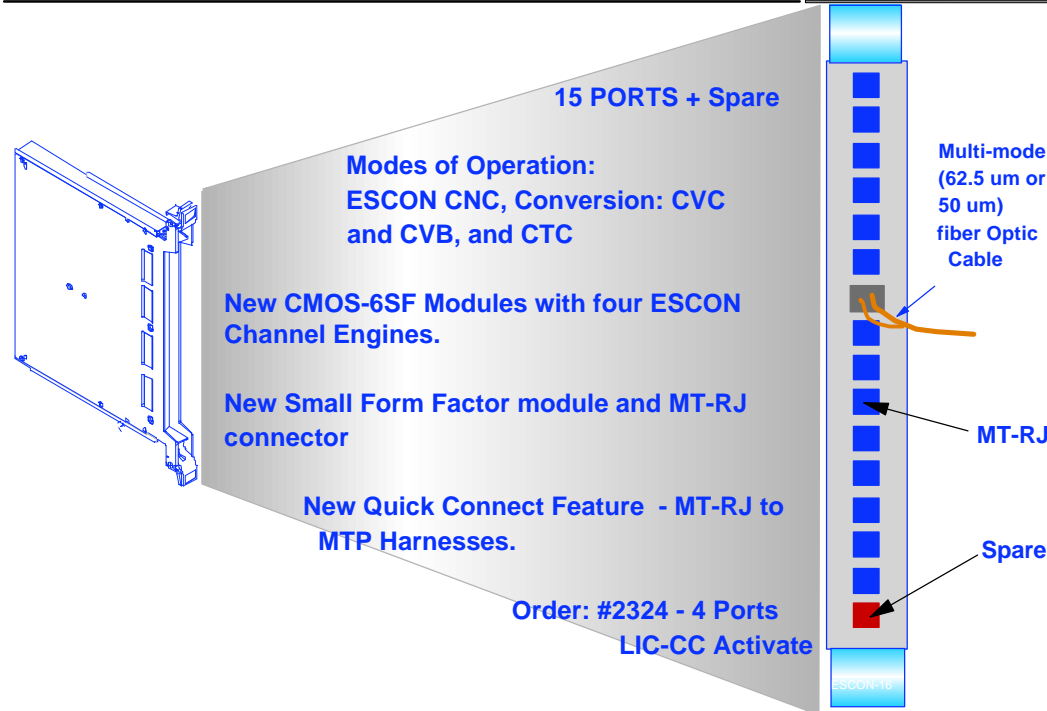


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16 Port ESCON Card - FC #2323



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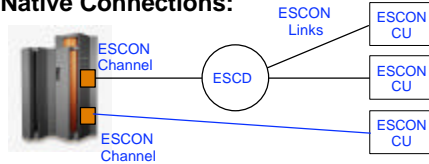
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ESCON Operating Modes



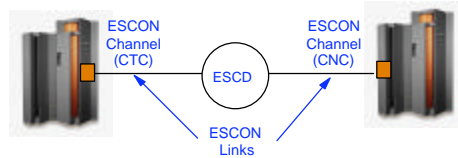
ESCON Native Connections:



- ★ *Native ESCON Control Units*
- ★ *Switched Point-to-point topology*
- ★ *Point-to-point topology*

Type=CNC

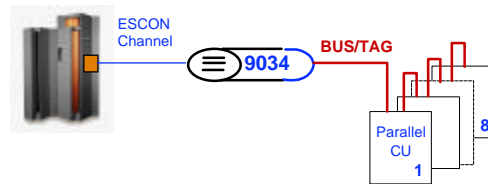
Channel-To-Channel Connections:



- ★ *Channel-to-Channel Adapter*

Type=CTC one end
Type=CNC other end

ESCON Conversion Connections:



- ★ *Exploit ESCON Channel with legacy Parallel Block Multiplex and Byte Multiplex control units*

Type=CVC
Type=CBY

ESCON Support



- On a new build, the required ESCON channel cards are spread across as many different STIs as possible for customer availability reasons
- 256 ESCON channels requires 18 ESCON channel cards (16 port channel cards) and all of these channel cards could be installed in one new style I/O cage
 - ▶ Each ESCON 16 port channel card has at least one spare channel. Except for the last spare channel all other ESCON channel ports are eligible for being LIC-CC enabled
 - ▶ The spare channel can be used in the case of an assigned channel that fails. The 2064 repair approach will request that a channel sparing action be invoked.
 - ▶ If a channel card fails after a previous sparing action and there are no spares left, the rule is disconnect all the fiber cables from the failing channel card, and after the card has been replaced return the cables to the same location that they were on the failing card
 - ▶ An MES action may require that an ESCON that has had multiple channel failures be replaced by the MES (no more LIC-CC channels on failed ESCON channel card)
 - ▶ Be aware that for 28 or less ESCON channels only 2 ESCON channel cards are installed.

ESCON Support



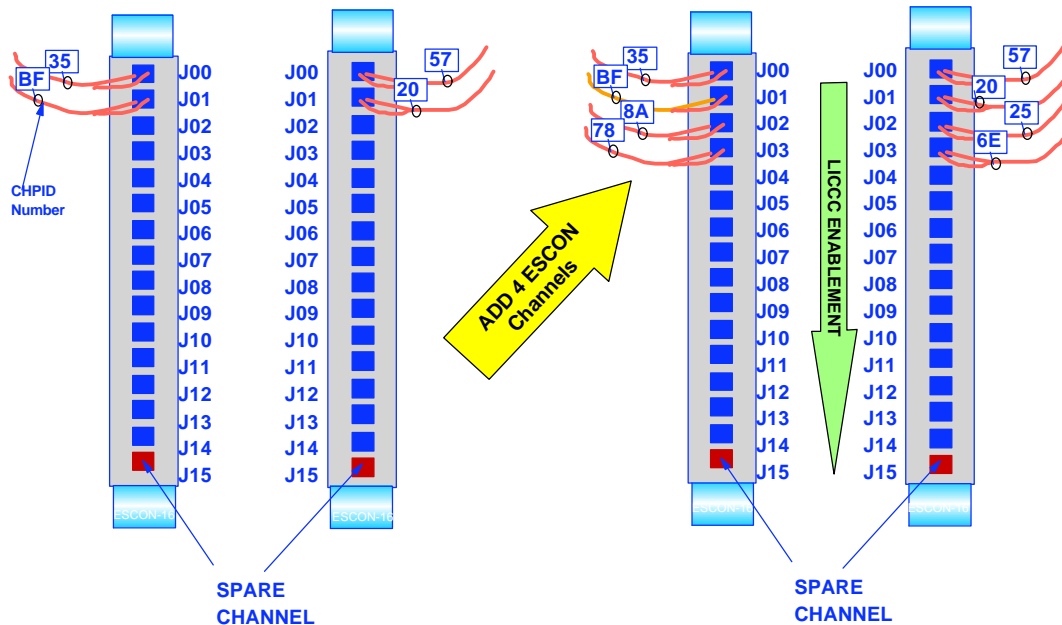
# ESCON Channels	# ESCON Cards	# Used ports per card	# Unused ports per card	# Reserved spare ports per card and total
4	2	2	13	1 / 2
8	2	4	11	1 / 2
16	2	8	7	1 / 2
28	2	14	1	1 / 2
32	4	8	7	1 / 4
60	4	15	0	1 / 4
64	6	11/11/11/11/10/10	4/4/4/4/5/5	1 / 6
68	6	12/12/11/11/11/11	3/3/4/4/4/4	1 / 6
76	6	13/13/13/13/12/12	2/2/2/2/3/3	1 / 6
88	6	15/15/15/15/14/14	0/0/0/0/1/1	1 / 6
92	8	12/12/12/12 11/11/11/11	3/3/3/3 4/4/4/4	1 / 8
120	8	15	0	1 / 8
124	10	13*4/12*6	2*4/3*6	1 / 10
148	10	15*8/14*2	0*8/1*2	1 / 10
152	12	13*8/12*4	2*8/3*4	1 / 12
180	12	15	0	1 / 12
184	14	14*2/13*12	1*2/2*12	1 / 14
208	14	15*12/14*2	0*12/1*2	1 / 14
212	16	14*4/13*12	1*4/2*12	1 / 16
240	16	15	0	1 / 16
244	18	14*10/13*8	1*10/2*8	1 / 18
256	18	15*4/14*14	0*4/1*14	1 / 18

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ESCON-16 LIC-CC Enabled

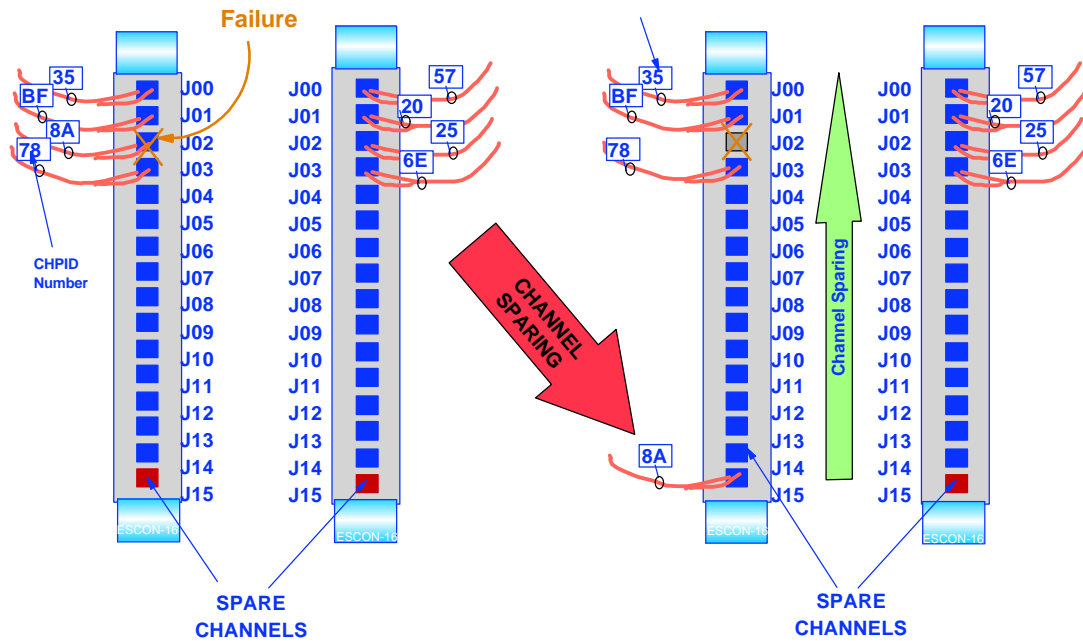


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ESCON-16 Channel Sparing

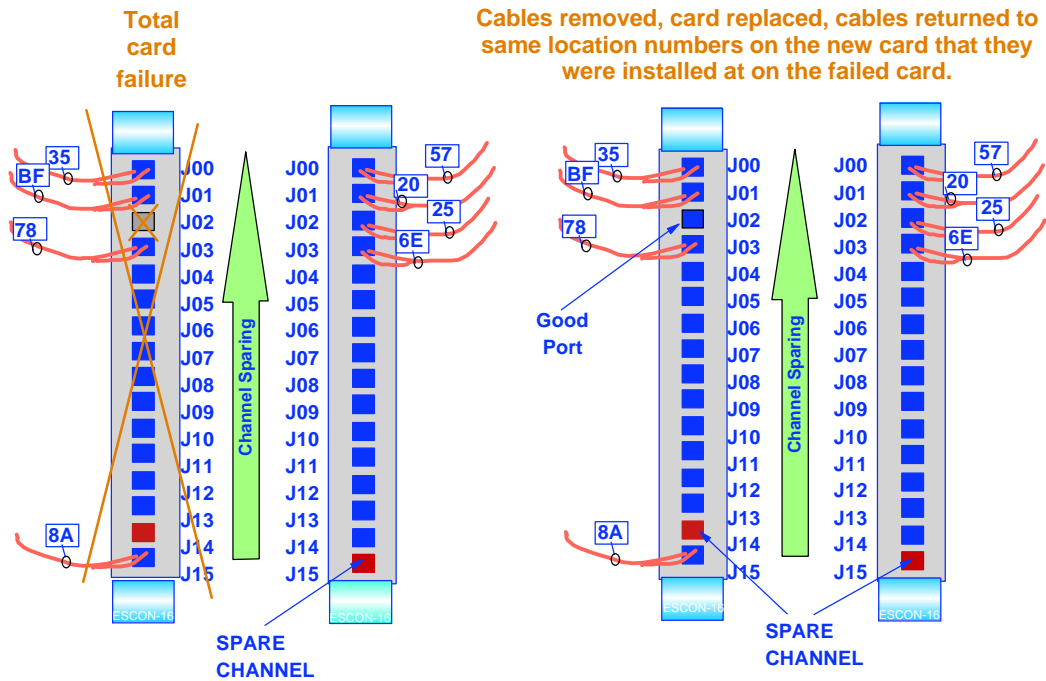


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ESCON-16 Card Failure

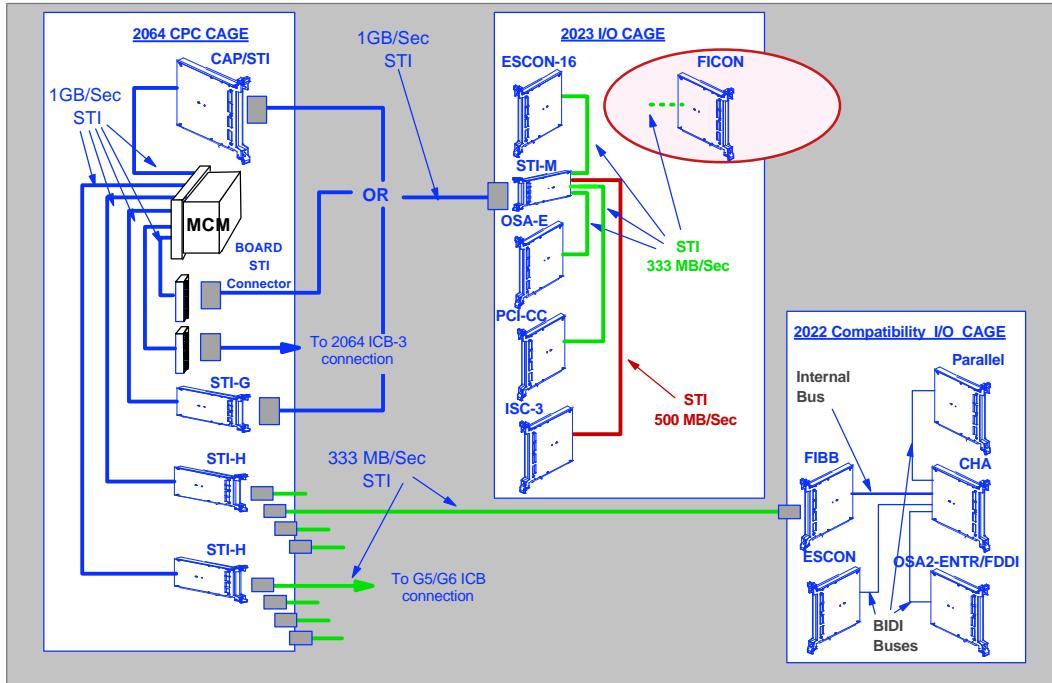


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Channel Connections - FICON



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FICON Channel Cards



Two Channels per Card

Two Channel Card types:

- LONG WAVE (LX) - #2315
- SHORT WAVE (SX) - #2318

Two Modes of Operation:

- FICON Bridge (FCV) - LX Only!
- Native Mode (FC)

Link Speed: One Gbit/Sec
Distance - LX:10 Km, 20 Km with RPQ; SX: ??? m

Up to 96 FICON channels
(32 maximum per new I/O cage)

SC-Duplex

E0 Single mode (9 um) or
D0

A0 B0 Multimode (62.5 um or 50 um)
C0 This also requires a MCP (mode conditioner patch cable)
A1 B1
C1

D1
E1

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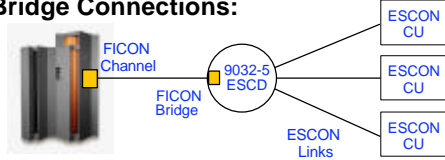
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FICON Operating modes



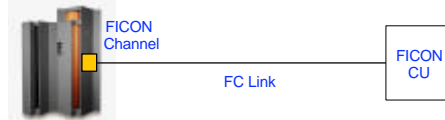
FICON Bridge Connections:



★ *Exploit FICON Channel with Existing ESCON Control Units*

Type=FCV
(FICON LX Only!)

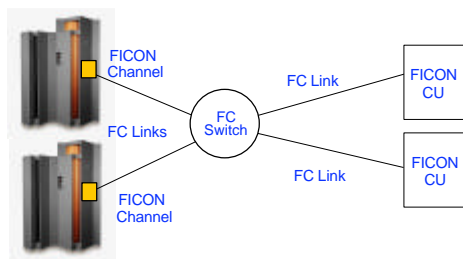
Native FICON Direct Attachment:



★ *Native FICON Control Units*

Type=FC

Native FICON Switched Connectivity:



★ *Full Dynamic Switching of FICON Control Units*

Type=FC

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z900 FICON Installation



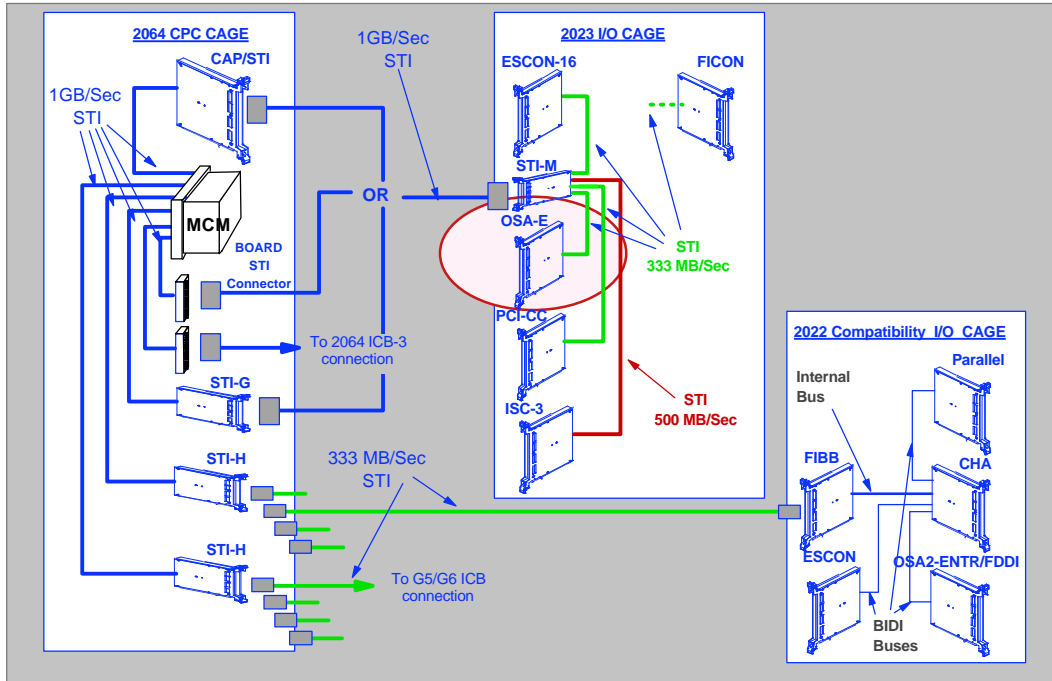
- Up to 16 FICON cards can be installed per new I/O cage (#2023) (FICON channels cannot be installed in the compatibility I/O cage (#2022))
- G5/6 MES to z900 - Existing FICON exchanged for like type (LX, SX)
- **2 FICON channel ports per card, order increment is 2**
- New build or z900 MES add of FICON
 - ▶ Cards are balanced among new I/O cages on a new build
 - ▶ FICON increment 34/35 (17th card) drives a second #2023
 - ▶ FICON increment 66/67 (33rd card) drives a third #2023
- If a compatibility I/O (#2022) cage is driven out
 - ▶ Parallel, OSA-2 - Moved to remaining #2022 or removed
 - ▶ ESCON - Moved to remaining #2022 or exchanged

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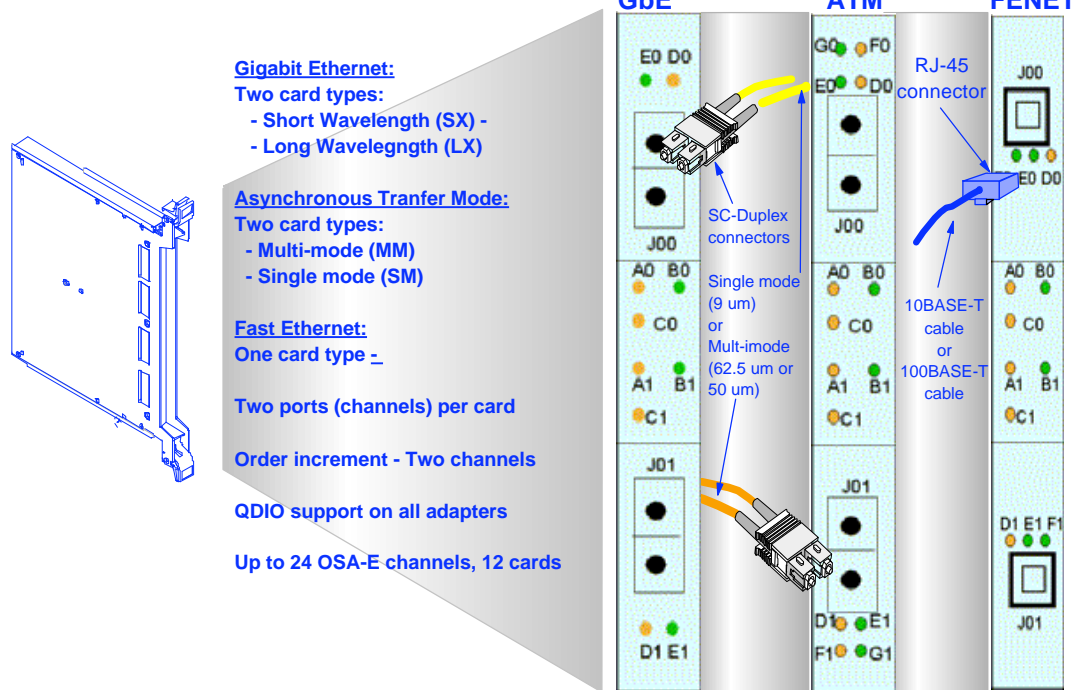
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Channel Connections - OSA-E



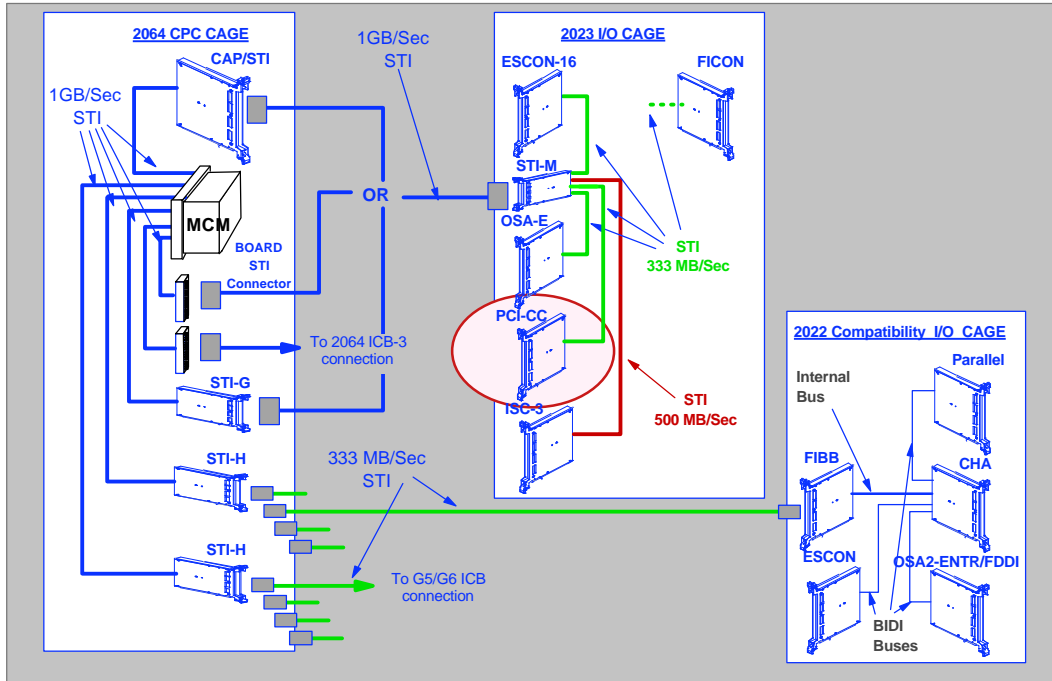
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OSA-E Channel Cards



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Channel Connections - PCI-CC

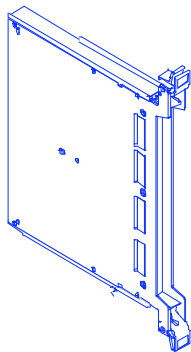


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PCI-CC Card



PCI Cryptographic Coprocessor:

- SSL
- Triple DES
- User Defined Extensions (UDX)

Up to eight PCI-CC cards
(All except Model 100 CF)

Two PCI-CC APs per card, up to 16 total

Each PCI-CC AP takes a CHPID number
(No HCD or IOCP definition)

There are no customer external
connections from the PCI-CC card



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FICON, OSA-E and PCI-CC



● Rules

- ▶ Up to 96 FICON channels (48 cards) can be installed
- ▶ Up to 24 OSA-Express channels (12 cards) can be installed
- ▶ Up to 16 PCI-CC engines (8 cards) can be installed
- ▶ All cards in FC #2023 cages, none in FC #2022
- ▶ Maximum 16 cards per FC #2023 cage
- ▶ 17th and 33rd cards force 2nd and 3rd FC #2023 cages

● Force out of a FC #2022 cage

- ▶ Parallel, OSA-2: Move to remaining FC #2022 or remove
- ▶ ESCON 4-port: Exchange to new ESCON card(s)

● Upgrade from G5/6

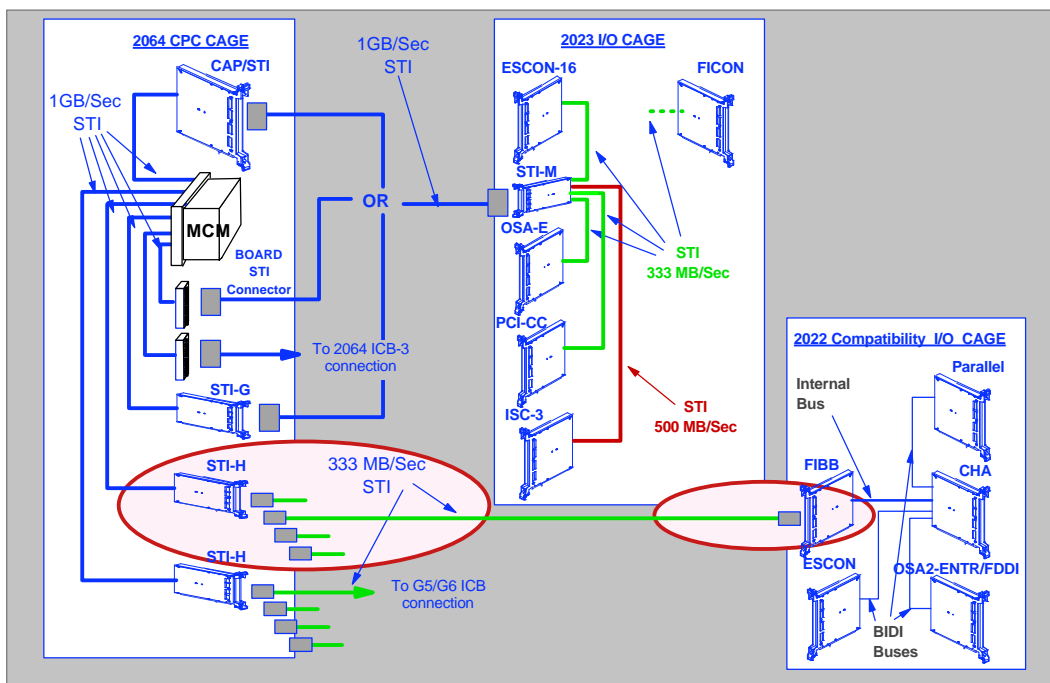
- ▶ Feature exchange for like type and like optical transceiver
- ▶ Odd number: Charge for the additional one (G5/6 MES cost?)

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Channel Connections - #2022 Cage



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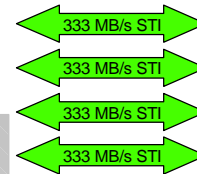
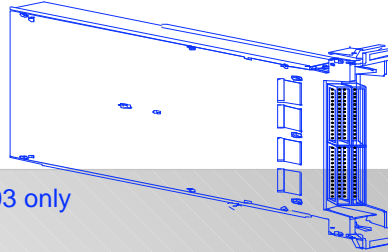
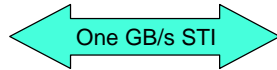
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STI-H Card



One GB/Sec STI From CPC Board

Four 333 MB/Sec STI connections
to legacy Passat cages or 9672
G5/G6 models (ICB-2)



CPC cage slot locations 01 and 03 only

Half high card assembly.

Up to two cards per slot

Multiplexes one 1 Gigabyte STI into four 333Mbyte STI links

The output connections from this card are all used by the legacy style I/O cage (FC 2022), or they are all used to support ICBs, no intermixing is allowed.

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Compatibility I/O Cage FC #2022



● Cards Supported

- ▶ Parallel channel cards with 3 or 4 ports
 - **Parallel 3 port cards only on G5/G6 MES upgrade**
 - **Parallel 4 port cards, G5/G6 upgrades or new builds**
- ▶ OSA-2 Token Ring
- ▶ OSA-2 FDDI
- ▶ ESCON 4 port channel cards
 - **G5/G6 MES upgrade only**

● Maximum Configurations - 22 cards per cage, one or two cages

- ▶ Parallel: 88 channels on new build
96 channels on new build by RPQ or on G5/6 upgrade with two cages
- ▶ OSA-2: 12 cards (channels) maximum on new build or G5/6 upgrade
- ▶ ESCON: 44 cards (176 channels) on G5/6 upgrade with two cages

● CHPID Mapping - Fully Supported

● Hot Plug: Same as G5/6 (All EXCEPT FIBB and CHA)

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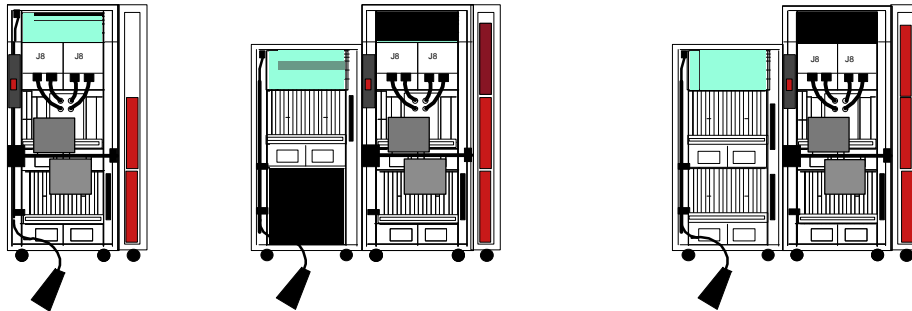
Channel type support



I/O channel/engine type ports or STIs per feature (maximum 256 CHPIDs)	Feature Codes	Total Quantity of Channels Supported and I/O Slots required	Channel Increments for Ordering	Comments Total I/O cages min 1 - max 3 (Cargo plus Passat)
ESCON - 4 ports	2313	176 / 44	4	G5/G6 upgrades only
ESCON - 15/16 ports	2323 (2324)	256 / 18	4 (LIC-CC)	1 spare port per card
Parallel - 4 ports	2304	88 / 22	4	96 via RPQ or G5/G6 u/g
Parallel - 3 ports	2303	96 / 32	3	G5/G6 MES upgrade only
FICON (SX & LX) - 2 ports	2318/2315	96 / 48	2	different features - SC conn
PCI-CC (Crypto) - 2 engines	0861	16 / 8	2	Not defined in IOCP
Fast Ethernet - 2 ports	2366	24 / 12	2	
Gbit Ethernet (SX & LX) - 2 ports	2365/2364	24 / 12	2	different features
ATM (155) (SM &MM) - 2 ports	2362/2363	24 / 12	2	different features
FE, Gbit and ATM	--	---	---	24 chan (12 card) maximum
FICON, PCI-CC, FE, Gbit, ATM	--	---	---	48 cards maximum per system 16 cards maximum per I/O cage
OSA-2 - FDDI - 1 port	5202	12 / 12	1	FDDI + TR = 12 cards max
OSA-2 - TR - 2 ports (1 chan)	5201	12 / 12	1	FDDI + TR = 12 cards max
ISC-3 (1 & 2 Gbit) - 2 + 2 ports	0217/0218/(0219)	32 / 8	1 (LIC-CC)	
ISC-3 (1 Gbit) - 2 + 2 ports	RPQ 8P2197	32/8	2	20 km Extended distance RPQ
ICB-3 - 1 STI connection	0993	16 / 0	1	1 GByte STI connection
ICB-2 - 1 STI connection	0992	8 / 0	1	333 MByte STI connection RPQ to 12 and to 16 max 16 on standalone CFs
ISC-3, ICB-3, ICB-2	--	---	---	32 channel maximum
IC Channel - 2 defined channels	--	32 / 0	2	microcode support
ISC-3, ICB-3, ICB-2, IC Channels	--	---	---	64 channel maximum

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I/O Concurrency



Channel Type	1 x FC2023	Channel Type	2 x FC2023	1 x FC2022 1 x FC2023	Channel Type	3 x FC2023	1 x FC2022 2 x FC2023	2 x FC2022 1 x FC2023
ESCON (max)	256	ESCON(max)	220	88 + 168	ESCON (max)	188	88 + 132	160
FICON	0	FICON	36	0	FICON	68	36	0
Parallel	0	Parallel	0	0	Parallel	0	0	96
FICON (Max)	32	FICON (Max)	64	32	FICON (max)	96	64	32
ESCON	180	ESCON	192	88 + 136	ESCON	160	88 + 104	128
Parallel	0	Parallel	0	0	Parallel	0	0	96
Maximum total I/O concurrency	436	Maximum total I/O concurrency	704	480	Maximum total I/O concurrency	928	704	480

1st FC2023 cage supports
1 to 16 FICON cards
2 channel ports per FICON card

17th FICON card drives a 2nd FC2023 cage
4 port ESCON cards in the FC2022 cage

33rd FICON card drives a 3rd FC2023 cage
88 (4 port) ESCON drives the 1st FC2022 cage
96+ (4 port) Parallel (upgrade) drives a 2nd FC2022 cage

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CHPID Mapping Function



- Ability to change the default CHPID Assignments
 - Better match environment
- Tool accessible by customers via Resource Link
 - Available when order sent to manufacturing database
- Protects current definitions
 - Ability to input IOCP source deck
- Two approaches (Resource Link)
 - Manual mapping
 - Availability mapping
 - ★ IOCP and Control Unit Priorities
- Output stored on diskette for input to SE
 - Extended outage time for manual input
- Customer maintains CHPID documentation

Availability Mapping



Control Unit Number	Priority	Defined CU Type	CU Path	CHPID numbers and availability intersect region	Comment
DCA0		CGA	FD		
DCB8		CGA	FD		
DE00		3900-3	3E	EE	
DE01		3900-3	3E	EE	
DE02		3900-3	3E	EE	
DE03		3900-3	3E	EE	
DE04		APP1	3E	EE	
DE05		APP1	3E	EE	
DE06		APP1	3E	EE	
DE07		APP1	3E	EE	
1000		3900	A1		
1001		3900	B4		
1002		3900	B7		
1003		3900	C0		
1004		3900	C4		

Looking for problems.....



Search: Go

Education

Resource Link Home
Site Overview
User Profiles
Planning
Education
Library
Service Support
Group Discussions
Site Feedback
Resource Link News

IOCCP Control Unit - CHPID Assignment and Availability

CHPID Home | IOCCP Load | **Availability** | Remapping | Reports | Diskette Download

Control Unit Number	Priority	Redund CU Type	CU Path	CHPID numbers and availability interest region	Comment
E40B		3990	13	1B	
E40C		3990	33	3B	
E40D		3990	93	9B	
E001		3990	23	1C	
E002		3990	9C	3D	
E003		3990	9C	9D	
E003		3990	9F		
ED0A		9032.5	21		
ED0A		9032.5	1B	3B	
ED13		9032.5	31	3B	
ED14		9032.5	C8	3B	
FFFE		C8S	A4	35 34 A5	

Legends-- No Priority assigned Availability interest reason C= Two or more assigned channels use the same channel card
S= Greater than half the assigned channels use the same STI
M= All the assigned channels are supported by the same MBA group
** Managed path

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Channel CHPID Assignment Task



CHPID Assignment Function

Options View Help

CHPID Assignment Function.

Enter the Proposed CHPID assignments and press apply.

Channel Cage	Location Card Slot	Jack	Bus/Link	Assigned CHPID	Proposed CHPID	Card
A01B	LG01	J.07	00-11.00	19		ESCON 16 CH
A01B	LG01	J.08	00-11.00	1A		ESCON 16 CH
A01B	LG01	J.09	00-11.00	1B		ESCON 16 CH
A01B	LG01	J.10	00-11.00	1C		ESCON 16 CH
A01B	LG01	J.11	00-11.00	1D		ESCON 16 CH
A01B	LG01	J.12	00-11.00	1E		ESCON 16 CH
A01B	LG01	J.13	00-11.00	1F		ESCON 16 CH
A01B	LG01	J.14	00-11.00	20	C0	ESCON 16 CH
A01B	LG02	J.00	01-10.00	21	C1	ESCON 16 CH
A01B	LG02	J.01	01-10.00	22	C2	ESCON 16 CH
A01B	LG02	J.02	01-10.00	23	C3	ESCON 16 CH
A01B	LG02	J.03	01-10.00	24		ESCON 16 CH
A01B	LG02	J.04	01-10.00	25		ESCON 16 CH
A01B	LG02	J.05	01-10.00	26		ESCON 16 CH
A01B	LG02	J.06	01-10.00	27		ESCON 16 CH
A01B	LG02	J.07	01-10.00	28		ESCON 16 CH
A01B	LG02	J.08	01-10.00	29		ESCON 16 CH
A01B	LG02	J.09	01-10.00	2A		ESCON 16 CH

Proposed CHPID Change... or Double Click selected line for Proposed CHPID change

Apply Verify Clear Exit Help

CPC Configuration

Channel CHPID Assignment

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Channel CHPID Assignment Notes



- Any channel or PCI-CC engine (AP) may have any CHPID number assigned
 - ▶ Assignment is performed either by manually entering the CHPID re-map information from the HMC/SE, or by importing a re-map diskette also from the HMC/SE
 - ▶ This is a CE-mode only function
- IC Channels are recommended to be assigned CHPID numbers from x'FF' downwards

I/O Definition Support (IOCP)



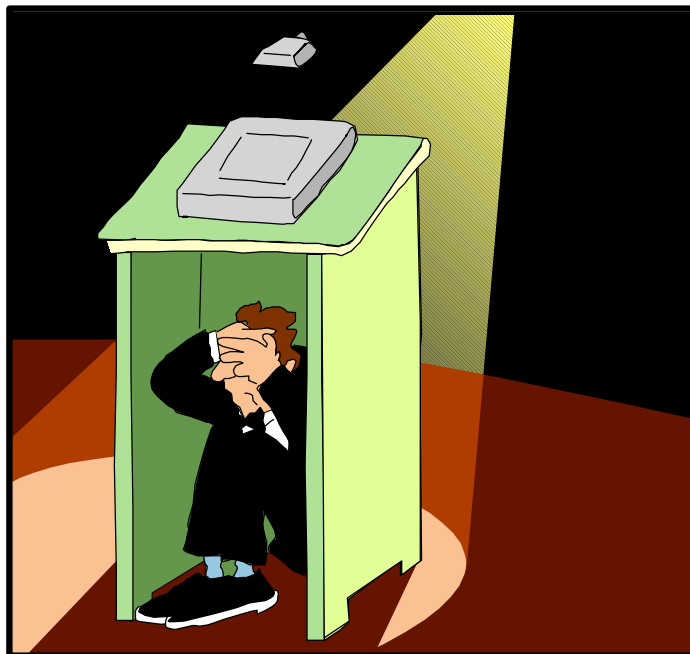
- IOCP program support for the Freeway is IYPIOCP (new IOCP program)
- There are minimal external changes to IOCP to support the 2064
- The IOCP statement changes for the 2064 are as follows:
 - ID statement
 - ▶ SYSTEM=(2064,1) for general purpose models (101 to 116 and 1C1 to 1C9)
 - ▶ SYSTEM=(2064,2) for CF only model (100)
 - RESOURCE statement
 - ▶ No change
 - CHPID statement support for DCM managed CHPIDs
 - ▶ OS=01 - Identifies CNC, FC or FCV channel as managed and shared
 - ▶ Required: IOCLUSTER=xxxxxxx - I/O cluster (sysplex) name of owning sysplex
 - ▶ Required: SWITCH - director identification
 - ▶ Not Allowed: PART, PARTITION, NOTPART - can't restrict candidate LP list

I/O Definition Support (IOCP)

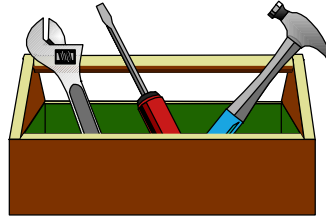


- CHPID macro-instruction statements unchanged (except for DCM)
 - ▶ FICON channels
 - FICON Bridge - FCV
 - FICON native - FC (Point-to-Point or Switched Point-to-Point)
 - ▶ ESCON
 - Connection - CNC (Point-to-Point or Switched Point-to-Point)
 - Channel-to-channel - CTC
 - Conversion byte - CBY
 - Conversion block - CVC
 - ▶ Parallel
 - Block - BL
 - Byte - BY
 - ▶ OSA-2
 - Open Systems Adapter - OSA
 - ▶ OSA-E
 - Open Systems Adapter - Express - OSD
 - Open Systems Adapter - Express - OSE
- PCI-CC engines
 - ▶ No support required - not defined by IOCP

Questions?



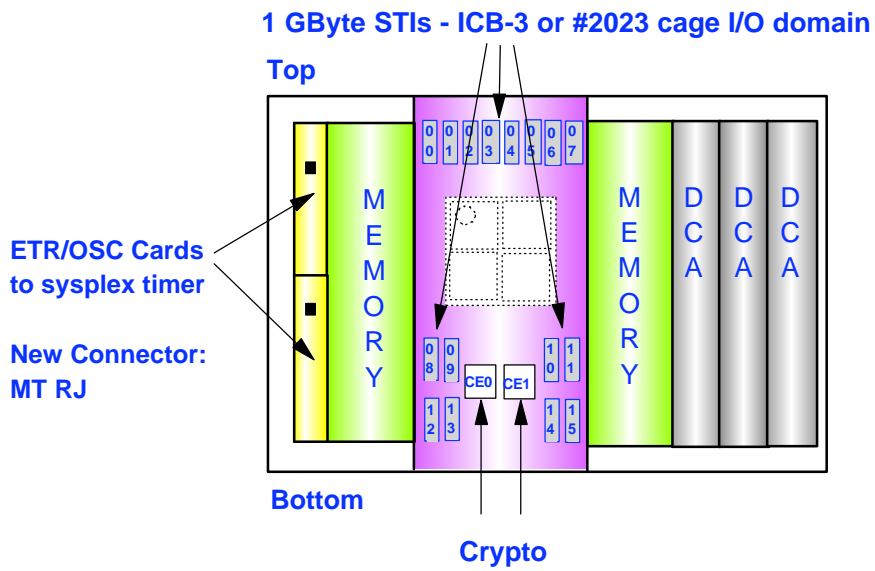
Backup Charts.....



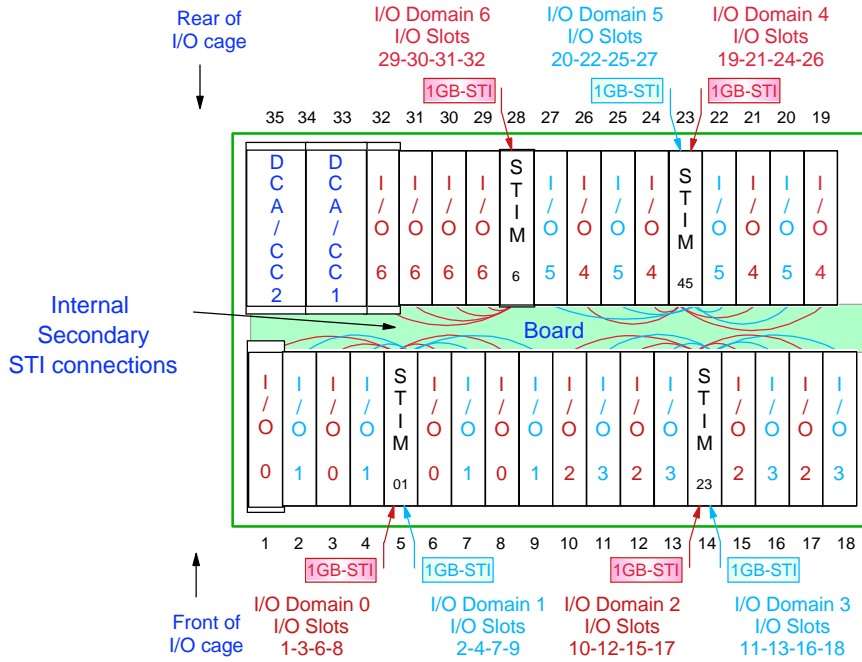
Self Timed Interfaces (STIs)



CPC Cage - Rear View

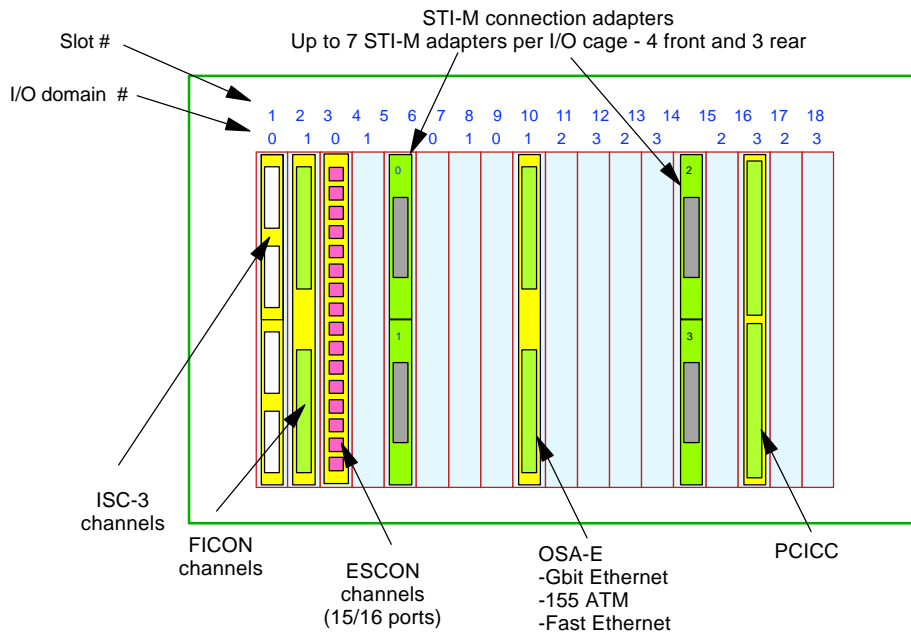


New I/O Cage FC2023 Top View



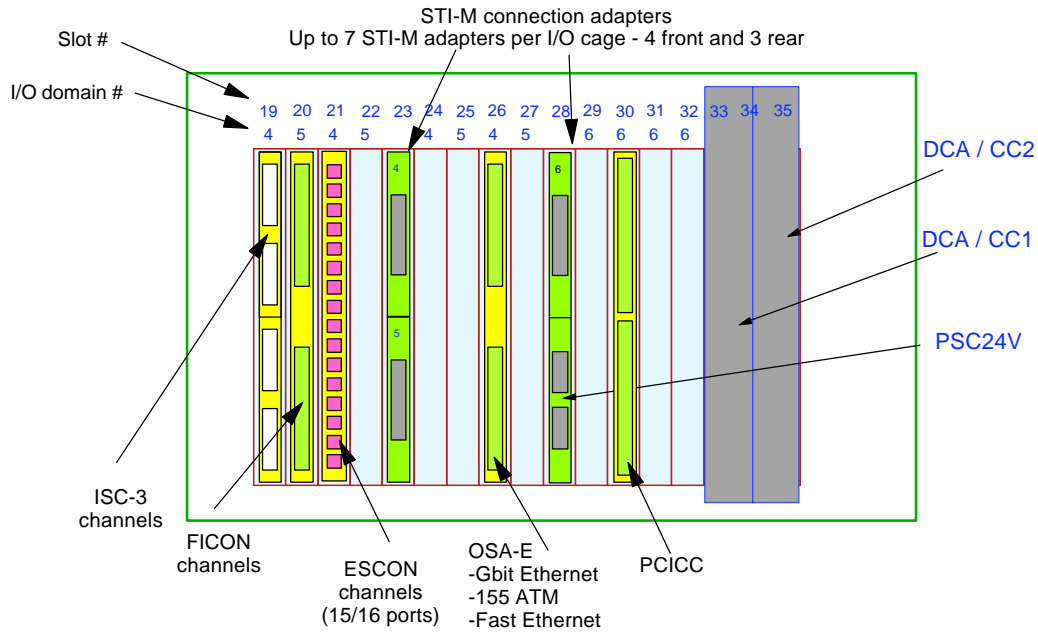
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New I/O Cage FC2023 - Front view



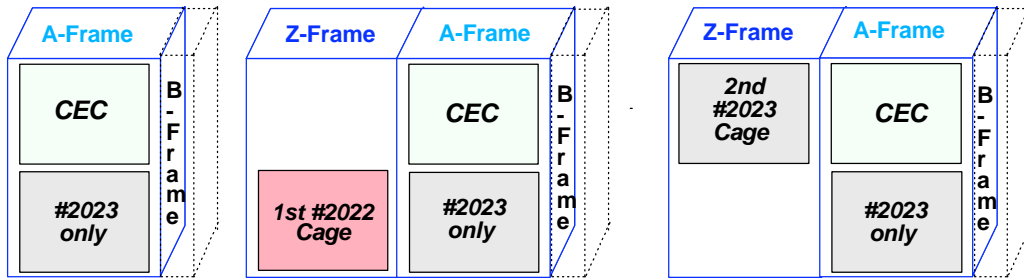
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New I/O Cage FC2023 - Rear view



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Frame and Cage Configurations



★ **One I/O Cage**

- 28 New Slots
- 7 Domains

★ **Two I/O Cages**

- 28 New Slots
- 22 Old Slots
- 10 Domains

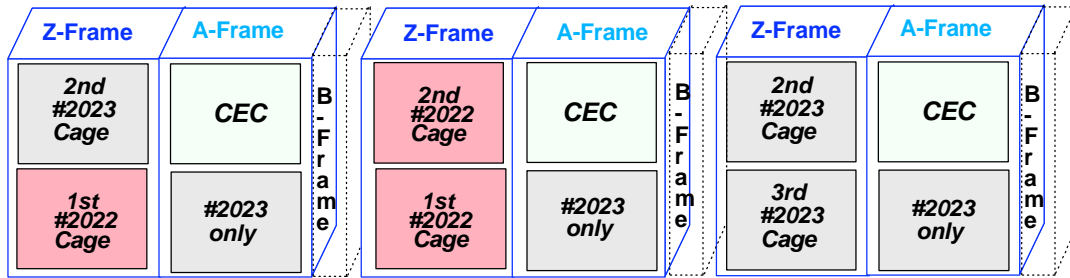
★ **Two I/O Cages**

- 56 New Slots
- 14 Domains

- B-Frame required for IBF Features
- #2022 - Compatibility Cage: Parallel, OSA-2 (TR/EN, FDDI), ESCON 4-Port, 3 domains, 22 slots
- #2023 - New I/O Cards, 7 domains, 28 slots

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Frame and Cage Configurations



★ Three I/O Cages

- 56 New Slots
- 22 Old Slots
- 17 Domains

★ Three I/O Cages

- 28 New Slots
- 44 Old Slots
- 13 Domains

★ Three I/O Cages

- 84 New Slots
- 21 Domains
- Note: No more than 20 domains ever needed for 256 channels.

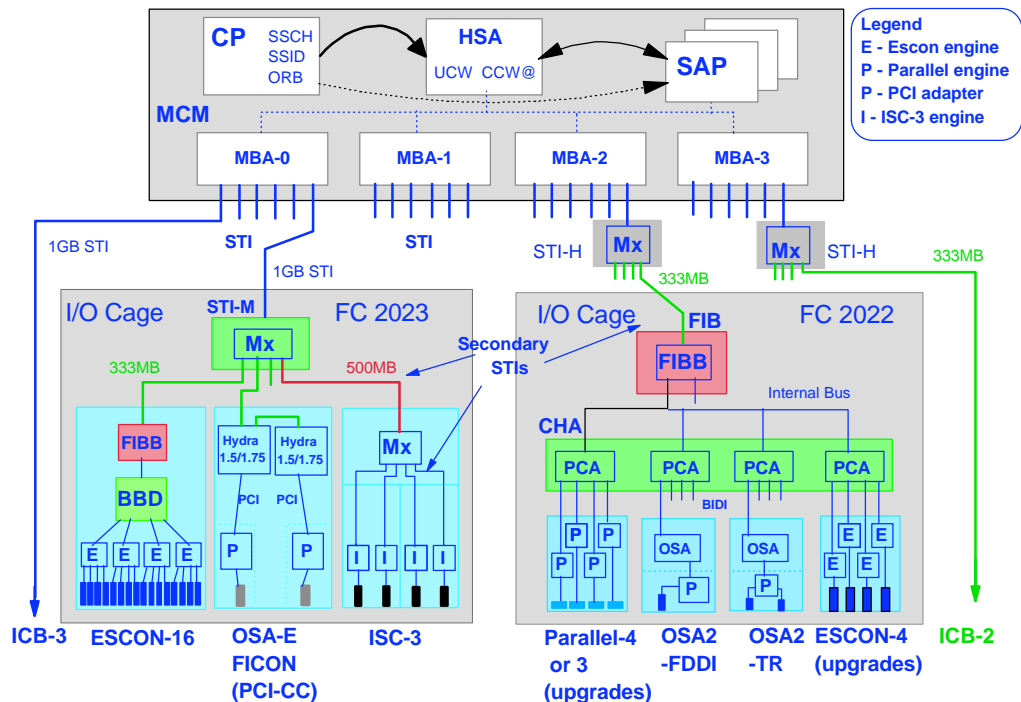
- B-Frame required for IBF Features
- #2022 - Compatibility Cage: Parallel, OSA-2 (TR/EN, FDDI), ESCON 4-Port, 3 domains, 22 slots
- #2023 - New I/O Cards, 7 domains, 28 slots

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Channel Subsystem Structure

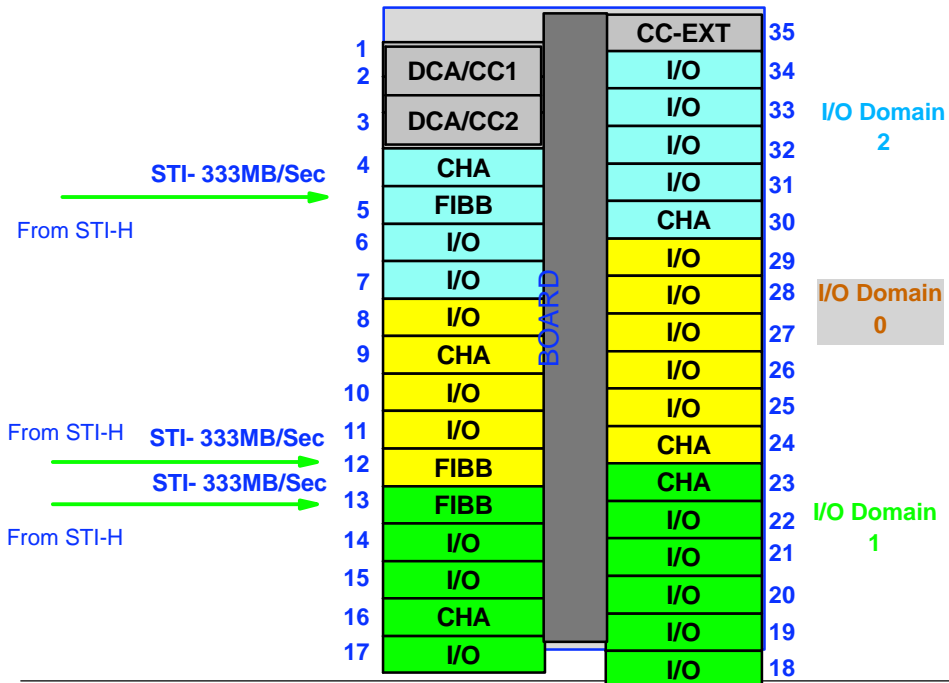


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Compatibility I/O Cage FC #2022



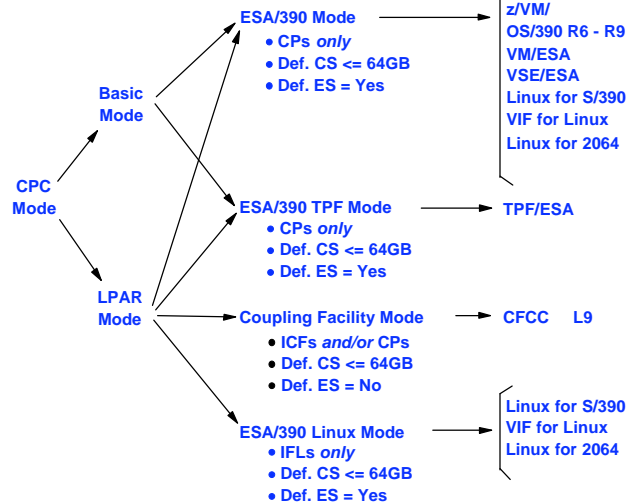
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Modes and Memory



● Notes:

- ▶ 64GB CS limit is based on maximum 2064 storage NOT an architectural limit
- ▶ IBM intends to release Linux for 2064 "soon"
- ▶ VIF for Linux will NOT support Linux for Fwy VM/NP will support Linux for Fwy



Addr. Mode	CS Usage Limit	ES Usage
z/OS		
64-bit	64GB	No
64-bit	64GB	No
OS/390 R10		
31-bit	2GB	Yes
z/VM		
64-bit	64GB	Yes
z/VM/		
31-bit	2GB	Yes
OS/390 R6 - R9		
31-bit	2GB	Yes
VM/ESA		
31-bit	2GB	Yes
VSE/ESA		
31-bit	2GB	No
Linux for S/390		
31-bit	2GB	No
VIF for Linux		
31-bit	2GB	Yes
Linux for 2064		
64-bit	64GB	No
TPF/ESA		
31-bit	2GB	Yes
CFCC L9		
31-bit	2GB	Yes
Linux for S/390		
31-bit	2GB	No
VIF for Linux		
31-bit	2GB	Yes
Linux for 2064		
64-bit	64GB	No

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