

Session G04

IBM System z9 LPAR Advanced Topics

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Agenda

- IBM System z9[™] Update
 - System z9 EC New Function
 - System z9 BC
 - Dispatching Pool Change
 - LPAR Configuration
- Activation Profile Changes
- Changing Running Partitions
- Memory and Addressability
- HSA Estimation Tool





IBM System z9™ Update



z9-109 New Functions and Features (2005)

Five New Hardware Models

Faster Uni Processor

Up to 54 CPs

Up to 512GB Memory

Up to 60 LPARs

CBU for IFL, ICF and zAAP

Separate PU Pool Management

Redundant I/O Interconnect

Enhanced Driver Maintenance

Enhanced Book Availability

Wild Branch PD Assist

54 additional hardware Instructions



Preview*
Server Time Protocol

Up to 16 2.7 GB STIs per book

MIDAW facility

Second Subchannel Set in each LCSS

Subchannel set 0 increased to 63.75K Subchannels

FICON Express2 supports 64 Open Exchanges

Increased Number of FICON®
Express2 features

N_Port ID Virtualization

IPv6 Support for HiperSockets™

OSA-Express2 1000BASE-T

OSA-Express2 OSN (OSA for NCP)

Enhanced CPACF with AES, PRNG and SHA-256

Configurable Crypto Express2

*This statement represents IBM's current intentions. IBM development plans are subject to change or withdrawal without further notice.



z9 EC - New functions/features enhancements Microcode level 63J plus MCLs (May, 2006)

24 New subcapacity settings

New Specialty Engine – IBM zIIP

CBU for zIIP

PU Conversions for zAAPs and zIIPs

CBU Enhancements

On/Off CoD
Enhancements



New FICON Express4

Improved FICON Error Recovery

OSA Layer 2/3 Enhancements

Crypto Enhancements

Availability in EU after implementation of RoHS

Note: Please refer to the latest PSP bucket for latest PTFs for new functions/features



Finding the server that meets your business needs

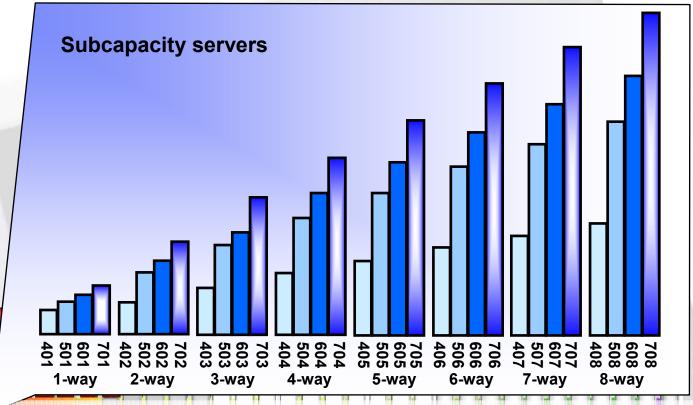
Approximate Capacity relative to full speed MCI 701:

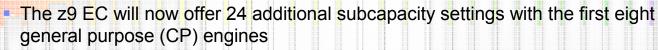
601 = 80%

501 = 66%

401 = 34%







- Entry point is approximately one third the capacity of the 701
- All general purpose processors must be the same capacity within one z9 EC
- The one for one relationship of zAAP or zIIP to CP still exists, but one CP can satisfy requirement for either or both specialty engines



z9 BC Hardware Model Structure

- One machine type 2096 two hardware models, R07 and S07
- Model number indicates PUs available for characterization
 - Single serial number
 - PU characterization is identified by number of features ordered
- One System Assist Processors (SAPs) per System
- z9 BC machine capacity indicators (MCIs) also called capacity settings
 - nxx, where n = subcapacity engine size and xx = number of CPs
 - For Model R07 n = A up to J and xx = 1 to 3
 - For Model S07 n = K up to Z and xx = 1 to 4
 - Total 73 MCIs for the two z9 BC hardware models
 - 20 for Model R07 and 53 for Model S07

Models	MCMs	Available PUs	Max Available Subcapacity CPs	Standard SAPs	Standard Spares	CP/IFL/ ICF/zAAP/zIIP ****	Max Memory	Max Channels
R07*	1	8	3	1	0	3/6/6/3/3	64 GB	240 ***
S07**	1	8	4	1	0	4/7/7/3/3	64 GB	420 ***

^{*} Must have a minimum of 1 CP

** Must have a minimum of 1 CP, IFL or ICF

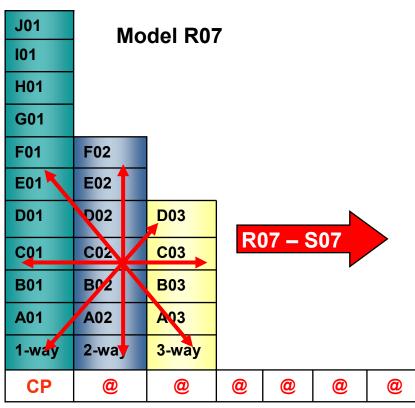
*** Max is for ESCON® channels.

**** For each zAAP and/or zIIP installed there must be a corresponding CP. The CP may satisfy the requirement for both the zAAP and/or zIIP.

The combined number of zAAPs and/or zIIPs can not be more than 2x the number of general purpose processors (CPs).



z9 BC Improved granularity and scalability



Full on demand upgradeability in the family

- ▶ Model R07 must have minimum 1 CP engine
- ► Model S07 may be a full IFL or ICF system
- ► Model R07 upgradeable to model S07
- ► Model S07 upgradeable to z9 EC Model S08

				i		
Z 01	Z 02	Z03	Z04	Mo	del S	07
Y01	Y02	Y03	/ Y04			
X01	X02	X03	X04			
W01	W02	W03	W04			
V01	VOS	V03	V04			
U01	U02	U03	UU4			
T01	T 02	T03	T04			
S01	S 02	S03	S04			
R01	R02	R03	R04			
	Q02	Q03	Q04	S07	7 – z9	EC
	P02	P03	P04			
	O02	O03	O04			
	N02	N03	N04			
		M03	M04			
		L03	L04			
			K04			
1-way	2-way	3-way	4-way			
#	@	@	@	@	@	@

= CP or IFL or ICF

@ = Any Specialty Engines. zAAPs and zIIPs have T & Cs



LIC Update to get 109 to EC and to support BC

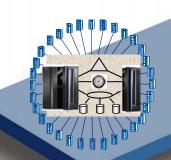
- LIC for the Support Element
 - Driver 63j (Same as z9-109)
 - Plus MCL Bundle 22A of June 1, 2006 (Concurrent)
- LIC for the HMC
 - Driver 64x
 - Plus MCL Bundle 3 of June 28, 2006
- Options to get the LIC
 - 1. Shipped with any new build System z9 or MES to z9-109
 - 2. Service can order SE and HMC AROMs for z9-109
 - 3. Dr 64x update for **HMC 0075 and later on non-z9 CECs**:
 Service can order **ECA 241** for the CEC that "owns" the HMC
 This ECA ships memory if needed on the older HMC
 Prerequisite: Bring SE up to current MCL level

Note: Driver 63 Exception Letter includes 63j and 64x



Technology evolution with specialty engines

Building on a strong track record of technology innovation with specialty engines, IBM introduces the System z9 Integrated Information Processor



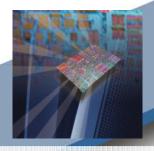
Internal Coupling Facility (ICF) 1997

Centralized data sharing across mainframes



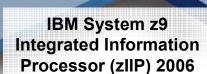
Integrated Facility for Linux (IFL) 2001

> Support for new workloads and open standards



System z9
Application Assist
Processor (zAAP)
2004

Designed to help improve resource optimization for z/OS Java technology-based workloads



Designed to help improve resource optimization for eligible data workloads within the enterprise



PR/SM™ Hypervisor™ PU Dispatching "Pools"

- PU Pool Physical PUs to dispatch to online logical PUs
- z9 EC with 10 CPs, 1 ICF, 2 IFLs, 1 zIIP and 3 zAAPs
 - CP pool contains 10 CP engines
 - ICF pool contains 1 ICF
 - IFL pool contains 2 IFLs
 - zAAP pool contains 3 zAAPs
 - zIIP pool contains 1 zIIP
 - z/OS LPAR can have different CP, zAAP and zIIP weights
- z990 with 11 CPs, 1 ICF, 2 IFLs, and 3 zAAPs
 - CP pool contains 11 CP engines
 - Specialty pool contains 6 engines ICFs, IFLs, zAAPs
 - z/OS LPAR zAAP weight is set equal to the initial CP weight





PR/SM Hypervisor PU Pool Rules

Logical PUs dispatched from supporting pool only

- Logical CPs from CP pool only, for example
- Pool "width"
 - Width equals the number of physical PUs in the pool
 - Limits an LPAR's maximum number of shared logical PUs brought online

PUs placed in pools by

- Activate (POR)
- Concurrent Upgrade On/Off CoD, CBU, CIU, CUoD MES
- Dedicated LPAR deactivation
- Dedicated LPAR configure logical PU OFF

PUs removed from pools by

- Concurrent Downgrade On/Off CoD, CBU, PU Conversion MES
- Dedicated LPAR activation ("width" permitting)
- Dedicated LPAR configure logical PU ON ("width" permitting)





z9 EC with 9 CPs, 1 ICF, 2 IFLs, 3 zAAPs and 1 zIIP

- LPAR Share = Pool PUs x (LPAR Pool Weight)/(Total Pool Weight)
 - Can't exceed number of Online Logical Processors dispatched from the pool
 - z/OS LPAR Separate "Initial" weights for CPs, zAAPs, and zIIPs





Total Pool Weights - CP = 900, zIIP = 1000, zAAP = 200, IFL= 400, ICF= 1000

LPAR Online Logical PUs, LPAR Weights				Intended PU Share: PUs						
Name	СР	zIIP	zAAP	IFL	ICF	СР	zIIP	zAAP	IFL	ICF
zOS1	5, 200	1, 100	2, 100	NA	NA	2	.1	1.5	0	0
zOS2	9, 700	1, 900	3, 100	NA	NA	7	.9	1.5	0	0
CF1	0, 0	NA	NA	NA	1, 500	0	0	0	0	.5
CF2	0, 0	NA	NA	NA	1, 500	0	0	0	0	.5
zVM1	0, 0	NA	NA	2, 100	NA	0	0	0	.5	0
Linux1	0, 0	NA	NA	2, 300	NA	0	0	0	1.5	0
	900 1000 200 400 1000 ← Sums of pool weights									
	Sums of PU shares by type →					9	1	3	2	1

Specialty engine weights are independent. All desired shares achieved.



z990 – 10 CPs, 1 ICF, 2 IFLs, and 3 zAAPs

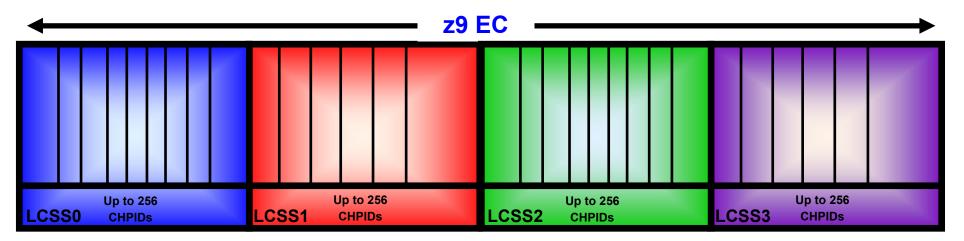
- LPAR Share = Pool PUs x (LPAR Pool Weight)/(Total Pool Weight)
 - Can't exceed number of Online Logical Processors dispatched from the pool
 - zIIP processors are not available
 - z/OS LPAR "Initial" CP weight applies to CPs and to zAAPs
- Pool PUs (Physical) CP =10, Specialty = 6 (ICF, IFL, zAAP)
- Total Pool Weights CP = 1000, Specialty = 2000

LPAR	Onlin	e Logical P	Us, LPAR V	PU Share: PUs		
Name	СР	zAAP	IFL	ICF	СР	Specialty
zOS1	5, 250	2, <mark>250</mark>	NA	NA	2.5	.75
zOS2	10, 750	3, <mark>750</mark>	NA	NA	7.5	2.25
CF1	0,0	NA	NA	1, 167	0	.5
CF2	0,0	NA	NA	1, 167	0	.5
zVM1	0, 0	NA	2, 166	NA	0	.5
LINUX1	0, 0	NA	2, 500	NA	0	1.5
	1000	2000	(Specialty	←Sums of	weights	
Sums of PU shares by type →					10	6

Specialty engine weights must be aggregated. Desired zAAP share not achieved.



z9 EC Logical Channel Subsystems (LCSSes)



- Up to four Logical Channel Subsystems (LCSSes) on z9 EC
 - Up to 15 LPARs per LCSS
 - ► Up to 256 channels per LCSS
- Multiple LCSSes Enable
 - ► Up to 60 Logical Partitions per CEC (Requires four LCSSes on z9 EC)
 - Up to 1024 external channels on z9 EC
- An LPAR can access channels ONLY in its assigned LCSS
- Some channels may be assigned to multiple LCSSes "Spanned Channels"
 - ► ICP, IQD, FC, FCP, OSE, OSD, OSC, OSN, CBP, CFP
 - But not ESCON or FICON Conversion



Partition ID, MIF ID, and Partition Number

	Partition Identifier	MIF ID	Partition (Zone) Number
Defined	By systems programmer in the LPAR image profile on HMC	By systems programmer: HCD "Partition Number" IOCP RESOURCE statement	z800/z900 = MIF ID else assigned at POR by PR/SM z9/z990/z890/assigned at POR by PR/SM
Range (Hex)	z800/z900 - 0-F z9/z890/z990 - 00-3F	1-F	z800/z900 - 1 to F z9/z990/z890 - 1 to 3C/1E
Size	z800/z900 - 4 bits z9/z890/z990 - 8 bits	4 bits	4 bits z800/z900 8 bits z9/z990/z890
Usage	Messages, Store CPUID, PGID z9/z990/z890 : CFRM Policy to identify a CF LPAR	MIF Channel Sharing z800/z900: CFRM policy to identify a CF LPAR	Internal usage, not externalized.
Aliases	LP ID, User logical Partition ID (UPID)	Image ID (IID), EMIF ID	None
Notes	Unique on the CEC. LPAR deactivate/activate to change.	z800/z900: Unique on CEC z9/z990/z890 : Unique in LCSS. POR to change.	Unique on the CEC. POR to change.

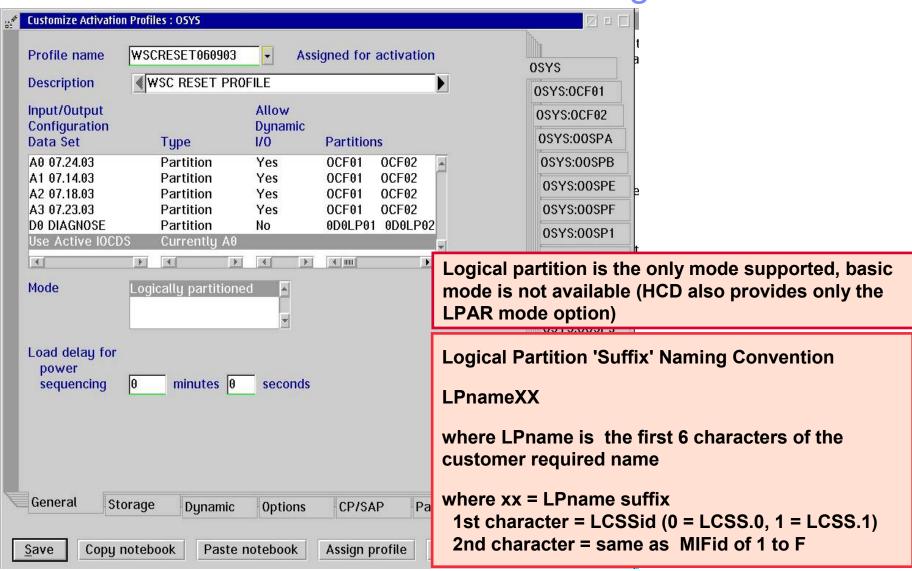
Note: z990 compatibility support for the OSes is required to support changes to Partition Identifier "Size" and "Usage" running on z9/z990/z890 and often on other images in a Sysplex with an OS image on one of these. ICKDSF R17 is required on any image sharing disk with an OS on z9/z990/z890 for the same reasons.



System z9 Activation Profile Changes

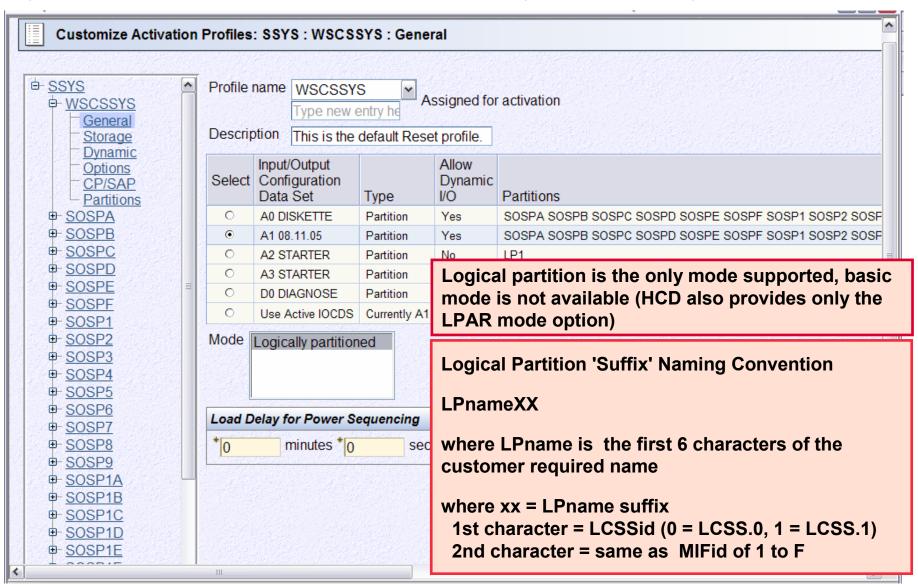


z990 HMC Reset Profile - General Page



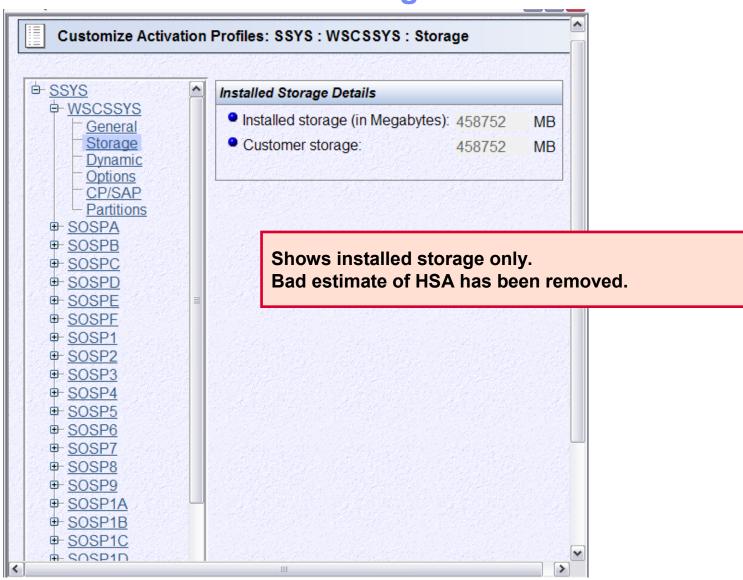


System z9 - Reset Profile - General (CEC SSYS)



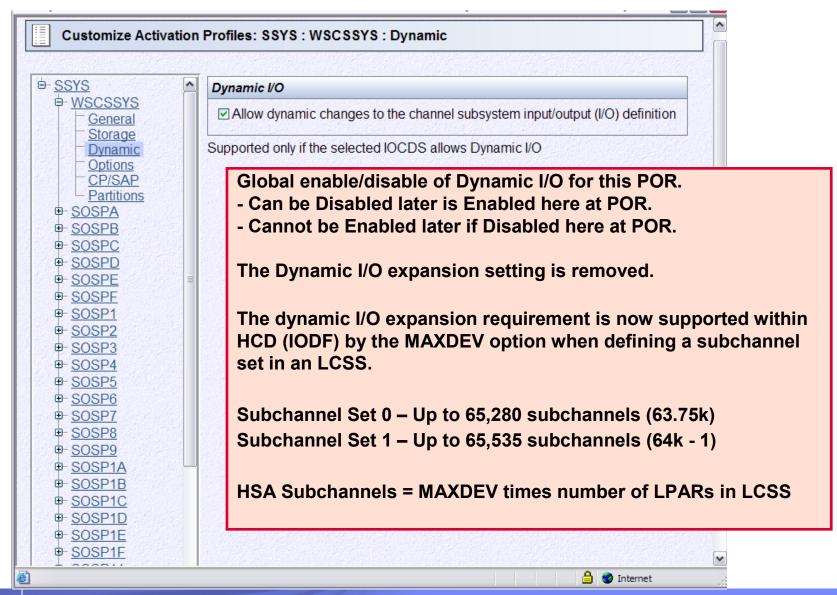


System z9 - Reset Profile - Storage



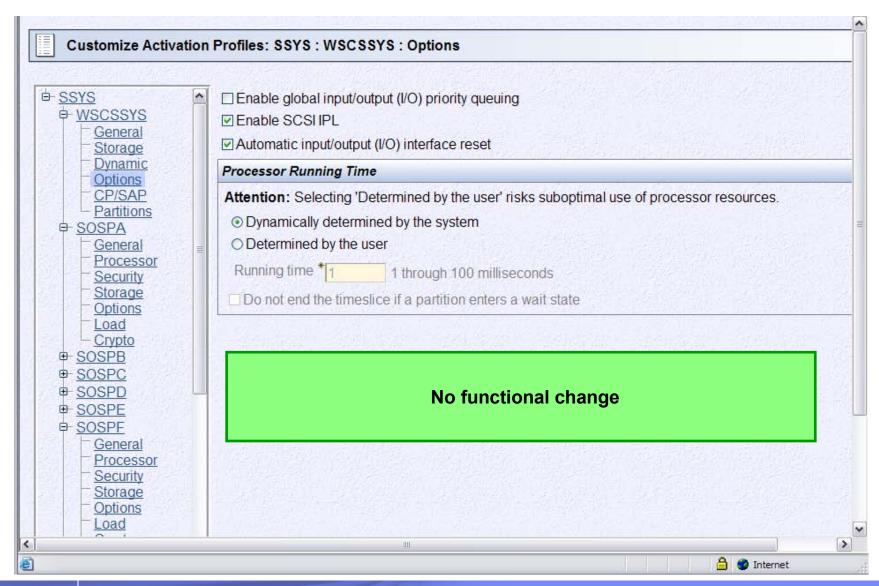


System z9 Reset Profile - Dynamic



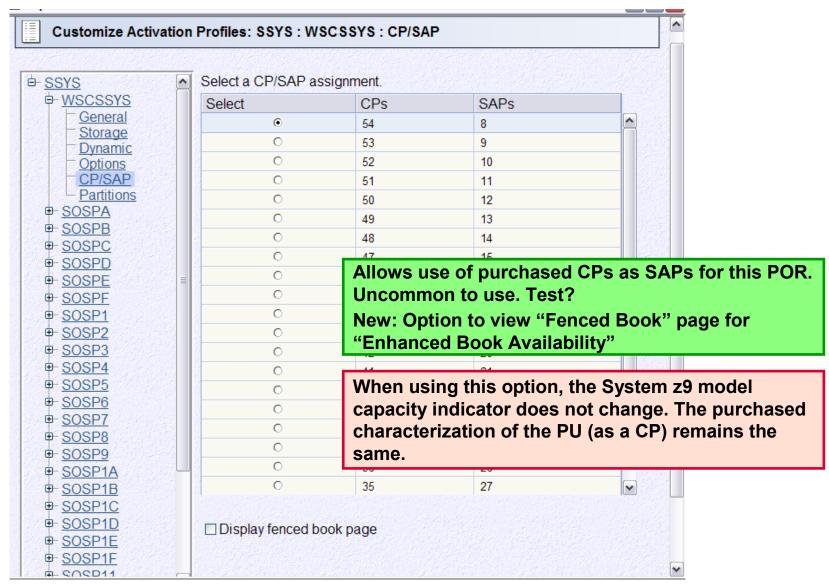


System z9 Reset Profile - Options



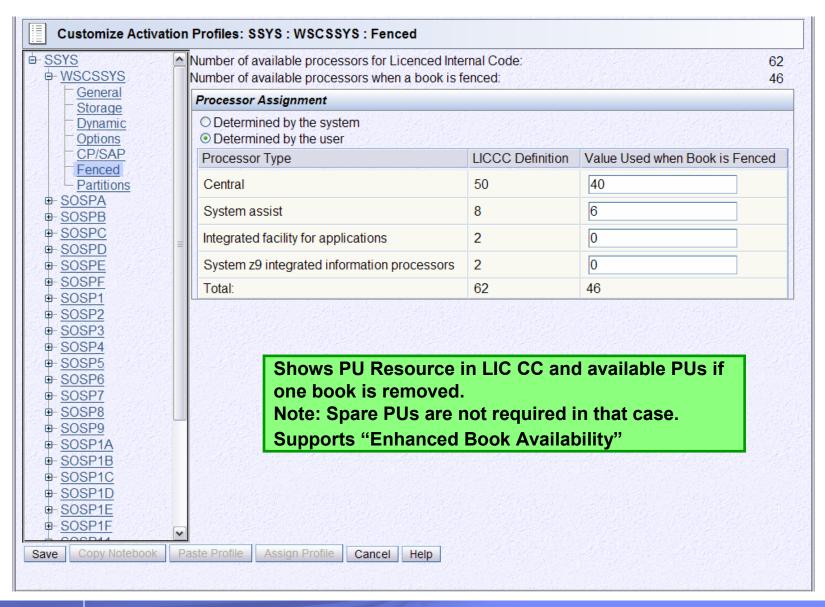


System z9 Reset Profile - CP/SAP



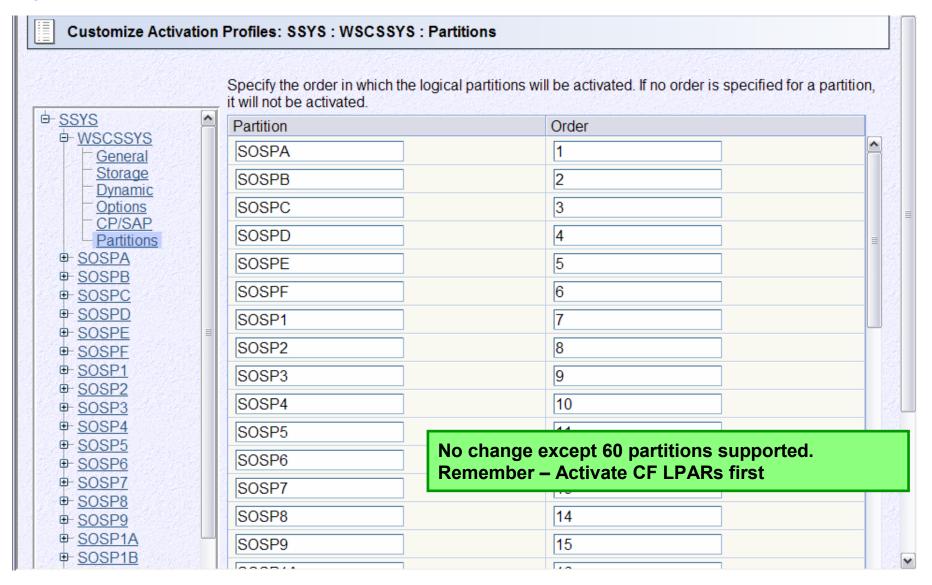


System z9 Reset Profile – Fenced Book Page



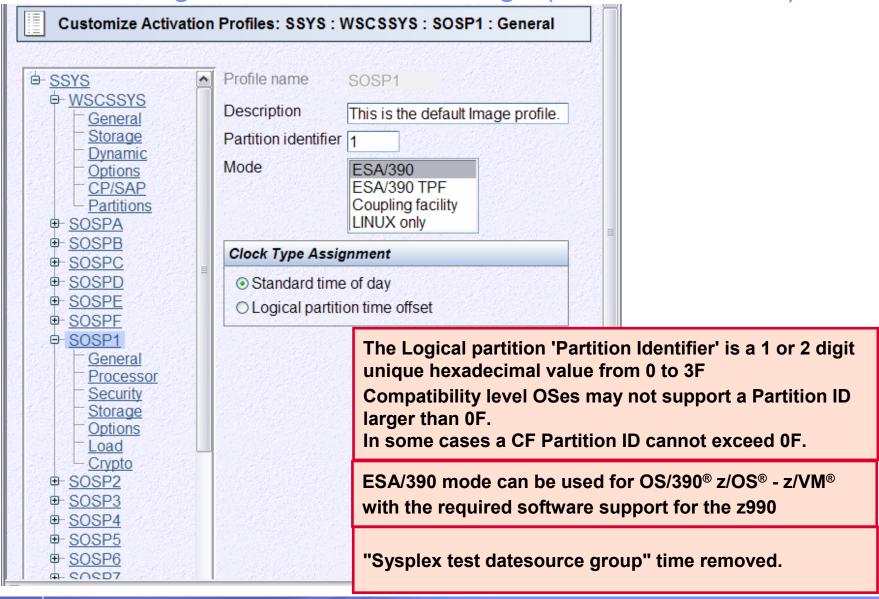


System z9 Reset Profile - Partitions



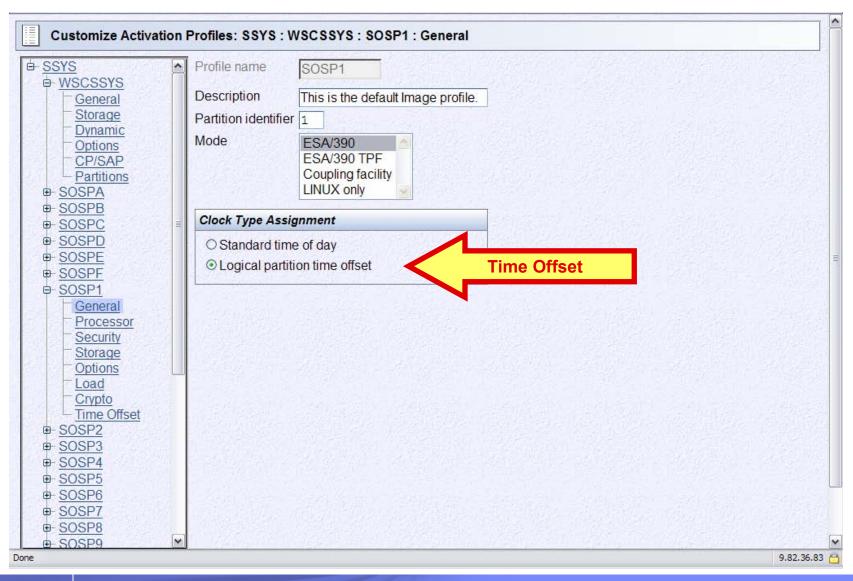


System z9 Image Profile - General Page (Partition SOSP1)



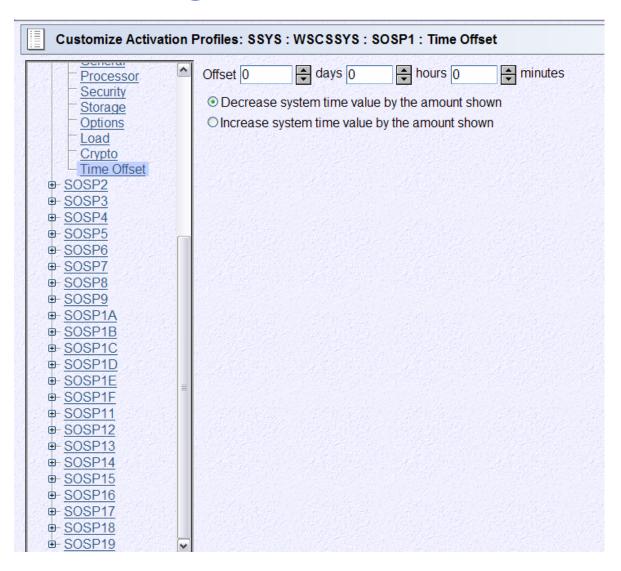


System z9 Image Profile - General Page



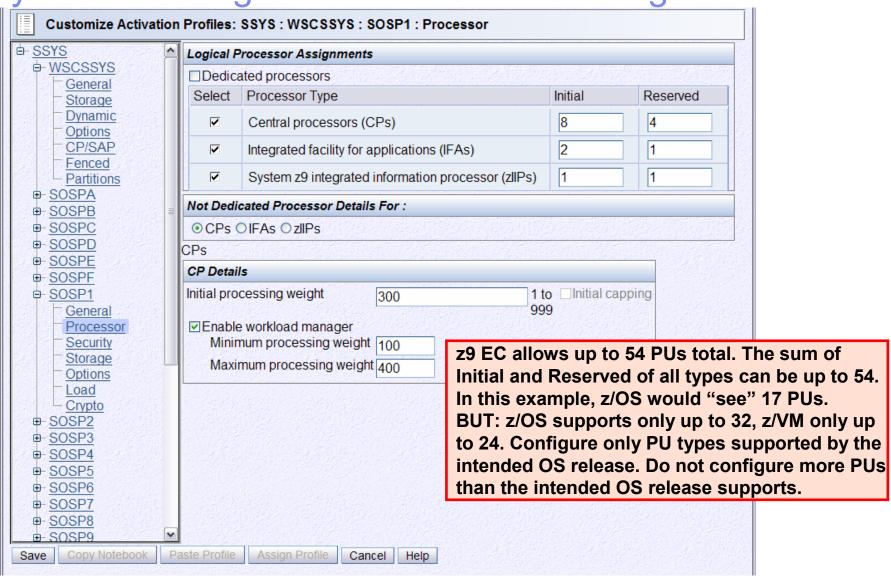


System z9 Image Profile – Time Offset



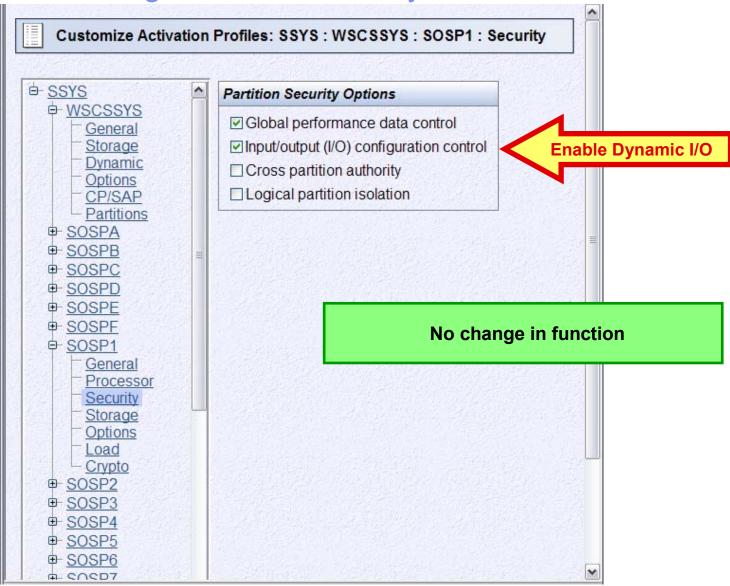


System z9 Image Profile - Processor Page



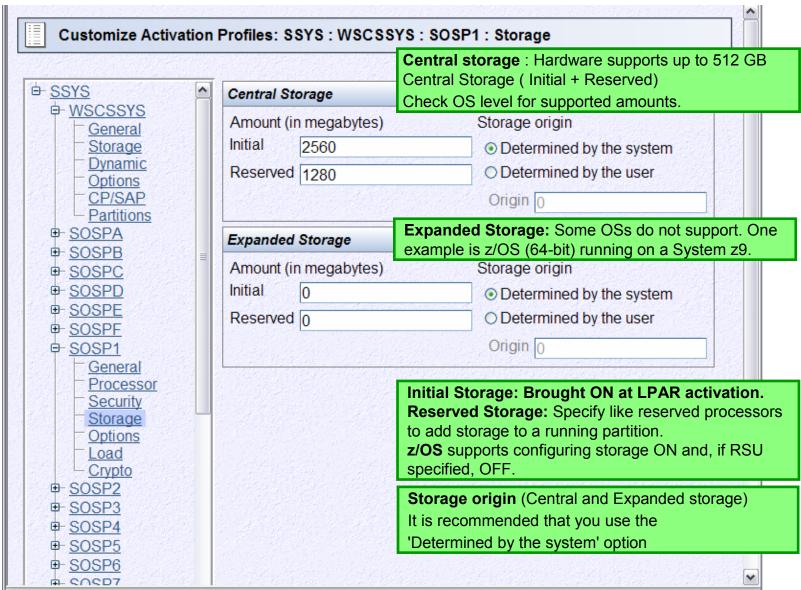


System z9 Image Profile - Security



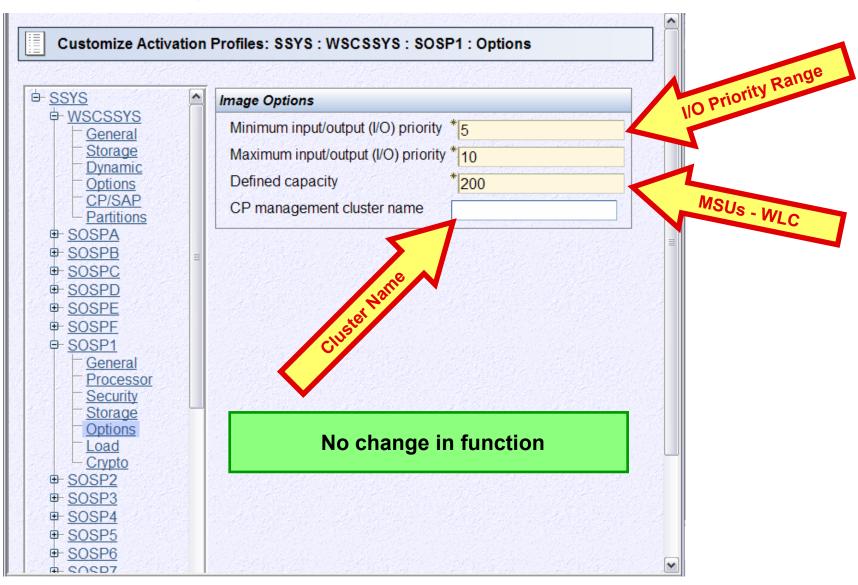


System z9 Image Profile - Storage



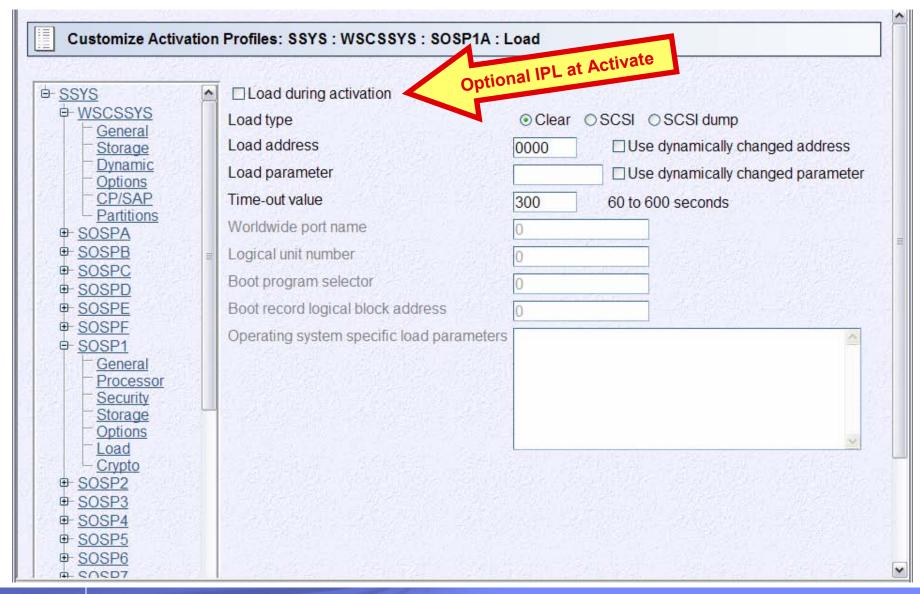


System z9 Image Profile - Options



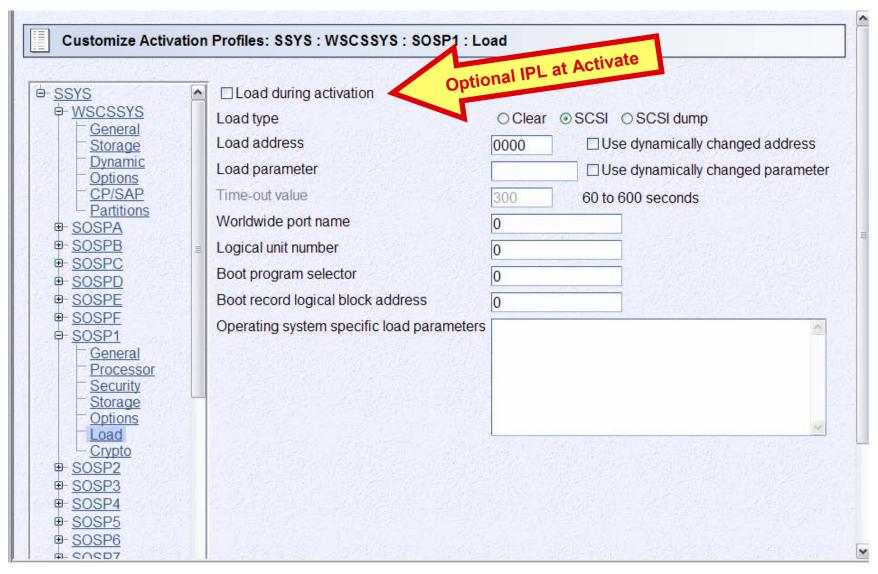


System z9 Image Profile – Load "Classic"



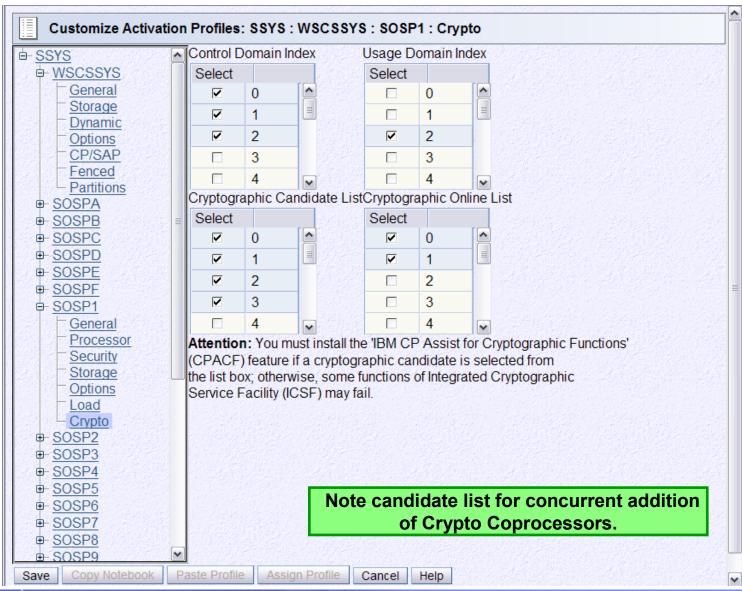


System z9 Image Profile – SCSI Load





System z9 Image Profile - Crypto for Crypto Express2

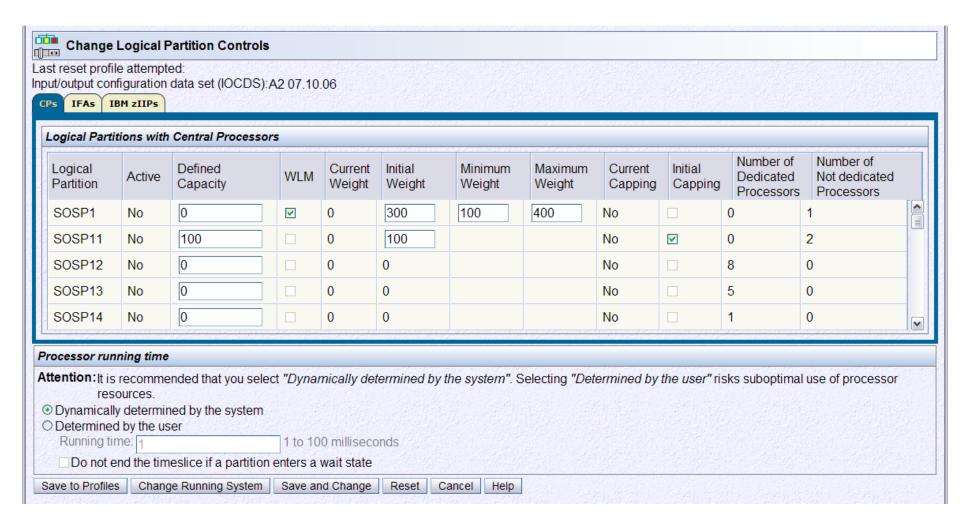




Changing Running Partitions

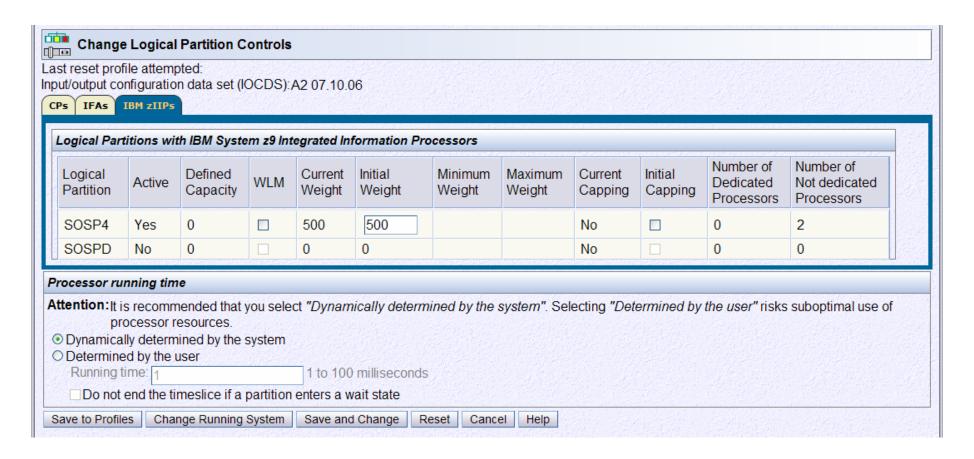


System z9 EC Change LPAR Controls – CP Tab (2006)



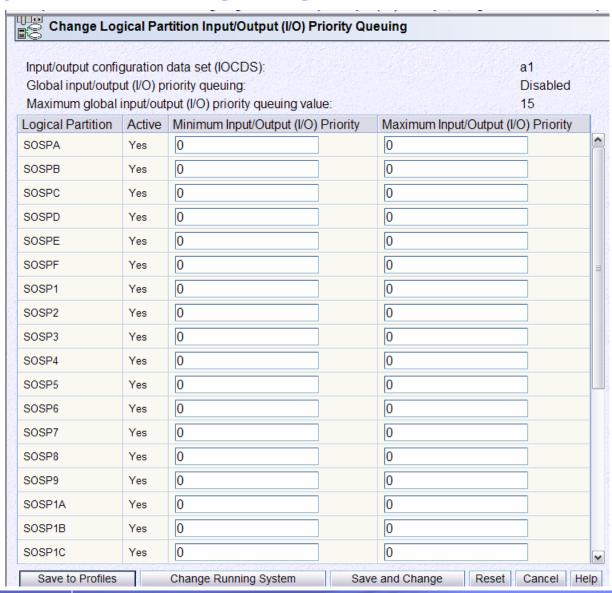


System z9 EC Change LPAR Controls – zIIP Tab (2006)





System z9 Change Logical Partition I/O Priority Queuing





System z9 Memory and Addressability



System z9, z990, and z890 Memory Granularity

- Memory Granularity = Increment Size
 - Storage assignments/reconfiguration and HSA must be an even multiple
 - Physical increment size fixed at 64 MB
 - Expanded memory granularity always 64 MB
 - Central memory granularity is virtualized for each LP
 - LP central memory increment is determined according to the size of the larger of the two central memory elements defined in the activation profile: Initial central memory or Reserved central memory
- Single Storage Pool All central storage
 - ES configured as needed from CS No POR needed
- Review MVS[™] RSU parameter. Large z990 increment size may result in too much memory being reserved for reconfiguration after migration unless the new RSU options introduced in OS/390 2.10 are used.

Large Element Size	Granularity	
64 MB to 32 GB	64 MB	Rare to exceed today
>32 GB to 64 GB	128 MB	
>64 GB to 128 GB	256 MB	z/OS Limit = 128 GB (1.8 = 512 GB)
>128 GB to 256 GB	512 MB	



MVS RSU Parameter for System z9, z990 or z890

- In IEASYSxx. Specifies the number of central storage increments to be made available for central storage reconfiguration
 - MVS attempts to keep this area free of long term fixed pages – calculated as follows for RSU = a number

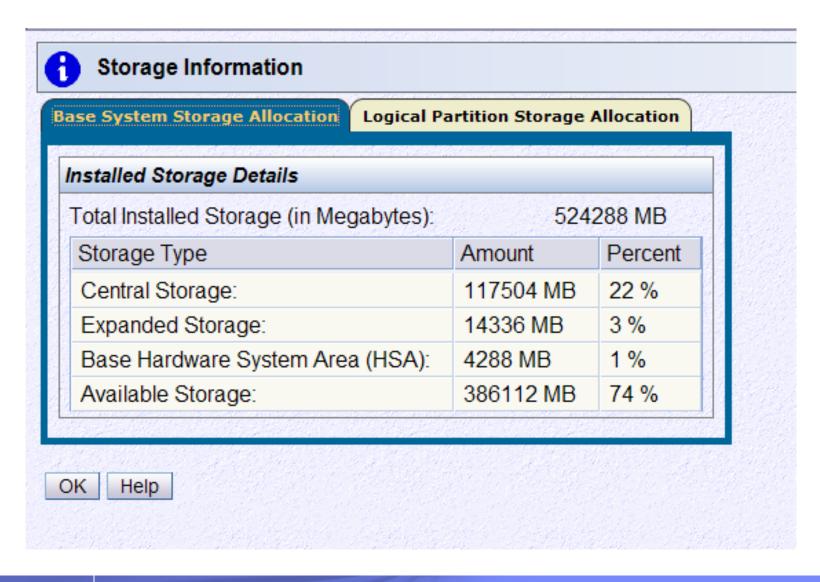
- Or: Storage to be kept free = RSU * increment
 - If element size is upgraded, check the RSU parameter!



- z/OS Recommended RSU coding instead of RSU = a number to allow z/OS to calculate the number of increments
 - RSU = % of storage, MBs or GBs
 - RSU = OFFLINE Amount is Reserved Storage amount.
 No storage is kept free of long term fixed pages except
 Reserved Storage that has been configured ON.



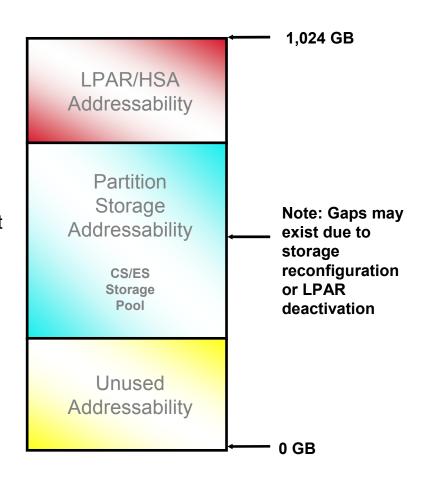
System z9 SE – Base System Storage Allocation





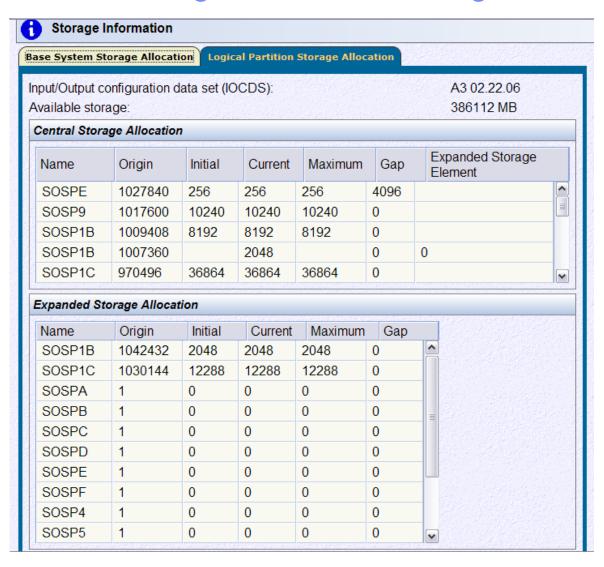
System z9, z990 and z890 Storage Addressability

- Storage Addressability for LPAR/HSA is allocated top down from highest supported address
 - z800, z900 Highest address based on installed memory
 - System z9/990/890 Highest address = 1,024 GB (1 TB), HUGE!
- Partition Storage Addressability for OS, applications, and I/O is assigned below HSA at LPAR activation
 - Origin address is assigned top/down by default but a specific origin can be requested. Default allocation is recommended.
 - All Initial and Reserved CS and ES takes addressability at LPAR activation
 - Addressability for an LPAR must be contiguous
- In Book/Memory Physical Storage
 - LPAR/HSA starts at book/memory physical address 0 (in book 0)
 - EBA can move partition memory and HSA
 - LPAR/HSA size is large
 - Physical storage assigned to LPARs is above this
 - No requirement for LPAR physical storage to be contiguous





System z9 SE - Logical Partition Storage Allocation





System z9 Hardware System Area (HSA)

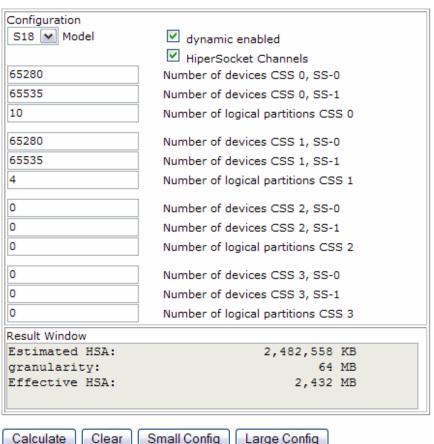


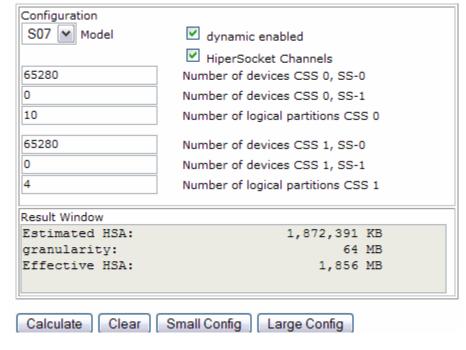
System z9 HSA Estimator tool 2.9.1 on Resource Link

https://www.ibm.com/servers/resourcelink/hom03010.nsf/pages/hsamain?opendocument

z9 EC Model S18 Example

z9 BC Model S07 Example







zEnd

