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# **CSX600 Runtime Software User Guide**

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## ***User Guide***

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## Conventions

Convention	Description
commands	This typeface means that the command must be entered exactly as shown in the text and the [Return] or [Enter] key pressed.
Screen displays	This typeface represents information as it appears on the screen.
[Key] names	Key names appear in the text written with brackets. For example [Return] or [F7]. If it is necessary to press more than one simultaneously, the key names are linked with a plus (+) sign: Press [Ctrl] + [Alt] + [Del]
<b>Bold-face text</b>	Signal names, instructions and register names are displayed in bold. Selections made via the menu hierarchy of a software application.
Words in <i>italicized</i> type	Italics emphasize a point, concept or denote new terms.
	This symbol indicates important information or instructions.

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# 1 Introduction

This document describes the components that make up the CSX600 runtime package. These are:

- Stand-alone host tools to reset the *Advance* accelerator boards and to load and run programs on the boards. See chapter 2, *Running code*, on page 8.
- A standard source code debugger, `csgdb`, is provided to allow debugging of applications running on the *Advance* accelerator boards. See chapter 3, *Debugger Reference*, on page 11.
- A set of diagnostic tools are provided with the runtime and driver software. These can be used to verify the correct installation of hardware and drivers and also to generate diagnostic information if problems are found. See chapter 4, *Diagnostic software reference*, on page 34.
- The host application programming interface and libraries used by a user application on the host to control and communicate with the CSX600 processor. See chapter 5, *Host interface library*, on page 36.
- A kernel level driver is installed at boot time on the host operating system and provides a very low level interface to the CSX600 processor. See chapter 7, *Kernel level driver*, on page 71.

## 2 Running code

To run compiled code on the CSX600 processor or a simulator, a program needs to be run on the host computer to load the code and start it running. As part of the SDK, a simple host program called `csrun` is provided. This will boot the CSX processor(s), load and run the specified CSX executable and then provide host services such as I/O.

Before code is run on the CSX600 processor, it needs to be reset. The command `csreset` (see page 9) can be used to reset one or all of the CSX600 processors in a system.

### 2.1 csrun

`csrun` is a simple system loader that enables executables to be run without the need to create a host application.

#### 2.1.1 Invoking csrun

The command line for `csrun` is:

```
csrun [option]* [filename]
```

Where the *filename* parameter is the executable `.csx` file name. `csrun` will search the paths specified in the `CSPATH` environment variable to find the executable.

**Note:** To run the program `fred.csx` in the *current directory*, the directory *must* be specified (as ``.``), for example:

```
csrun ./fred.csx  (on Linux)
csrun .\fred.csx  (on Windows)
```

#### 2.1.2 Command line options

The `csrun` command line options are summarized in Table 2.1.

Long name	Short name	Valid values	Description
<code>--chip</code>	<code>-c</code>	integer	Select a given chip.
<code>--cstrace</code>		filename	This command is not currently supported.
<code>--host</code>		<i>name or address</i>	Connect to a simulator on a remote host.
<code>--instance</code>	<code>-i</code>	integer	Select a board or a simulator instance.
<code>--sim</code>	<code>-s</code>		Connect to a simulator rather than hardware.

Table 2.1 `csrun` command line options summary

**`-c chip-id`**  
**`--chip chip-id`**

Selects the chip on which to run the code. Chip numbering starts with 0 for the first chip on a board. The default if not specified is 0, the first chip.



**--host *name* | *address***

If the simulator is running on a different host, this option must be used to inform `csr` where the connection should be made.

**-i *number***  
**--instance *number***

Specifies which board or instance of the simulator to connect to. Instance numbers start from 0. The default if not specified is 0.

**-s**  
**--sim**

Specifies that `csr` should connect to a simulator rather than search for hardware.

### Example

To run an executable on the second chip on the third board, use a command of the form:

```
csr -i 2 -c 1 executable_name.csx
```

In order to load an executable that is not on the current `CSPATH` environment path, a full path name can be specified, for example:

```
csr /home/fred/csx/fred.csx
```

## 2.2 csreset

`csreset` provides a means of resetting the CSX600 processor. Under normal circumstances, resetting should only be needed once, at system or simulator startup, when all chips on the boards installed in the system are reset. `csreset` also provides a finer level of control over which boards, or chips on a board, are to be reset.

**Note:** `csreset` should be run after each reboot.

### 2.2.1 Invoking csreset

The command line for `csreset` is:

```
csreset [option]*
```

### 2.2.2 Command line options

The `csreset` command line options are summarized in Table 2.2.

**-A**  
**--all**

Resets all the boards in the system. The `-A` option has no effect if the `-i` option is used.

**-c *chip-id***  
**--chip-id *chip-id***

Selects the chip to reset. Chip numbering starts with 0 for the first chip on a board. If the `-c` option is not used then all chips on the specified boards will be reset.

Long name	Short name	Valid values	Description
--all	-A	none	Reset all boards.
--chip-id	-c	integer	Select a given chip.
--host		name or address	Connect to a simulator on a remote host.
--instance	-i	integer	Select a board or a simulator instance.
--sim	-s		Connect to a simulator rather than hardware.
--system			Perform a full system reset for a given instance.

Table 2.2 *csreset* command line options summary

**--host *name* | *address***

If the simulator is running on a remote host, this option must be used to inform `csreset` where the connection should be made.

**-i *number***

**--instance *number***

Specifies which board or instance of the simulator to connect to. Instance numbers start from 0. The default if not specified is 0.

**-s**

**--sim**

Specifies that `csreset` should connect to a simulator rather than search for hardware.

**--system**

Perform a full system reset.

## Examples

Resetting all boards:

```
csreset -A
```

Resetting all chips on the second board (instance 1):

```
csreset -i 1 -A
```

Resetting all simulated boards:

```
csreset --sim -A
```

## 3 Debugger Reference

This chapter describes the changes made to the standard GDB command set to allow debugging of the CSX architecture.

`csgdb` is a port of the GNU open source debugger GDB to support the CSX family of microprocessors. It has been extended to provide support for the **C** language and the data-parallel architecture of the CSX processors.

The aim in porting GNU GDB to support the CSX architecture is to utilize as much of the standard functionality provided and change only what is required to allow access to novel features of CSX processors. The standard GDB reference manual *Debugging with GDB* [9] is provided with the SDK. This is a comprehensive document that explains how to use all of the standard features of the debugger.

### 3.1 New commands and features

The following new commands have been added to the debugger:

- `connect` command (see *Connect command and options*, on page 12).
- *linked* instructions (see *Disassemble command*, on page 16).
- `peregs` command (see *Reading poly registers – peregs command*, on page 17).
- `pex` command (see *Reading poly memory – pex command*, on page 18).
- `whatis` command displays information about poly values (see *Symbolic debug*, on page 20).
- `sysreg` command (see *System register viewer*, on page 23).
- `semaphores` command (see *TSC semaphore viewer*, on page 26).

In addition to these new commands, it is now also possible to display the enable state (see *Viewing the enable state*, on page 19).

### 3.2 Invoking the debugger

When debugging a “stand-alone” application that runs entirely on the CSX600, use the following command to start the debugger from the command line:

```
csgdb [executable-file]
```

The name of the executable file to be debugged can be passed on the command line.

The debugger will initialize, print a copyright notice and then display a command prompt: `(gdb) .`

#### 3.2.1 Using the debugger with a host application

When the code running on the CSX600 is loaded and used by an application running on the host, then a different method needs to be used to allow both the host application and the debugger to connect to the CSX code.

1. Set the environment variables `CS_CSAPI_DEBUGGER=1` and `CS_CSAPI_DEBUGGER_ATTACH=1`.

Setting `CS_CSAPI_DEBUGGER` initializes the debug interface inside the host application.

Setting `CS_CSAPI_DEBUGGER_ATTACH` allows the user to attach to the device before the host application executes any code, and set a breakpoint.

2. Start the host application and note the *port number* displayed by the host application. The full path of the file loaded will also be displayed by the host application.
3. In another window, start the debugger with a command line of the form:

```
csgdb csx_file_name port_number
```

or:

```
ddd --debugger csgdb csx_file_name port_number
```

4. where *port\_number* is the value that was displayed by the host application.
5. Set a breakpoint in the CSX code where you want to stop.
6. Press **[Run]** in ddd, or use the `r` command in `csgdb`.
7. Press a key in the host application window to allow it to continue.

### 3.3 Commands

The following sections provide an overview of the debugger commands with a detailed description of the new features for the CSX architecture. For clarity, irrelevant output from `csgdb` has been omitted from the following examples. Where necessary, these omissions will be marked with ellipses (`. . .`).

#### 3.3.1 Connect command and options

`csgdb` is a cross-debugger, that is, it runs on a host machine and debugs a program running on another system (the CSX processor). The `connect` command has been added to allow you to specify and initialize the connection between `csgdb` and the remote device. This connection must be made before any code can be executed within the debugger environment.

The `connect` command is executed from the `csgdb` prompt:

```
(gdb) connect
0x80000000 in ?? ()
(gdb)
```

When connected, `csgdb` shows the current program counter (PC) value and the symbolic information associated with that location.

There are a number of options to the `connect` command. These are the same as those used with `cstrun`.

Long name	Short name	Valid values	Description
<code>--chip</code>	<code>-c</code>	integer	Select a given chip.
<code>--host</code>		name or address	Connect to a specified host.
<code>--instance</code>	<code>-i</code>	integer	Select a board or a simulator instance.
<code>--mode</code>	<code>-m</code>	direct or attach	Select the mode of operation.
<code>--sim</code>	<code>-s</code>		Connect to a simulator rather than hardware.

Table 3.1 Connect options summary

**-c *chip-id***  
**--chip *chip-id***

Selects the chip to connect to. Note that chip numbering starts with 1, as opposed to instancing, which is zero based. The default is 1.

**--host *name* | *address***

If the daemon is running on a different host than the `localhost`, this option must be used to inform `csgdb` where the connection should be made.

**-i *number***  
**--instance *number***

Specifies which instance of the board or the simulator to connect to. Instancing is 0 based. The default is 0.

**-m *direct* | *attach***  
**--mode *attach* | *attach***

Specifies which mode `csgdb` should use to access the device. The default mode is `direct`. By specifying `attach` the device can be accessed via the debug interface built into CSAPI host applications.

**-s**  
**--sim**

Specifies that `csgdb` should connect to a simulator rather than search for hardware.

### 3.3.2 Loading code

The executable code to be debugged needs to be loaded into the target device. This can be done in two ways.

- If the executable file name has been passed to the debugger on the command line, it can be loaded by using the `load` command with no arguments:

```
csgdb cfitest.csx
...
(gdb) connect
0x80000000 in __FRAME_BEGIN_MONO__ ()
(gdb) load
(gdb)
```

- The executable file name can also be provided as an argument to the `load` command:

```
csgdb
...
(gdb) connect
0x80000000 in ?? ()
(gdb) load /csxtests/cfitest.csx
(gdb)
```

### 3.3.3 Executing code

Once code is loaded you can start the code executing. The `continue` command, `c`, will start the code from the entry point and run until the program terminates:

```
csgdb
...
(gdb) connect
0x80000000 in ?? ()
(gdb) load /csxtests/cfittest.csx
(gdb) c
Continuing.

Mtap 0 has terminated.
Program exited normally.
(gdb)
```

The code can also be single stepped at the instruction or source code statement level by using the `stepi` or `step` commands. Subroutine branches can be stepped over at the instruction or source code level by using the `nexti` or `next` command.

The command `run` performs the equivalent of a `load` followed by `continue` and is very useful for starting or restarting program execution.

### 3.3.4 Mono debugging

`csgdb` supports the debugging of mono code at both the instruction and source code level. The standard GDB commands documented in *Debugging with GDB* [9] are all available for use when debugging mono code.

#### Reading mono registers – `regs` command

The `regs` command is used for reading mono registers and takes a *size* parameter to allow the registers to be viewed as 2, 4 or 8-byte values. The register size is optional. If not supplied, it defaults to 2 bytes.

For example, to display mono registers as 4-byte values:

```
(gdb) regs 4
pc      0x8001380c
ret     0x80013898
pred    0x0035
0m4     0x80000000
4m4     0x0
8m4     0x0
12m4    0x0
...
56m4    0x0
60m4    0x80013898
(gdb)
```

The three other registers (`pc`, `ret` and `pred`) are provided whenever the mono registers are displayed. These correspond to the program counter, the function return address and the predicates register.

Individual registers can be specified within the GDB command language by using register names displayed by the `regs` or `peregs` command. The register names must be preceded by a `$`. The register names correspond to the names in the assembler syntax for registers, without the colon (`:`). That is, `0:m4` becomes `$0m4` in the debugger command language syntax.

For a detailed description of the assembly mapping, refer to *Registers*, on page 32.

For example:

```
(gdb) p/x $16m4
$1 = 0x8001380c
(gdb) p/x $ret+8
$2 = 0x800138a0
(gdb)
```

For further information on the GDB command language variables, see *Debugging with GDB* [9].

## Writing mono registers – `regs` command

The debugger supports the setting of values into mono registers via the command language. This is done using the following syntax:

```
(gdb) set $16m4=0x11223344
(gdb) p/x $16m4
$1 = 0x11223344
(gdb) set $16m2=0x8899
(gdb) p/x $16m4
$2 = 0x11228899
```

The values are written to the device on a restart such as a step or continue. Currently only mono registers can be written.

## Reading mono memory – `x` command

The standard GDB `x` command is used to read mono memory and can be used to display a variety of formats.

### Examples of using the `x` command

Displaying 3 words from address `0x80000000`:

```
(gdb) x/3w 0x80000000
0x80000000 <__FRAME_BEGIN_MONO__>:      0x00000000      0x80013898      0x00000000
(gdb)
```

Displaying 10 bytes from the current PC:

```
(gdb) x/10b $pc
0x8001380c <main+20>:      0x00      0x00      0x80      0x30      0x00      0x00      0x80      0x30
0x80013814 <main+28>:      0x0a      0x00
(gdb)
```

The symbolic register names can be passed to this command and also symbolic information from the code.

Displaying 10 bytes from `main()`:

```
(gdb) x/10b main
0x800137f8 <main>:      0x00      0x00      0xa0      0x11      0x80      0x79      0x78      0x88
0x80013800 <main+8>:      0x80      0x59
(gdb)
```

The `x` command can also be used to list instructions in the following way:

```
(gdb) x/11i main
0x800137f8 <main>:      st          0:m4          16:m4
0x800137fc <main+4>:    mono.result.get 60:m2          Return_Lo
0x80013800 <main+8>:    mono.result.get 62:m2          Return_Hi
0x80013804 <main+12>:   st          0:m4          60:m4          0x4
0x80013808 <main+16>:   st          4:m2          8:p4
0x8001380c <main+20>:   nop.poly
0x80013810 <main+24>:   nop.poly
0x80013814 <main+28>:   mono.immed    0xa              \
0x80013818 <main+32>:   mov          0:p2          mono_immediate /
0x8001381c <main+36>:   mono.immed    0x0              \
0x80013820 <main+40>:   mov          2:p2          mono_immediate /
(gdb)
```

## Disassemble command

The `x` command can be used to display a number of instructions but the `disassemble` command can be used to display the assembler instructions for a whole function.

For example, to disassemble the whole of the function `main()`:

```
(gdb) disassemble main
Dump of assembler code for function main:
0x800137f8 <main+0>:      st          0:m4          16:m4
0x800137fc <main+4>:    mono.result.get 60:m2          Return_Lo
0x80013800 <main+8>:    mono.result.get 62:m2          Return_Hi
0x80013804 <main+12>:   st          0:m4          60:m4          0x4
0x80013808 <main+16>:   st          4:m2          8:p4
...
0x80013858 <main+96>:   ld          16:m4          0:m4
0x8001385c <main+100>:  ld          60:m4          0:m4          0x4
0x80013860 <main+104>:  j.lo         60:m2              \
0x80013864 <main+108>:  j.hi         62:m2              /
End of assembler dump.
(gdb)
```

The `>` marks at the end of a pair of lines denote *linked* instructions. When you set a breakpoint on the second half of the linked pair, the debugger will automatically move it back one instruction. When you attempt to single step over the first part of a linked instruction, the debugger will not return until all parts of the sequence have been executed.

## Breakpoints

Breakpoints are used to stop execution of code at a particular point of interest. GDB has good support for various different kinds of breakpoints and these are well documented in *Debugging with GDB* [9].

As you have full symbolic debug you can set a breakpoint on a function call. For example:

```
(gdb) b main
Breakpoint 1 at 0x8001380c: file cfitest.cn, line 27.
(gdb) c
Continuing.
Breakpoint 1, main () at cfitest.cn:27
27      poly int value = 10;
(gdb)
```



Breakpoints can also be set on a specific address in the code, as shown below:

```
Breakpoint 1, main () at cfittest.cn:27
27      poly int value = 10;
(gdb) x/4i $pc
0x8001380c <main+20>:  nop.poly
0x80013810 <main+24>:  nop.poly
0x80013814 <main+28>:  mono.immed      0xa
0x80013818 <main+32>:  mov              0:p2      mono_immediate      /
(gdb) b *0x80013814
Breakpoint 2 at 0x80013814: file cfittest.cn, line 27.
(gdb) c
Continuing.
Breakpoint 2, 0x80013814 in main () at cfittest.cn:27
27      poly int value = 10;
(gdb)
```

## Symbolic debug

Full symbolic debug of functions and variables is available for mono types. Objects can be viewed using the standard commands described in *Debugging with GDB* [9].

The most common way of viewing symbolic data is with the `print` (or `p`) command.

For example, printing a mono integer variable:

```
Breakpoint 1, main () at csgdb_example.cn:22
22      mono_int++;
(gdb) print mono_int
$1 = 1
(gdb)
```

Printing a mono array and an array element:

```
Breakpoint 1, main () at csgdb_example.cn:22
22      mono_int++;
(gdb) p mono_int_array
$1 = {1000, 1001, 1002, 1003}
(gdb) p mono_int_array[3]
$2 = 1003
(gdb)
```

### 3.3.5 Poly debugging

`csgdb` has been extended to provide support for the poly multiplicity specifier in the **C** language and to allow visibility of the processing elements (PEs) in the CSX architecture. As with mono data, *Debugging with GDB* [9] provides the majority of the command descriptions for debugging poly code but there are some additional commands which are documented here.

#### Reading poly registers – `peregs` command

This command is the poly equivalent of the `regs` command.

The `peregs` command is used for reading poly registers and takes a *size* parameter to allow the registers to be viewed as 1, 2, 4 or 8-byte values. The register size is optional. If not supplied, it defaults to 1 byte.

Also, the poly registers are special as they encapsulate the value from all of the PEs in a single register value. If the value is the same across the whole array, the value will only be printed once but the debugger will inform you how many times the value repeats.

For example, displaying poly registers as 4-bytes values:

```
(gdb) pereg4
0p4      {0x34 <repeats 96 times>}
4p4      {0x34 <repeats 96 times>}
8p4      {0x0, 0x1, 0x2, 0x3, 0x4, 0x5, 0x6, 0x7, 0x8, 0x9, 0xa, 0xb, 0xc, 0xd, 0xe,
0xf, 0x10, 0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1a, 0x1b, 0x1c,
0x1d, 0x1e, 0x1f, 0x20, 0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2a,
0x2b, 0x2c, 0x2d, 0x2e, 0x2f, 0x30, 0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38,
0x39, 0x3a, 0x3b, 0x3c, 0x3d, 0x3e, 0x3f, 0x40, 0x41, 0x42, 0x43, 0x44, 0x45, 0x46,
0x47, 0x48, 0x49, 0x4a, 0x4b, 0x4c, 0x4d, 0x4e, 0x4f, 0x50, 0x51, 0x52, 0x53, 0x54,
0x55, 0x56, 0x57, 0x58, 0x59, 0x5a, 0x5b, 0x5c, 0x5d, 0x5e, 0x5f, 0x60, 0x61, 0x62,
0x63, 0x64, 0x65, 0x66, 0x67}
...
116p4    {0xdeaddddd <repeats 96 times>}
120p4    {0xdeadeeee <repeats 96 times>}
124p4    {0xdeadffff <repeats 96 times>}
(gdb)
```

As with the `regs` command the register names are available in the command language. The poly registers are accessed from the command line with the name preceded by a `$`, as shown below:

```
(gdb) print/d $8p4
$3 = {0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21,
22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42,
43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63,
64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84,
85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95}
(gdb)
```

The poly registers can also be indexed to view the value on an individual PE. In this example, the values of register 8 on PEs 10 and 84 are displayed:

```
(gdb) print/d $8p4[10]
$4 = 10
(gdb) print/d $8p4[84]
$5 = 84
(gdb)
```

## Reading poly memory – `pex` command

This command is the poly equivalent of the standard `x` command.

The `pex` command is used to display memory across a range of PEs using the same formats available with the `x` command. Symbolic names can also be passed to the `pex` command.

### Examples of the `pex` command

Printing a word in decimal from the address `0x28` on PEs 0 to 20:

```
(gdb) pex/dw 0x28 0..20
(PE 0)    0x28 <__FRAME_BEGIN_POLY__+40>:      1000
(PE 1)    0x28 <__FRAME_BEGIN_POLY__+40>:      1001
(PE 2)    0x28 <__FRAME_BEGIN_POLY__+40>:      1002
...
(PE 18)   0x28 <__FRAME_BEGIN_POLY__+40>:      1018
(PE 19)   0x28 <__FRAME_BEGIN_POLY__+40>:      1019
(PE 20)   0x28 <__FRAME_BEGIN_POLY__+40>:      1020
(gdb)
```

Printing 4 bytes from address &poly\_int\_array on PEs 50 to 75:

```
(gdb) pex/4b &poly_int_array 50..75
(PE 50) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x1a    0x04    0x00    0x00
(PE 51) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x1b    0x04    0x00    0x00
(PE 52) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x1c    0x04    0x00    0x00
...
(PE 73) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x31    0x04    0x00    0x00
(PE 74) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x32    0x04    0x00    0x00
(PE 75) 0x28 <__FRAME_BEGIN_POLY__+40>:      0x33    0x04    0x00    0x00
(gdb)
```

## Viewing the enable state

When debugging applications that use poly conditional code, it is useful to see the enable state of the PEs. In csgdb, the enable state is mapped into a register name \$enable. This register can be viewed using the standard print command.

### Displaying the PE enable state info

Printing the enable state at main, where all PEs are enabled:

```
Breakpoint 1, main () at csgdb_example.cn:4
4      mono_int mono_int = 0;
(gdb) p/x $enable
$1 = {0xff <repeats 96 times>}
(gdb)
```

Printing the enable state inside a poly conditional:

```
Breakpoint 2, main () at csgdb_example.cn:23
23     mono_int++;
(gdb) p/x $enable
$2 = {0xfe <repeats 48 times>, 0xff <repeats 48 times>}
(gdb)
```

Printing the enable state as binary to see all eight levels within each PE:

```
(gdb) p/t $enable
$3 = {11111110 <repeats 48 times>, 11111111 <repeats 48 times>}
(gdb)
```

Printing a simplified view of the enable state can be done as follows with + denoting enabled and – denoting disabled:

```
Breakpoint 4, main () at simple.cn:8
8      X = 10;
(gdb) p $enabled
$1 = '-' <repeats 49 times>, '+' <repeats 47 times>
(gdb)
```

The number of enabled PEs can be displayed by doing the following:

```
(gdb) p $numenb
$3 = 47
```

In a similar fashion, the total number of PEs can be retrieved:

```
(gdb) p $numpes
$4 = 96
```

The number of disabled PEs can be worked out using the command language:

```
(gdb) p $numpes-$numenb
$5 = 49
```

### Displaying the PE mac status info

This is done by viewing the following command language variable:

```
(gdb) p/x $status
$6 = {0xc9 <repeats 48 times>, 0xd2, 0xc2 <repeats 47 times>}
(gdb) p/t $status
$7 = {11001001 <repeats 48 times>, 11010010, 11000010 <repeats 47 times>}
(gdb)
```

### Displaying the PE fpadd status

The fpadd status is displayed by viewing the following command language variable:

```
(gdb) p/x $fpadd
$8 = {0xc0 <repeats 96 times>}
(gdb) p/t $fpadd
$9 = {11000000 <repeats 96 times>}
```

### Displaying the PE fpmul status

To see the current fpmul status, the following command should be issued:

```
(gdb) p/x $fpmul
$10 = {0xc2 <repeats 96 times>}
(gdb) p/t $fpmul
$11 = {11000010 <repeats 96 times>}
```

### Symbolic debug

Full symbolic debug of variables is available for poly types. Objects can be viewed using the standard commands described in the GDB reference value. Poly variables are special inside the debugger as they are expanded to display the value on every PE.

For example displaying the value of a poly integer produces the following result:

```
(gdb) p poly_int
$4 = {0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21,
22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42,
43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63,
64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84,
85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95}
(gdb)
```

The value of the variable `poly_int` from each PE is displayed.

A poly array produces similar results with the array values displayed from each PE. For example, to display a four element poly integer array.

```
(gdb) p poly_int_array
$2 = {{1000, 1001, 1002, 1003, 1004, 1005, 1006, 1007, 1008, 1009, 1010, 1011, 1012,
1013, 1014, 1015, 1016, 1017, 1018, 1019, 1020, 1021, 1022, 1023, 1024, 1025, 1026,
1027, 1028, 1029, 1030, 1031, 1032, 1033, 1034, 1035, 1036, 1037, 1038, 1039, 1040,
1041, 1042, 1043, 1044, 1045, 1046, 1047, 1048, 1049, 1050, 1051, 1052, 1053, 1054,
1055, 1056, 1057, 1058, 1059, 1060, 1061, 1062, 1063, 1064, 1065, 1066, 1067, 1068,
```

```

1069, 1070, 1071, 1072, 1073, 1074, 1075, 1076, 1077, 1078, 1079, 1080, 1081, 1082,
1083, 1084, 1085, 1086, 1087, 1088, 1089, 1090, 1091, 1092, 1093, 1094, 1095}, {2000,
2002, 2004, 2006, 2008, 2010, 2012, 2014, 2016, 2018, 2020, 2022, 2024, 2026, 2028,
2030, 2032, 2034, 2036, 2038, 2040, 2042, 2044, 2046, 2048, 2050, 2052, 2054, 2056,
2058, 2060, 2062, 2064, 2066, 2068, 2070, 2072, 2074, 2076, 2078, 2080, 2082, 2084,
2086, 2088, 2090, 2092, 2094, 2096, 2098, 2100, 2102, 2104, 2106, 2108, 2110, 2112,
2114, 2116, 2118, 2120, 2122, 2124, 2126, 2128, 2130, 2132, 2134, 2136, 2138, 2140,
2142, 2144, 2146, 2148, 2150, 2152, 2154, 2156, 2158, 2160, 2162, 2164, 2166, 2168,
2170, 2172, 2174, 2176, 2178, 2180, 2182, 2184, 2186, 2188, 2190}, {3000, 3003, 3006,
3009, 3012, 3015, 3018, 3021, 3024, 3027, 3030, 3033, 3036, 3039, 3042, 3045, 3048,
3051, 3054, 3057, 3060, 3063, 3066, 3069, 3072, 3075, 3078, 3081, 3084, 3087, 3090,
3093, 3096, 3099, 3102, 3105, 3108, 3111, 3114, 3117, 3120, 3123, 3126, 3129, 3132,
3135, 3138, 3141, 3144, 3147, 3150, 3153, 3156, 3159, 3162, 3165, 3168, 3171, 3174,
3177, 3180, 3183, 3186, 3189, 3192, 3195, 3198, 3201, 3204, 3207, 3210, 3213, 3216,
3219, 3222, 3225, 3228, 3231, 3234, 3237, 3240, 3243, 3246, 3249, 3252, 3255, 3258,
3261, 3264, 3267, 3270, 3273, 3276, 3279, 3282, 3285}, {4000, 4004, 4008, 4012, 4016,
4020, 4024, 4028, 4032, 4036, 4040, 4044, 4048, 4052, 4056, 4060, 4064, 4068, 4072,
4076, 4080, 4084, 4088, 4092, 4096, 4100, 4104, 4108, 4112, 4116, 4120, 4124, 4128,
4132, 4136, 4140, 4144, 4148, 4152, 4156, 4160, 4164, 4168, 4172, 4176, 4180, 4184,
4188, 4192, 4196, 4200, 4204, 4208, 4212, 4216, 4220, 4224, 4228, 4232, 4236, 4240,
4244, 4248, 4252, 4256, 4260, 4264, 4268, 4272, 4276, 4280, 4284, 4288, 4292, 4296,
4300, 4304, 4308, 4312, 4316, 4320, 4324, 4328, 4332, 4336, 4340, 4344, 4348, 4352,
4356, 4360, 4364, 4368, 4372, 4376, 4380}}
(gdb)

```

An individual array element can be viewed as follows:

```

(gdb) p poly_int_array[2]
$4 = {3000, 3003, 3006, 3009, 3012, 3015, 3018, 3021, 3024, 3027, 3030, 3033, 3036,
3039, 3042, 3045, 3048, 3051, 3054, 3057, 3060, 3063, 3066, 3069, 3072, 3075, 3078,
3081, 3084, 3087, 3090, 3093, 3096, 3099, 3102, 3105, 3108, 3111, 3114, 3117, 3120,
3123, 3126, 3129, 3132, 3135, 3138, 3141, 3144, 3147, 3150, 3153, 3156, 3159, 3162,
3165, 3168, 3171, 3174, 3177, 3180, 3183, 3186, 3189, 3192, 3195, 3198, 3201, 3204,
3207, 3210, 3213, 3216, 3219, 3222, 3225, 3228, 3231, 3234, 3237, 3240, 3243, 3246,
3249, 3252, 3255, 3258, 3261, 3264, 3267, 3270, 3273, 3276, 3279, 3282, 3285}
(gdb)

```

The symbolic names of poly variables can also be passed to the `pex` command. It will use the address of the variable and display the data accordingly.

The standard GDB `whatis` command will display the type and multiplicity of a variable:

```

(gdb) whatis poly_int_array
type = poly int [4]<96 PEs>
(gdb)

```

The information `<96 PEs>` describes how many elements the variable is visible over and the type displays the poly keyword.

### 3.3.6 Hardware threads

`csgdb` has extended the threading support in GDB to allow it to debug the threads supported by the hardware. After loading an application with multiple threads the debugger can see the state of all threads in the system.

The state of the threads can be seen by using the `info threads` command:

```
(gdb) info threads
* 8 Software Thread 7  0x02003b18 in _start7 ()
  7 Software Thread 6  _start6 () at thread_test.is:68
  6 Software Thread 5  _start5 () at thread_test.is:87
  5 Software Thread 4  _start4 () at thread_test.is:102
  4 Software Thread 3  _start3 () at thread_test.is:117
  3 Software Thread 2  _start2 () at thread_test.is:132
  2 Software Thread 1  _start1 () at thread_test.is:147
  1 Software Thread 0  _start () at thread_test.is:162
0x02003b18 in _start7 ()
(gdb)
```

The currently executing thread is marked with `*`.

Each thread has an identifier assigned to it by `csgdb` and these are used to select which thread to view. The software thread number is displayed after the GDB identifier. In the example above, the currently executing thread has the GDB identifier 8 and is software thread 7.

The `thread` command is used to select a thread.

```
(gdb) (gdb) thread 2
[Switching to thread 2 (Software Thread 1)]#0  _start1 () at thread_test.is:147
147  sem.wait SEM_SIG6
(gdb) list
142  sem.wait SEM_THREAD_FINISH
143
144 _start1::
145 global _start1
146
147  sem.wait SEM_SIG6
148  mov 32:m2, 0
149 _testloop6::
150 global _testloop6
151  j.ifn 32:m2, _testloop6
(gdb)
```

All following commands will be applied to the currently selected thread unless otherwise specified. For example, commands can be executed on all threads in the following way:

```
(gdb) thread apply all x/i $pc

Thread 8 (Software Thread 7):
0x2003b18 <_start7>:sem.put          77          4

Thread 7 (Software Thread 6):
0x200312c <_start6>:mov              16:m2          0x20

Thread 6 (Software Thread 5):
0x200317c <_start5>:sem.select       36

Thread 5 (Software Thread 4):
0x20031b0 <_start4>:sem.select       37
```

```

Thread 4 (Software Thread 3):
0x20031e4 <_start3>:sem.select      38

Thread 3 (Software Thread 2):
0x2003218 <_start2>:sem.select      39

Thread 2 (Software Thread 1):
0x200324c <_start1>:sem.select      3A

Thread 1 (Software Thread 0):
0x2003280 <_start>:sem.select       3B
#0  0x02003b18 in _start7 ()

```

You can change the view of whether csgdb displays the software or hardware mapping of the threads. To view the hardware mapping, use the command:

```
set print cs_hardware_thread_view on
```

When any of the thread commands are used with this option set the debugger will display the hardware thread identifiers.

```

(gdb) info threads
* 8 Hardware Thread 0  0x02003b18 in _start7 ()
  7 Hardware Thread 1  _start6 () at thread_test.is:68
  6 Hardware Thread 2  _start5 () at thread_test.is:87
  5 Hardware Thread 3  _start4 () at thread_test.is:102
  4 Hardware Thread 4  _start3 () at thread_test.is:117
  3 Hardware Thread 5  _start2 () at thread_test.is:132
  2 Hardware Thread 6  _start1 () at thread_test.is:147
  1 Hardware Thread 7  _start () at thread_test.is:162
0x02003b18 in _start7 ()

```

To disable this mode and to view the software mapping, use the command:

```
set print cs_hardware_thread_view off
```

### 3.3.7 System register viewer

The debugger lets you view the system register present in the CSX600 device. The values and fields contained within them are presented in a form that can be clearly understood. To display this information, use the command `sysreg` which has been added to csgdb.

#### Getting help in csgdb

The `sysreg` command has limited online help which can be accessed with the `help` argument to `sysreg`.

```

(gdb) sysreg help

***** MTAP system register viewer help *****

Command      Arguments                                     Description
-----
list          {reg group} | {reg full name}               List system register information
              {reg group} {reg name}
display      {reg group} | {reg full name}               Display system register values
              {reg group} {reg name}

(gdb)

```

The help option lists the other available options to the command and the arguments that go with them.

### Listing system register information

The `list` option lets you view the information about the register groups, registers contained within the groups and the bit field information for individual registers. For example, to get a list of all registers groups you would use the `list` option but with no arguments.

```
(gdb) sysreg list

System register group list :

Group Name
-----
CCBRS
CCBRS_P0
CCBRS_P1
ISU
ISU_GIU
ISU_GSU
ISU_IG
LMI
LMI_DMA
LMI_DMA_AEU
LMI_LMICOM
LMI_LMIRIF
LMI_LMISRV
MTAP
MTAP_AC
MTAP_AC_DB
MTAP_AC_DB_CM
MTAP_AC_IT
MTAP_AC_MS
MTAP_GPIOC0
MTAP_GPIOC0_GPIOC
MTAP_GPIOE0
MTAP_GPIOE0_GPIOE
MTAP_TSC
MTAP_TSC_ICACHE
MTAP_TSC_ICACHE_IBUFFER
MTAP_TSC_LSU
MTAP_TSC_SCHED
MTAP_TSC_SEM
MTAP_TSC_TP
MTAP_TSC_TP_DP
MTAP_TSC_TP_TPREG
SYS

(gdb)
```

This command lists all the available register groups that are visible to the debugger.

### Viewing the information about a register group

You can view the register groups themselves by passing the name of the group along with the `list` argument. The register group names are displayed when you use the `list` argument on its own.



For example, to display the contents of the `MTAP_TSC_SCHED` group, enter the following command:

```
(gdb) sysreg list MTAP_TSC_SCHED

System register list for group MTAP_TSC_SCHED :

Register Name
-----
CONTROL
STATUS
SWITCH_THREAD

(gdb)
```

This lists the register names that are contained within the group.

### Listing the information about a specific register

The `list` option also lets you display information about a specific register and this is done by passing both the group and register names along with the `list` argument.

For example, viewing the information for the `STATUS` register of the `MTAP_TSC_SCHED` group is done as follows:

```
(gdb) sysreg list MTAP_TSC_SCHED STATUS

System register definition
-----
Name :          STATUS
Group :         MTAP_TSC_SCHED
Description :    Status of each thread
Reset Value :   255
Address :       0x102

Bit Fields
-----
Name :  READY
First : 0
Last  : 7
Width : 8

Name :  THREAD
First : 16
Last  : 18
Width : 3

Name :  YIELD
First : 8
Last  : 15
Width : 8

(gdb)
```

This command will list the definition and bit field information for the requested register.

The register can also be accessed by joining the group and register name together.

```
(gdb) sysreg list MTAP_TSC_SCHED_STATUS
```

The above command will produce the same output for the register.

## Displaying system register values

As well as displaying the information regarding the make up of each of the groups and registers, it is possible to display the values contained within them. The values can be displayed at a group level or also at an individual register level to allow the bit-field values to be displayed. This is done by using the `display` argument to the `sysreg` command.

### Displaying the values of a register group

The values of all registers contained within a register group can be displayed using the `display` argument with a valid register group name.

```
(gdb) sysreg display MTAP_TSC_SCHED

System register display for group MTAP_TSC_SCHED :

Register Name          Value
-----
CONTROL                0x8
STATUS                 0x70000
SWITCH_THREAD          0x7

(gdb)
```

### Displaying the value of an individual register

The value of a register can be displayed by passing a valid group and register name with the `display` argument.

```
(gdb) sysreg display MTAP_TSC_SCHED STATUS

System register display for MTAP_TSC_SCHED_STATUS :

Value : 0x70000 0b00000000000000111000000000000000

Bit Fields          Value
-----
READY              0x0
THREAD             0x7
YIELD              0x0

(gdb)
```

The register value can also be accessed by using the full name as with the `list` command.

```
(gdb) sysreg display MTAP_TSC_SCHED_STATUS
```

### 3.3.8 TSC semaphore viewer

When synchronizing code between the TSC and the host processor or between separate threads within the TSC, semaphores are used. The debugger lets you list the information about the semaphores of the TSC it is connected to. The command, `semaphores`, has been added to `csgdb` to let you view the semaphore information. The command can also be shortened to `sem` and the command language will take care of extending the name.

## Getting help in csgdb

The `semaphores` command has limited online help which can be accessed with the `help` argument to `semaphores`.

```
(gdb) sem help

***** TSC semaphore viewer help *****

Use help {command} for more detailed command specific instruction.
```

Command	Arguments	Description
<code>display</code>	<code>{semaphore}   all   allval</code>	Display all semaphore information
<code>value</code>	<code>{semaphore}   all   allval</code>	Display the current semaphore value(s)
<code>nonzero</code>	<code>{semaphore}   all   allnon</code>	Display the current nonzero status(s)
<code>interrupt</code>	<code>{semaphore}   all   allint</code>	Display the current interrupt enable
<code>status(s)</code>		
<code>overflow</code>	<code>{semaphore}   all   allovr</code>	Display the current overflow status
<code>thread</code>	<code>{thread}   all</code>	Display the current thread / semaphore use

```
(gdb)
```

## Displaying semaphore information

Using the `semaphores` command with the `display` option lists all information about a particular set of semaphores. The arguments to this command can be an individual semaphore, all semaphores or all semaphores which currently have a value. Use the `all` argument to list the information. For example, to display information about all TSC semaphores is done as follows:

```
(gdb) sem display all
```

Semaphore	Value	NonZero	Interrupt	Overflow
0	0	0	0	0
1	0	0	0	0
2	0	0	0	0
3	0	0	0	0
4	0	0	0	0
...				
...				
...				
123	0	0	0	0
124	0	0	0	0
125	0	0	0	0
126	0	0	0	0
127	0	0	1	0

There are 128 semaphores and this command lists all the information about all of them.

### Listing information for all semaphores with a current value

The `semaphores` command with the `display` argument can be used to list information about only those semaphores with a value. This is done by passing the `allval` argument to the command.

```
(gdb) sem display allval
```

Semaphore	Value	NonZero	Interrupt	Overflow
-----	----	-----	-----	-----
30	3	1	0	0
34	2	1	0	0
36	1	1	0	0
39	4	1	0	0
45	1	1	0	0
55	2	1	0	0
56	1	1	0	0
68	1	1	0	0
119	4	1	0	0

### Listing semaphore information for an individual semaphore

To limit the information displayed, the debugger can list information for an individual semaphore. This is done by passing the semaphore number to the `semaphores display` command.

```
(gdb) sem display 34
```

Semaphore	Value	NonZero	Interrupt	Overflow
-----	----	-----	-----	-----
34	2	1	0	0

### Displaying only the values of semaphores

The following examples show the use of the `value` argument to the `semaphores` command. Use the options as follows:

- `all` displays the current value of all semaphores.
- `allval` displays the values for the semaphores which have a value greater than 0.
- A semaphore number displays the information for just that numbered semaphore.

```
(gdb) sem value all
```

Semaphore	Value
-----	----
0	0
1	0
2	0
3	0
4	0
...	
...	
...	
123	0
124	0
125	0
126	0
127	0

```
(gdb) sem value allval
```

Semaphore	Value
-----	-----
30	3
34	2
36	1
39	4
45	1
55	2
56	1
68	1
119	4

```
(gdb) sem value 30
```

Semaphore	Value
-----	-----
30	3

### Displaying only the nonzero status of the semaphores

The debugger can list the nonzero status of each of the semaphores. This is done by passing the `nonzero` option to the `semaphores` command. The arguments that work with the `nonzero` option are:

- `all` to list the nonzero fields for all semaphores.
- `allnon` to list just those with values in the nonzero field.
- An individual semaphore number.

```
(gdb) sem nonzero all
```

Semaphore	NonZero
-----	-----
0	1
1	1
2	1
3	1
4	1
...	
...	
...	
123	1
124	1
125	1
126	1
127	0

```
(gdb) sem nonzero allnon
```

Semaphore	NonZero
-----	-----
0	1
1	1
2	1
3	1
4	1
...	
...	
...	
122	1
123	1
124	1
125	1
126	1

```
(gdb) sem nonzero 125
```

Semaphore	NonZero
-----	-----
125	1

### Displaying the interrupt enable status of the semaphores

It is possible to list the interrupt enable status of each of the semaphores. This is done by passing the `interrupt` option to the `semaphores` command. The arguments that work with the `nonzero` option are:

- `all` to list the interrupt fields for all semaphores.
- `allint` to list just those with values in the interrupt field.
- An individual semaphore number.

```
(gdb) sem interrupt all
```

Semaphore	Interrupt
-----	-----
0	0
1	0
2	0
3	0
4	0
...	
...	
...	
123	0
124	0
125	0
126	0
127	1

```
(gdb) sem interrupt allint
```

Semaphore	Interrupt
-----	-----
127	1

```
(gdb) sem interrupt 120
```

Semaphore	Interrupt
-----	-----
120	0

## Displaying the overflow status of the semaphores

The debugger can display the overflow status of each of the semaphores. This is done by passing the `overflow` option to the `semaphores` command. The arguments that work with the `overflow` option are:

- `all` to list the overflow fields for all semaphores.
- `allovr` to list just those with values in the overflow field.
- An individual semaphore number.

```
(gdb) sem overflow all
```

Semaphore	Overflow
-----	-----
0	0
1	0
2	0
3	0
4	0
...	
...	
...	
123	0
124	0
125	0
126	0
127	0

```
(gdb) sem overflow allovr
```

Semaphore	Overflow
-----	-----
10	1
18	1
123	1

```
(gdb) sem overflow 20
```

Semaphore	Overflow
-----	-----
20	0

### Displaying the current thread / semaphore usage

It is also possible to view the semaphore usage for the TSC threads so that you can see which semaphore number is currently selected. This is done using the `thread` argument to the `semaphores` command. The `thread` argument takes sub options of either `all` or a semaphore number to specify what to display.

```
(gdb) sem thread all
```

Thread	Semaphore
-----	-----
0	10
1	23
2	43
3	101
4	102
5	32
6	22
7	25

```
(gdb) sem thread 1
```

Thread	Semaphore
-----	-----
1	23

```
(gdb) sem thread 2
```

Thread	Semaphore
-----	-----
2	43

## 3.4 Registers

Table 3.2 shows the register mapping for the debugger and assembly language.

Debugger register name	Assembler register name	Description
\$pc	n/a	Program counter
\$pred	n/a	Predicates register
\$pestat	n/a	Full PE status register
\$ret	n/a	Return register
\$status	n/a	Nonfloating point PE status
\$enable	n/a	Enable state (all levels)
\$fpadd	n/a	PE floating point add status
\$fpmul	n/a	PE floating point mul status
\$enabled	n/a	PE enabled register
\$numenb	n/a	Number of enabled PE's
\$numpes	n/a	Number of PE's
\$0m2 ... \$62m2	0:m2 ... 62:m2	2 byte mono registers

Table 3.2 Debugger to Assembly Language Register Mapping



Debugger register name	Assembler register name	Description
\$0m4 ... \$60m4	0:m4 ... 60:m4	4 byte mono registers
\$0m8 ... \$56m2	0:m8 ... 56:m8	8 byte mono registers
\$0p1 ... \$127p1	0:p1 ... 127:p1	1 byte poly registers
\$0p2 ... \$126p2	0:p2 ... 126:p2	2 byte poly registers
\$0p4 ... \$124p4	0:p4 ... 124:p4	4 byte poly registers
\$0p8 ... \$120p8	0:p8 ... 120:p8	8 byte poly registers
\$0p16 ... \$112p16	0:p16 ... 112:p16	4 byte poly vector registers
\$0p32 ... \$96p32	0:p32 ... 96:p32	8 byte poly vector registers

Table 3.2 Debugger to Assembly Language Register Mapping

### 3.5 Using DDD

As `csgdb` is a port of the GDB debugger for the CSX architecture, it is possible to use the standard Linux DDD graphical user interface (GUI). This is not provided as part of the SDK but can be used if installed. The DDD GUI comes as standard on most Linux systems. For Windows, you will need to have the cygwin tools installed.

You can start DDD by using `csgdb` as follows:

```
> ddd -debugger csgdb [executable-file]
```

Once it has been started you must connect `csgdb` to the target as described in the section *Connect command and options*, on page 12.

After this you can set a breakpoint and select **[Run]** in DDD. You can then debug the application using the GUI front end.

## 4 Diagnostic software reference

This chapter describes the commands relating to the board diagnostic operations. The board diagnostic software is a package installed on the host operating system and includes two diagnostic tools:

- Diagnostic tests using Perl
- A Mandelbrot demonstration

The Mandelbrot demonstration is a quick way of checking that the board has been installed correctly. However, to run a full set of tests, you must use the Perl-based diagnostic tests.

The following sections give the instructions for running these tests on both Windows XP and Linux, as appropriate.

### 4.1 Diagnostic tests using Perl

The diagnostic tests, which are run using Perl, enable you to check that all the parts of the board are working properly. For example, the tests check the CSX600 processors, the memory, and the performance of the DMA transfers.

You should always run these tests:

- After you have installed the boards.
- If you experience a specific problem when using the board.

To run the full tests, you must have the Perl interpreter installed on your machine. Linux machines usually have Perl already installed. For a ready-to-install distribution of Perl for Windows refer to ActivePerl at:

<http://www.activestate.com>

#### 4.1.1 Full diagnostic tests for Windows XP

To run the diagnostic tests:

1. Double-click the desktop shortcut `csx600_m512_le` to open the command window.
2. In the new window, run the following commands:

```
perl -S run_tests.pl
```

**Note:** Some of the tests may take several minutes to run to completion. If you need to interrupt this process, press [Ctrl]+[C].

#### 4.1.2 Full diagnostic tests for Linux

To run the diagnostic tests:

1. Go to a Linux directory where you have writing privileges, for example:

```
cd /tmp
source /opt/clearspeed/csx600_m512_le/bin/bashrc
```

2. Run the program:

```
perl run_tests.pl
```

Note: Some of these tests may take several minutes to run to completion. If you need to interrupt this process, press [Ctrl]+[C].

### 4.1.3 What to do if the tests fail

If any of the tests fail, it is possible that the board is not plugged in correctly. We recommend that you reinstall the board and run the tests again.

If the tests fail to detect the board and all the tests fail, you may not have installed the driver correctly. We recommend you reinstall the drivers as described in the installation instructions on: <http://support.clearspeed.com/>.

If the board and drivers are installed correctly but some of the tests still fail, refer to the troubleshooting guide in the board *User's Guide* to help you solve the problem.

## 4.2 Mandelbrot demonstration

The Mandelbrot set is a type of infinitely complex mathematical object known as a fractal. It can be run to check that the board or boards have been installed correctly. This test is successful when it displays a Mandelbrot set. This program is based in part on the work of the FLTK project ([www.fltk.org](http://www.fltk.org)).

### 4.2.1 How to run the Mandelbrot demonstration in Windows XP

To run the Mandelbrot demonstration:

3. Double-click the desktop shortcut `csx600_m512_le` created by the installation. This opens a command window.
4. Reset all the boards, by entering the following command in the new window:  

```
csreset -Av
```
5. Run the Mandelbrot demo:  

```
app_mandelbrot
```

Running `app_mandelbrot` causes a window to appear which zooms in and out of a Mandelbrot set. If this does not happen, please run the full diagnostic tests described below.
6. Press [Esc] to exit from the Mandelbrot program.

### 4.2.2 How to run the Mandelbrot demonstration in Linux

To run the Mandelbrot program:

1. Reset the board:  

```
source /opt/clearspeed/csx600_m512_le/bin/bashrc  
csreset -Av
```
2. Run the Mandelbrot demo:  

```
./app_mandelbrot
```

## 5 Host interface library

This chapter describes the CSX600 driver library and the CSAPI functions.

To load code onto a CSX600 processor and communicate with it, a host program must use the ClearSpeed Application Programming Interface (CSAPI).

A set of host driver libraries is provided to allow host applications to communicate with and control the installed *Advance* accelerator boards via the CSAPI. The user-level libraries make use of a kernel-level driver to provide a complete driver for the *Advance* accelerator boards.

### 5.1 CSX600 driver library

The driver library provides an API known as CSAPI (ClearSpeed API) which is available for C and C++ programs using the header file `csapi.h`. The library consists of a set of dynamic shared libraries. The driver libraries are provided as `.dll` files on Windows or `.so` files on Linux.

To identify the *Advance* accelerator boards and in some instances the individual processor on the board, most of the CSAPI functions take a state pointer which describes the board and, if necessary, the processor. To use the CSAPI interface, a `CSAPI_new` call must be made to build and return this state variable.

The library is thread safe. It is safe for concurrent threads to access the library but this is only guaranteed if the same `CSAPIState` instance is used. The CSAPI library uses host semaphores and other concurrency objects to provide concurrent but safe access to the *Advance* accelerator boards.

The Driver Library makes use of an environment variable `CS_CLEARD_ARGS` to allow options to be passed to the library components. In general, it is not necessary to use these options but, for example, it is possible to specify a verbose option to the library by setting this environment variable:

- `export CS_CLEARD_ARGS="--verbose"` (Linux)
- `set CS_CLEARD_ARGS="--verbose"` (Windows)

These options are also available as command line arguments to the standalone tools and the environment variable `CS_CLEARD_ARGS` is essentially a command line to the CSX600 driver libraries.

### Options

There is a set of driver-specific options, listed in Table 5.1, which you can set using the `CS_CLEARD_ARGS` environment string.

Long name	Short Name	Valid values	Description
<code>--bus-big-endian</code>	<code>-B</code>	None	Force the ClearConnect bus to be configured as big endian, even for a little endian CSX600 processor configuration.
<code>--bus-little-endian</code>	<code>-b</code>	None	Force the ClearConnect bus to be configured as little endian, even for a big endian CSX600 processor configuration.
<code>--ccbr-clock-fast</code>		Integer	Clock speed, in MHz, for interface between CSX600 processors.
<code>--ccbr-clock-slow</code>		Integer	Clock speed, in MHz, for interface to FPGA.
<code>--dcache-bypass</code>		on   off	Data cache bypass mode. The default is off.
<code>--ddr-clock</code>		Integer	Set speed, in MHz, of DDRII interface.

Table 5.1 Driver command line options

Long name	Short Name	Valid values	Description
--ecc		on   off	Turn error checking on or off.
--fuse-check		on   off	Strict PE fuse check (on may be faster). The default is off.
--help	-h		Outputs the information in this table.
--icache-bypass		on   off	Instruction cache bypass mode. The default is off.
--load-fuse-mask		<i>filename</i>	Load PE configuration mask from file.
--logall		None	Set logging to the highest level.
--logmask	-l	Integer (hex or decimal)	Controls the log.
--no-fpga			Used for fake FPGA targets.
--no-zero-bss	-n	None	Suppress the normal initialization of BSS sections.
--reset-flags		Bit mask	Set bit 1 to perform a full system reset. The default = 0.
--save-fuse-mask		<i>filename</i>	Save PE configuration mask to file.
--verbose	-v		Switch on verbose display.
--version	-V		Display version string.

Table 5.1 Driver command line options

## 5.2 ClearSpeed host application programming interface

This section describes the ClearSpeed Host Application Programming Interface (CSAPI).

The state of the API is held in a structure called `CSAPIState`. Before it can be used it has to be correctly initialized. After use it has to be disposed off. Most of the functions return a `DRVErrno` code to indicate success or failure. The return values are described in 5.2.2, *Error codes*, on page 39. All the structures, constants and function prototypes are declared in the `csapi_errno.h` and `csapi.h` header files.

### 5.2.1 Common parameters

Many of the functions have a set of common parameters, which are described in this section. The remaining parameters are described for each function.

<b>Parameter</b>	<code>struct CSAPIState *s</code>
<b>Description</b>	A pointer to an API state structure. A new instance of this structure is created and initialized by a call to the <code>CSAPI_new()</code> function.

*Table 5.2 CSAPI state structure*

<b>Parameter</b>	<code>unsigned int proc_inx</code>
<b>Description</b>	A processor index.

*Table 5.3 Processor index*

Figure 5.1 shows the timeline for driver interactions.

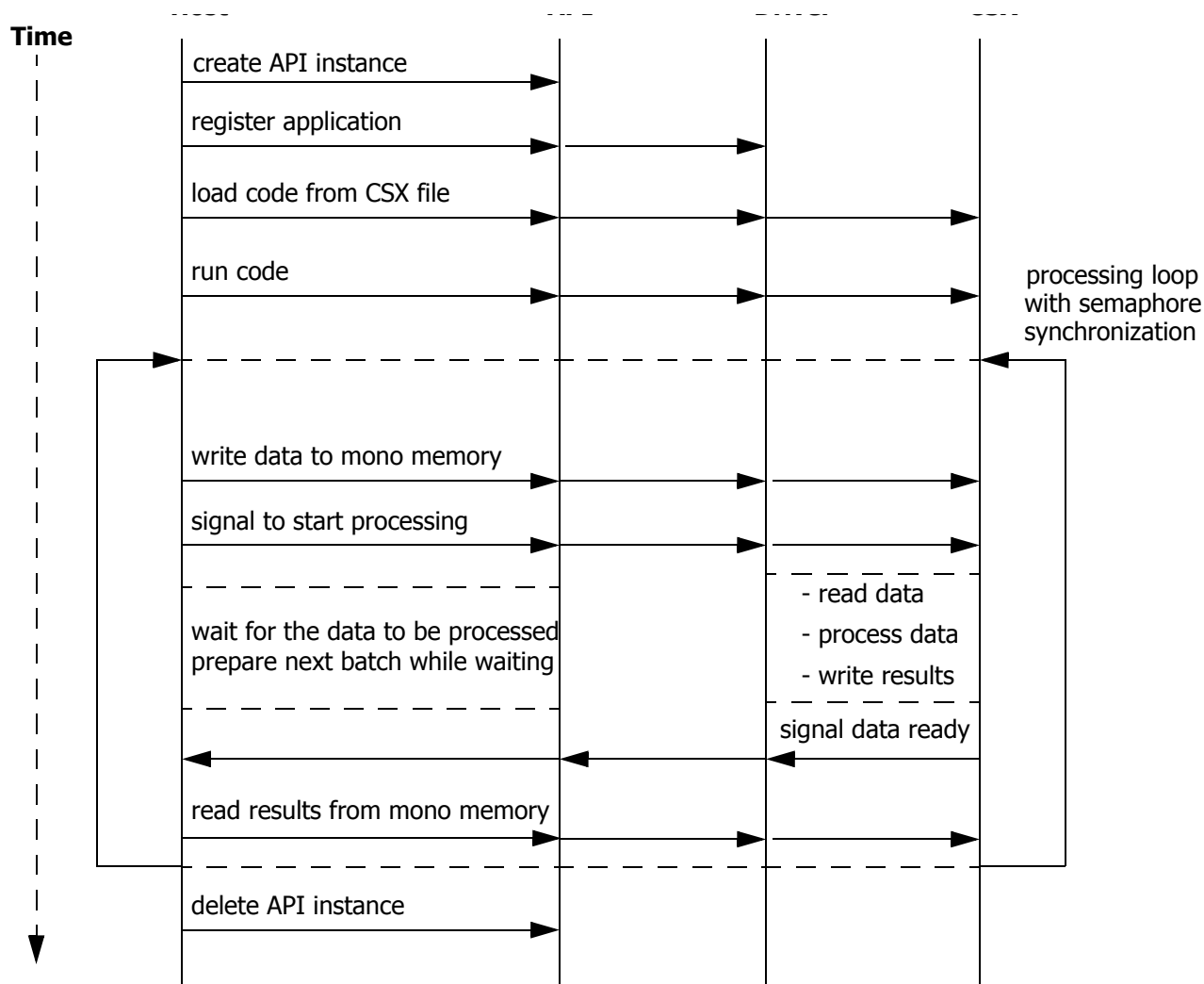


Figure 5.1 Timeline for driver interactions

## 5.2.2 Error codes

Error codes, listed in Table 5.4, are defined in `csapi_errno.h`. An error string can be obtained by using the `CSAPI_get_error_string` function.

DRVErrno	Description
success (= 0)	Operation succeeded.
error (= 1)	Returned on a generic error condition. Usually the API calls return a more specific error code as defined below.
address_in_use	Failed to connect to given host.
bad_csapi_state	CSAPIState *s parameter is invalid.
cannot_open_file	The specified file cannot be opened. There may be various reasons for this: the volume is not mounted, network connection failure, and so on.

Table 5.4 Error codes

DRVErrno	Description
connection_broken	The connection to the driver has been broken.
failed_to_create_thread	Internal error with thread management.
failed_to_find_memory_block	Trying to delete with an invalid pointer or with a pointer that has already been deleted.
failed_to_signal_semaphore	Internal error with thread management.
failed_while_waiting_for_semaphore	Internal error with thread management.
invalid_proc_inx	proc_inx parameter is invalid.
kernel_max_app_count_exceeded	The maximum number of connecting user applications has already been reached. No more connections can be accepted.
lldclient_error	This is the base error code for the low level driver. Use CSAPI_get_error_string to determine the exact error.
permission_denied	The operation could not be performed on the specified processor as an application has not been loaded or registered on that processor.
bad_csapi_arg	An invalid argument was passed to a CSAPI function.
no_symbol	The requested symbol was not found in the CSX executable.
not_connected	The application is not connected to a board. Either CSAPI_connect has not been called or it was not successful.
not_enough_blocks	Unable to allocate memory block as internal limit has been reached.
not_enough_memory	Unable to allocate memory on the board as there is not a large enough gap for the requested size.
program_running	Cannot allocate memory while program is running.
semaphore_number_out_of_bounds	The specified semaphore number is out of range.

Table 5.4 Error codes

### 5.2.3 Initialization and maintenance functions

The CSAPI functions used for initialization and maintenance are described in Table 5.5 to Table 5.8.

<b>Function</b>	<code>struct CSAPIState* CSAPI_new( enum CSAPIMode mode );</code>
<b>Description</b>	Loads the dynamic library for the selected mode and creates a new instance of the API state. A pointer to the API state structure is passed as a parameter to all of the CSAPI functions.
<b>Parameters</b>	mode: Use <code>CM_Direct</code> unless attaching the debugger or using tracing.
<b>Returns</b>	A pointer to a newly-created and initialized instance of the API state structure.

Table 5.5 struct CSAPIState\* CSAPI\_new



<b>Function</b>	<code>void CSAPI_delete(     struct CSAPIState* const s );</code>
<b>Description</b>	Unloads the dynamic library and destroys the instance of the API state. It is not possible to call any CSAPI function with this state once it has been passed to CSAPI_delete.
<b>Parameters</b>	s: State created by CSAPI_new. Will be destroyed by this function.
<b>Returns</b>	Nothing.

Table 5.6 void CSAPI\_delete

<b>Function</b>	<code>DRVErrno CSAPI_connect(     struct CSAPIState* const s,     const char* host_or_addr,     unsigned int instance );</code>
<b>Description</b>	Connects to the driver either locally or on the specified host. Connect should be called once for each CSAPIState created with CSAPI_new. Disconnect by deleting the state with CSAPI_delete.
<b>Parameters</b>	s: State created by CSAPI_new host_or_addr: Host on which the simulator is running or NULL for local hardware. For the host name, use either localhost, DNS name or IP address N.N.N.N. instance: Instance number for hardware or simulator, starting from zero. Use CSAPI_INSTANCE_ANY to connect to any hardware available
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_failed_to_create_thread DRVErrno_lldclient_error, DRVErrno_error

Table 5.7 DRVErrno CSAPI\_connect

<b>Function</b>	<code>DRVErrno CSAPI_reset(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int flags );</code>
<b>Description</b>	Resets the processor specified by proc_inx. This includes resetting the DMA, GSU and PIO engine, stopping and setting up the TSC and setting up the system endianness and instruction cache. The hardware semaphores and interrupts are then initialized and the bootstrap code is run to set up the microcode.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to be reset: CS_PROCESSOR0 or CS_PROCESSOR1 flags: Use CSAPIFlags_FULL_SYSTEM_RESET to reset CCB, CCIs, DDRs and bus monitor
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_error

Table 5.8 DRVErrno CSAPI\_reset

### 5.2.4 Program setup

The CSAPI functions used for program setup are described in Table 5.9 to Table 5.12.

<b>Function</b>	<code>DRVErrno CSAPI_register_application(     struct CSAPIState* const s,     unsigned int proc_inx );</code>
<b>Description</b>	Each connecting application needs to register itself with the driver. This tells the driver that the processor is active and tells the processor where to send its events.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to register application for. CS_PROCESSOR0 or CS_PROCESSOR1
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected, DRVErrno_kernel_max_app_count_exceeded

Table 5.9 DRVErrno CSAPI\_register\_application

<b>Function</b>	<code>DRVErrno CSAPI_load(     struct CSAPIState* const s,     unsigned int proc_inx,     const char* prog_name );</code>
<b>Description</b>	Loads the executable code from a .csx file onto a specified processor.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to load application to. CS_PROCESSOR0 or CS_PROCESSOR1 prog_name: Specifies the path to the CSX executable, which is relative to the CSPATH environment variable. If the program is in the local directory, ./ must be used or be in CSPATH
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_cannot_open_file DRVErrno_error

Table 5.10 DRVErrno CSAPI\_load

<b>Function</b>	<pre> DRVErrno CSAPI_get_last_loaded_handle(     struct CSAPIState* const s,     unsigned int proc_inx,     struct DRVProcess** process ); </pre>
<b>Description</b>	Sets process to contain the pointer to the previously loaded executable.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to get last process pointer for. CS_PROCESSOR0 or CS_PROCESSOR1 process: Pointer to value to be given a pointer to the last loaded process on the processor
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_connection_broken

Table 5.11 DRVErrno CSAPI\_get\_last\_loaded\_handle

<b>Function</b>	<pre> DRVErrno CSAPI_unload(     struct CSAPIState* const s,     unsigned int proc_inx,     struct DRVProcess* process ); </pre>
<b>Description</b>	Unloads the specified process from the specified processor.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to unload application from. CS_PROCESSOR0 or CS_PROCESSOR1 process: Pointer to the process to be unloaded
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg DRVErrno_error

Table 5.12 DRVErrno CSAPI\_unload

### 5.2.5 Processor control

The CSAPI functions used for controlling the processor are described in Table 5.13 to Table 5.18.

<b>Function</b>	<code>DRVErrno CSAPI_run(     struct CSAPIState* const s,     unsigned int proc_inx );</code>
<b>Description</b>	Executes a program loaded onto the given processor.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to start (continue) running. CS_PROCESSOR0 or CS_PROCESSOR1
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_error

Table 5.13 DRVErrno CSAPI\_run

<b>Function</b>	<code>DRVErrno CSAPI_run_process(     struct CSAPIState* const s,     unsigned int proc_inx,     struct DRVProcess* process );</code>
<b>Description</b>	Executes the specified process on the processor it has been loaded on
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to start (continue) running. CS_PROCESSOR0 or CS_PROCESSOR1 process: Pointer to the process to execute
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_error

Table 5.14 DRVErrno CSAPI\_run\_process

<b>Function</b>	<code>DRVErrno CSAPI_halt(     struct CSAPIState* const s,     unsigned int proc_inx );</code>
<b>Description</b>	Halts the execution of a program running on the specified processor.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to stop running. CS_PROCESSOR0 or CS_PROCESSOR1
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected, DRVErrno_permission_denied DRVErrno_error

Table 5.15 DRVErrno CSAPI\_halt

<b>Function</b>	<pre>DRVErrno CSAPI_start(     struct CSAPIState* const s,     unsigned int proc_inx );</pre>
<b>Description</b>	Starts the execution of a halted program.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to start (restart) running. CS_PROCESSOR0 or CS_PROCESSOR1
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_error

Table 5.16 DRVErrno CSAPI\_start

<b>Function</b>	<pre>DRVErrno CSAPI_wait_on_terminate(     struct CSAPIState* const s,     unsigned int proc_inx );</pre>
<b>Description</b>	Awaits the termination signal from the program running on the given processor.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to wait on for termination. CS_PROCESSOR0 or CS_PROCESSOR1
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx

Table 5.17 DRVErrno CSAPI\_wait\_on\_terminate

<b>Function</b>	<pre>DRVErrno CSAPI_get_return_value(     struct CSAPIState* const s,     unsigned int proc_inx,     int* const return_value );</pre>
<b>Description</b>	Obtains the return status of the CSX application which has just terminated. This should be called after CSAPI_wait_on_terminate. If the function is not successful, return_value is undefined.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to get return value from. CS_PROCESSOR0 or CS_PROCESSOR1 return_value: Pointer to value to be given the return value from the terminated CSX program
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg

Table 5.18 DRVErrno CSAPI\_get\_return\_value

### 5.2.6 Accessing registers

The CSAPI functions used for accessing the registers are described in Table 5.19 to Table 5.22.

<b>Function</b>	<pre>DRVErrno CSAPI_write_control_register(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int reg_addr,     unsigned int value );</pre>
<b>Description</b>	Writes the given value to a control register at address <code>reg_addr</code> in the processor specified by <code>proc_inx</code> . Use of this function requires detailed knowledge of the architecture and the function of the control registers.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code></p> <p><code>proc_inx</code>: Index of processor to write control register on. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code>.</p> <p><code>reg_addr</code>: Address of control register to write to.</p> <p><code>value</code>: New value to be written to the control register.</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_error</pre>

Table 5.19 *DRVErrno CSAPI\_write\_control\_register*

<b>Function</b>	<pre>DRVErrno CSAPI_write_control_register_raw(     struct CSAPIState* const s,     unsigned int reg_addr,     unsigned int value );</pre>
<b>Description</b>	Same as <code>CSAPI_write_control_register</code> , except that the <code>reg_addr</code> must be shifted for the appropriate processor index before calling this function. This allows for a more efficient implementation.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code></p> <p><code>reg_addr</code>: Address of control register to write to</p> <p><code>value</code>: New value to be written to the control register</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_not_connected DRVErrno_error</pre>

Table 5.20 *DRVErrno CSAPI\_write\_control\_register\_raw*

<b>Function</b>	<pre> DRVErrno CSAPI_read_control_register(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int reg_addr,     unsigned int* const value ); </pre>
<b>Description</b>	Reads the control register at address <code>reg_addr</code> from the processor specified by <code>proc_inx</code> into the memory location pointed to by <code>value</code> . If the function is not successful, the value is undefined. Use of this function requires detailed knowledge of the architecture and the function of the control registers.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code></p> <p><code>proc_inx</code>: Index of processor to read control register on. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code></p> <p><code>reg_addr</code>: Address of control register to read from</p> <p><code>value</code>: Pointer to memory to be given the value read from the control register</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_error </pre>

Table 5.21 DRVErrno CSAPI\_read\_control\_register

<b>Function</b>	<pre> DRVErrno CSAPI_read_control_register_raw(     struct CSAPIState* const s,     unsigned int reg_addr,     unsigned int* const value ); </pre>
<b>Description</b>	Same as <code>CSAPI_read_control_register</code> , except that the <code>reg_addr</code> must be shifted for the appropriate processor index before calling this function. This allows for a more efficient implementation.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code></p> <p><code>reg_addr</code>: Address of control register to read from</p> <p><code>value</code>: Pointer to memory to be given the value read from the control register</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_not_connected DRVErrno_error </pre>

Table 5.22 DRVErrno CSAPI\_read\_control\_register\_raw

### 5.2.7 Accessing mono memory and registers

The CSAPI functions used for accessing mono memory and registers are described in Table 5.23 to Table 5.32. Use of the register access functions requires detailed knowledge of the architecture and the function of the control registers.

<b>Function</b>	<pre>DRVErrno CSAPI_write_mono_memory(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int address,     unsigned int size,     const void* data_in );</pre>
<b>Description</b>	Copies data from a buffer on the host pointed to by <code>data_in</code> , to a location in mono memory (DRAM) on the board given by <code>address</code> . The size is specified in bytes. The processor specified by <code>proc_inx</code> is halted and the data cache is flushed during the transfer. The appropriate processor for the mono memory address should be used.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>proc_inx</code>: Index of processor to halt while writing to DRAM memory. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code>  <code>address</code>: Address of mono memory (DRAM on the board) to start writing to  <code>size</code>: Number of bytes to copy from <code>data_in</code> (on host) to address (on the board)  <code>data_in</code>: Address on host to start copying data from</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_error</pre>

Table 5.23 DRVErrno CSAPI\_write\_mono\_memory

<b>Function</b>	<pre>DRVErrno CSAPI_write_mono_memory_raw(     struct CSAPIState* const s,     unsigned int address,     unsigned int size,     const void* data_in );</pre>
<b>Description</b>	Same as <code>CSAPI_write_mono_memory</code> , except that the processor is not halted and the data cache is not flushed. This may cause the transfer to take longer, but allows the processor to continue during the transfer.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>address</code>: Address of mono memory (DRAM on the board) to start reading from  <code>size</code>: Number of bytes to copy from address (on the board) to <code>data_out</code> (on host)  <code>data_out</code>: Pre-allocated address on host to start copying data to</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_not_connected DRVErrno_error</pre>

Table 5.24 DRVErrno CSAPI\_write\_mono\_memory\_raw



<b>Function</b>	<pre> DRVErrno CSAPI_read_mono_memory(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int address,     unsigned int size,     void* const data_out ); </pre>
<b>Description</b>	Copies data from a location in mono memory (DRAM) on the board given by address, to a pre-allocated buffer on the host pointed to by data_out. The size is specified in bytes. The processor specified by proc_inx is halted and the data cache is flushed during the transfer. The appropriate processor for the mono memory address should be used. The data_out buffer must be allocated on the host before calling this function.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to halt while reading from DRAM memory. CS_PROCESSOR0 or CS_PROCESSOR1 * address: Address of mono memory (DRAM on the board) to start reading from size: Number of bytes to copy from address (on the board) to data_out (on host) data_out: Pre-allocated address on host to start copying data to
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_error

Table 5.25 DRVErrno CSAPI\_read\_mono\_memory

<b>Function</b>	<pre> DRVErrno CSAPI_read_mono_memory_raw(     struct CSAPIState* const s,     unsigned int address,     unsigned int size,     void* const data_out ); </pre>
<b>Description</b>	Same as CSAPI_read_mono_memory, except that the processor is not halted and the data cache is not flushed. This may cause the transfer to take longer, but allows the processor to continue during the transfer.
<b>Parameters</b>	s: State created by CSAPI_new address: Address of mono memory (DRAM on the board) to start reading from size: Number of bytes to copy from address (on the board) to data_out (on host) data_out: Pre-allocated address on host to start copying data to
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_not_connected DRVErrno_error

Table 5.26 DRVErrno CSAPI\_read\_mono\_memory\_raw

<b>Function</b>	<pre>DRVErrno CSAPI_write_mono_memory_async(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int address,     unsigned int size,     const void* data_in );</pre>
<b>Description</b>	Starts the <code>CSAPI_write_mono_memory</code> function in a separate thread and returns immediately. The host is then free to continue in the current thread. The host can check when the transfer has completed by using the <code>CSAPI_write_mono_memory_async_poll</code> function, or it can wait for the transfer to complete by using the blocking <code>CSAPI_write_mono_memory_async_wait</code> function.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>proc_inx</code>: Index of processor to halt while writing to DRAM memory. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code>  <code>address</code>: Address of mono memory (DRAM on the board) to start writing to  <code>size</code>: Number of bytes to copy from <code>data_in</code> (on host) to address (on the board)  <code>data_in</code>: Address on host to start copying data from</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_failed_to_signal_semaphore DRVErrno_failed_while_waiting_for_semaphore</pre>

Table 5.27 DRVErrno CSAPI\_write\_mono\_memory\_async

<b>Function</b>	<pre>DRVErrno CSAPI_write_mono_memory_async_wait(     struct CSAPIState* const s );</pre>
<b>Description</b>	Allows the host to wait for the completion of an asynchronous transfer started by <code>CSAPI_write_mono_memory_async</code> . The wait will only return once the transfer has completed or if there is an error. The return status should be checked to ensure that the transfer completed successfully.
<b>Parameters</b>	<code>s</code> : State created by <code>CSAPI_new</code>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_failed_while_waiting_for_semaphore, DRVErrno_failed_to_signal_semaphore</pre>

Table 5.28 DRVErrno CSAPI\_write\_mono\_memory\_async\_wait

<b>Function</b>	<pre>DRVErrno CSAPI_write_mono_memory_async_poll(     struct CSAPIState* const s,     unsigned int* const completed );</pre>
<b>Description</b>	Allows the host to check the status of an asynchronous transfer started by <code>CSAPI_write_mono_memory_async</code> . The status is passed back using the <code>completed</code> parameter. The return status should be checked to ensure that a valid status was obtained.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>completed</code>: Pointer to value to be given the status of the transfer in progress</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_failed_while_waiting_for_semaphore</pre>

Table 5.29 DRVErrno CSAPI\_write\_mono\_memory\_async\_poll

<b>Function</b>	<pre> DRVErrno CSAPI_read_mono_memory_async(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int address,     unsigned int size,     void* const data_out ); </pre>
<b>Description</b>	Starts the <code>CSAPI_read_mono_memory</code> function in a separate thread and returns immediately. The host is then free to continue in the current thread. The host can check when the transfer has completed by using the <code>CSAPI_read_mono_memory_async_poll</code> function, or it can wait for the transfer to complete by using the blocking <code>CSAPI_read_mono_memory_async_wait</code> function.
<b>Parameters</b>	<p><b>s:</b> State created by <code>CSAPI_new</code></p> <p><b>proc_inx:</b> Index of processor to halt while reading from DRAM memory. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code></p> <p><b>address:</b> Address of mono memory (DRAM on the board) to start reading from</p> <p><b>size:</b> Number of bytes to copy from address (on the board) to <code>data_out</code> (on host)</p> <p><b>data_out:</b> Pre-allocated address on host to start copying data to</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_failed_to_signal_semaphore DRVErrno_failed_while_waiting_for_semaphore </pre>

Table 5.30 DRVErrno CSAPI\_read\_mono\_memory\_async

<b>Function</b>	<pre> DRVErrno CSAPI_read_mono_memory_async_wait(     struct CSAPIState* const s ); </pre>
<b>Description</b>	Allows the host to wait for the completion of an asynchronous transfer started by <code>CSAPI_read_mono_memory_async</code> . The wait will only return once the transfer has completed or if there is an error. The return status should be checked to ensure that the transfer completed successfully.
<b>Parameters</b>	<b>s:</b> State created by <code>CSAPI_new</code>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_failed_while_waiting_for_semaphore, DRVErrno_failed_to_signal_semaphore </pre>

Table 5.31 DRVErrno CSAPI\_read\_mono\_memory\_async\_wait

<b>Function</b>	DRVErrno CSAPI_read_mono_memory_async_poll( struct CSAPIState* const s, unsigned int* const completed );
<b>Description</b>	Allows the host to check the status of an asynchronous transfer started by CSAPI_read_mono_memory_async. The status is passed back using the completed parameter. The return status should be checked to ensure that a valid status was obtained.
<b>Parameters</b>	s: State created by CSAPI_new completed: Pointer to value to be given the status of the transfer in progress
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_failed_while_waiting_for_semaphore

Table 5.32 DRVErrno CSAPI\_read\_mono\_memory\_async\_poll

### 5.3 Example of host and CSX code cooperation

A simple example of both the host and the CSX600 processor assembly code is presented below. It shows how to designate an area of mono memory and synchronize host and card processing.

**Note:** this example does not include as much error checking as a real program would require to make it robust. After every statement of the form `status = CSAPI...` the returned value should be checked to ensure it is equal to `DRVErrno_success`, or to report an error if it is not.

```
.section .mono.bss

_SHARED_MEMORY::
.fill 16,1,0           // reserve 16 bytes as the data transfer buffer
.global _SHARED_MEMORY // make it visible globally

.section .text

.LL_loop_start::
    sem.wait _SEM_DATA_IN_READY_
    ....
    load data from _SHARED_MEMORY_ and process
    ....
    sem.sig _SEM_PROCESSING_COMPLETE_
j .LL_loop_start      // go back and wait for the next batch
```

The corresponding user application code could look like this:

```
// payload, that corresponds in size to the _SHARED_MEMORY_ above
struct MyPayload
{
    int a;
    int b;
    int c;
    int d;
};

#define PROC1 0

main()
{
    DRVErrno status;
    struct MyPayload pl;
    unsigned int shared_memory;    // this is the address on the CSX600 processor

    // create a state object
    struct CSAPIState * state = CSAPI_new(CM_Direct);

    // connect to the hardware instance 0
    status = CSAPI_connect( state, 0, 0 );

    if ( status == DRVErrno_success )
    {
        // NOTE: for simplicity the status checking will be omitted
        // from this example

        // obtain the shared memory address using the helper function
        status = CSAPI_get_symbol_value( state ,
                                         "/home/mh/simple.csx",
                                         "_SHARED_MEMORY_",
                                         &shared_memory );

        status = CSAPI_register_application( state , PROC1 );
        status = CSAPI_load( state, PROC1, "/home/mh/simple.csx" );
        status = CSAPI_register_semaphore ( state, PROC1, _SEM_PROCESSING_COMPLETE_ );
        status = CSAPI_run( state, PROC1 );

        while ( ... there is data to process ... )
        {
            // prepare the data
            pl.a = 1; pl.b = 2; ...

            // copy the data onto the card side
            status = CSAPI_write_mono_memory(
                state,
                PROC1,
                shared_memory,
                sizeof( MyPayload ),
                (void*)(&pl) );

            // signal the start semaphore
            status = CSAPI_semaphore_signal( state, PROC1, _SEM_DATA_IN_READY_ );

            // wait for results
            // in case of multi-threaded applications
            // something useful could be done during the wait
            // for single threaded applications, the explicit polling/do
            // something useful loop could be implemented

            status = CSAPI_semaphore_wait( state, PROC1, _SEM_PROCESSING_COMPLETE_ );
        }
    }
}
```

```

// consume the data after processing, assuming here, that
// the shared memory is used both for receiving and sending
// data

status = CSAPI_read_mono_memory(
    state,
    PROC1,
    shared_memory,
    sizeof( MyPayload ),
    (void*)(&pl) );
}
}
return 0;
}

```

**Note:** For simplicity, the code above does not use double-buffering which is the recommended method for efficiently interleaving compute and memory transfer operations.

### 5.3.1 Endian functions

The CSAPI endian functions are described in Table 5.33 to Table 5.34.

<b>Function</b>	<pre> DRVErrno CSAPI_buffer_to_native_endian(     struct CSAPIState* const s,     unsigned int proc_inx,     void* out,     const void* in,     int size ); </pre>
<b>Description</b>	Copies and converts data from the endianness used by the processor <code>proc_inx</code> in the current connection to the native host endianness. The number of bytes to copy and convert from <code>in</code> to <code>out</code> is set by the <code>size</code> parameter.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>. Required to ensure the dynamic CSAPI library is loaded</p> <p><code>proc_inx</code>: Index of processor to convert endianness from. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code></p> <p><code>out</code>: Pointer to memory to write out going data to, in native host endianness</p> <p><code>in</code>: Pointer to memory to read incoming data from, in the selected processor endianness</p> <p><code>size</code>: Number of bytes to copy and convert</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected </pre>

Table 5.33 *DRVErrno CSAPI\_buffer\_to\_native\_endian*

<b>Function</b>	<pre>DRVErrno CSAPI_endianness(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int* const endian );</pre>
<b>Description</b>	Gets the current endianness of the specified processor on the current board.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor get endianness of. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>endian: Pointer to value to be given the current endianness of the specified processor on the current board. Value will be set to either CS_LITTLE_ENDIAN or CS_BIG_ENDIAN</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg</pre>

Table 5.34 DRVErrno CSAPI\_endianness

### 5.3.2 Thread functions

The thread functions are described in Table 5.35 to Table 5.36.

<b>Function</b>	<pre>DRVErrno CSAPI_set_thread(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int thread_id,     unsigned int* const old_thread_id );</pre>
<b>Description</b>	Switches to the specified thread on specified processor, identified by thread ID. The previous thread ID is returned in old_thread_id so that we can switch back to it if necessary.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor to set thread on. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>thread_id: ID of thread to switch to on processor</p> <p>old_thread_id: Pointer to value to be given the ID of thread that was running on processor</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_not_connected DRVErrno_permission_denied DRVErrno_error</pre>

Table 5.35 DRVErrno CSAPI\_set\_thread

<b>Function</b>	<pre>DRVErrno CSAPI_num_threads(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int* const num_of_threads );</pre>
<b>Description</b>	Provides the total number of threads supported by the specified processor. Note this will include threads used by the debugger or other system applications.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor to count threads on. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>num_of_threads: Pointer to value to be given the number of threads supported by the processor</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg</pre>

*Table 5.36 DRVErrno CSAPI\_num\_threads*



### 5.3.3 Semaphore handling

The CSAPI functions used for handling semaphore are described in Table 5.37 to Table 5.40.

<b>Function</b>	<code>DRVErrno CSAPI_register_semaphore(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int sem_number );</code>
<b>Description</b>	Registers that the host is intending to wait on the specified semaphore number on the specified processor. Registration must be performed only on semaphores that the host will be waiting on. A race condition will be created if the host signals, or the board processor waits on, a registered semaphore.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to register semaphore on. CS_PROCESSOR0 or CS_PROCESSOR1 sem_number: Semaphore number to register on processor
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_semaphore_number_out_of_bounds DRVErrno_error

Table 5.37 DRVErrno CSAPI\_register\_semaphore

<b>Function</b>	<code>DRVErrno CSAPI_semaphore_wait(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int sem_number );</code>
<b>Description</b>	Allows the host to wait on the specified semaphore on the specified processor. The semaphore must be registered before it can be waited upon.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor wait for semaphore on. CS_PROCESSOR0 or CS_PROCESSOR1 sem_number: Semaphore number to wait on
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_failed_while_waiting_for_semaphore, DRVErrno_semaphore_number_out_of_bounds

Table 5.38 DRVErrno CSAPI\_semaphore\_wait

<b>Function</b>	<pre>DRVErrno CSAPI_semaphore_signal(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int sem_number );</pre>
<b>Description</b>	Signals the specified semaphore on the specified processor. The semaphore must be NOT be registered if it is going to be signalled by the host.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor to signal semaphore on. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>sem_number: Semaphore number to signal</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_not_connected DRVErrno_error</pre>

Table 5.39 DRVErrno CSAPI\_semaphore\_signal

<b>Function</b>	<pre>DRVErrno CSAPI_num_semaphores(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int* const num_of_semaphores );</pre>
<b>Description</b>	Provides the total number of semaphores supported by the specified processor. Note this will include system semaphores, and semaphores used by the debugger and asynchronous memory read / write functions.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor to count semaphore on. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>num_of_semaphores: Pointer to value to be given the number of semaphores supported</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg</pre>

Table 5.40 DRVErrno CSAPI\_num\_semaphores

### 5.3.4 Callback functions

The CSAPI functions for callback are described in Table 5.41 to Table 5.42.

<b>Function</b>	<pre>DRVErrno CSAPI_get_callback(     struct CSAPIState* const s,     unsigned int event,     CSAPI_EventFnPtr* const event_cb_out );</pre>
<b>Description</b>	Gets the function call back registered with <code>CSAPI_register_callback</code> for the specified event. Typically used to check see if a call back is registered, or to chain user call back functions.
<b>Parameters</b>	s: State created by <code>CSAPI_new</code> event: Event to trigger the call back function event_cb_out: Pointer to value to be given the call back Function pointer
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_error

Table 5.41 DRVErrno CSAPI\_get\_callback

<b>Function</b>	<pre>DRVErrno CSAPI_register_callback(     struct CSAPIState* const s,     unsigned int event,     CSAPI_EventFnPtr event_cb,     void* user_data );</pre>
<b>Description</b>	Registers a user call back function for an event signalled by the board. When the event occurs the event call back function will be called with the state, event data and a pointer to specified user data.
<b>Parameters</b>	s: State created by <code>CSAPI_new</code> event: Event to trigger the call back function event_cb: Function pointer to an even handler on the host. See definition at top of file user_data: Pointer to user data to be passed to the event handler on the host
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_error

Table 5.42 DRVErrno CSAPI\_register\_callback

### 5.3.5 Memory allocation

The CSAPI functions for allocating memory are described in Table 5.43 to Table 5.48.

<b>Function</b>	<pre>DRVErrno CSAPI_get_free_mem(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int mem_inx,     unsigned int* const size_out );</pre>
<b>Description</b>	Returns the total available mono memory (DRAM) local to the specified processor. Processors can see mono memory on other processors but will not access this as efficiently as local memory.
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>proc_inx: Index of processor to get free memory on. CS_PROCESSOR0 or CS_PROCESSOR1</p> <p>mem_inx: Index of memory. Currently unused as free space can only be used for mono memory</p> <p>size_out: Pointer to value to be given the current amount of free memory local to the processor</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg, DRVErrno_program_running</pre>

Table 5.43 DRVErrno CSAPI\_get\_free\_mem

<b>Function</b>	<pre> DRVErrno CSAPI_allocate_shared_memory(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int mem_inx,     unsigned int size,     unsigned int alignment,     const char* symbol_name,     unsigned int* const mem_ptr_out ); </pre>
<b>Description</b>	<p>Allocates mono memory (DRAM) local to the specified processor for use by both the CSX program and the host. The memory allocation can use an alignment and the allocated address will be given to the variable specified by 'symbol_name' in the current CSX program. The same address will be returned in the mem_ptr_out parameter. Processors can see mono memory on other processors but will not access this as efficiently as local memory. Memory allocation and releasing can only be done when the processor is not running.</p>
<b>Parameters</b>	<p>s: State created by CSAPI_new  proc_inx: Index of processor to allocate shared memory on. CS_PROCESSOR0 or CS_PROCESSOR1  mem_inx: Index of memory. Currently unused as only mono memory can be allocated  size: Number of contiguous bytes to attempt to allocate in the free space  alignment: Bytes to align allocated memory to on the board  symbol_name: Name of static global variable in the CSX program to be given the allocated address  mem_ptr_out: Pointer to value to be given the address of the allocated memory on the processor</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg DRVErrno_program_running, DRVErrno_no_symbol DRVErrno_cannot_open_file DRVErrno_not_enough_blocks DRVErrno_not_enough_memory DRVErrno_error </pre>

Table 5.44 DRVErrno CSAPI\_allocate\_shared\_memory

<b>Function</b>	<pre> DRVErrno CSAPI_allocate_static_shared_memory(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int mem_inx,     unsigned int mem_ptr_in,     unsigned int size,     const char* symbol_name ); </pre>
<b>Description</b>	<p>Attempts to allocate mono memory (DRAM) local to the specified processor at the address given by <code>mem_ptr_in</code>. This is the same as <code>CSAPI_allocate_shared_memory</code>, except that instead of returning an address for the allocated memory, it simply returns success or fail for allocating at the address given by <code>mem_ptr_in</code>. This can be useful for supporting programs where the addresses of the data arrays are hard coded in the CSX program. Processors can see mono memory on other processors but will not access this as efficiently as local memory. Memory allocation and releasing can only be done when the processor is not running.</p>
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>proc_inx</code>: Index of processor to allocate static shared memory on. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code>  <code>mem_inx</code>: Index of memory. Currently unused as only mono memory can be allocated  <code>mem_ptr_in</code>: Desired address of the allocated memory on the processor  <code>size</code>: Number of bytes to attempt to allocate at the <code>mem_ptr_in</code> address  <code>symbol_name</code>: Name of static global variable in the CSX program to be given the allocated address</p>
<b>Returns</b>	<pre> DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg DRVErrno_program_running, DRVErrno_no_symbol DRVErrno_cannot_open_file DRVErrno_not_enough_blocks DRVErrno_address_in_use DRVErrno_error </pre>

Table 5.45 DRVErrno CSAPI\_allocate\_static\_shared\_memory

<b>Function</b>	<pre>DRVErrno CSAPI_free(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int mem_ptr );</pre>
<b>Description</b>	Releases previously allocated memory local to the specified processor at the address given by <code>mem_ptr</code> . This memory on the board is then available to be allocated by <code>CSAPI_allocate_shared_memory</code> . Processors can see mono memory on other processors but will not access this as efficiently as local memory. Memory allocation and releasing can only be done when the processor is not running.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>proc_inx</code>: Index of processor to release allocated memory on. <code>CS_PROCESSOR0</code> or <code>CS_PROCESSOR1</code>  <code>mem_ptr</code>: Index of memory. Currently unused as only mono memory can be freed</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_program_running, DRVErrno_failed_to_find_memory_block</pre>

Table 5.46 DRVErrno CSAPI\_free

<b>Function</b>	<pre>DRVErrno CSAPI_get_symbol_value(     struct CSAPIState* const s,     const char* prog_name,     const char* symbol_name,     unsigned int* const symbol_value_out );</pre>
<b>Description</b>	Returns the value of the symbol <code>symbol_name</code> in the given CSX program. This is typically the address of the corresponding static variable in the program. The address can then be used with the <code>CSAPI_read_mono_memory</code> and <code>CSAPI_write_mono_memory</code> functions to access the contents of the memory on the board pointed to by the variable.
<b>Parameters</b>	<p><code>s</code>: State created by <code>CSAPI_new</code>  <code>prog_name</code>: Name of the CSX program that declares the symbol  <code>symbol_name</code>: Name of the symbol to obtain the value for (typically a static variable)  <code>symbol_value_out</code>: On success this is set to the value associated with <code>symbol_name</code>. (typically the address of a static variable given by <code>symbol_name</code>)</p>
<b>Returns</b>	<pre>DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_no_symbol DRVErrno_cannot_open_file, DRVErrno_error</pre>

Table 5.47 DRVErrno CSAPI\_get\_symbol\_value

<b>Function</b>	<pre>DRVErrno CSAPI_get_symbol_value_loaded(     struct CSAPIState* const s,     struct DRVProcess* process,     const char* symbol_name,     unsigned int* const symbol_value_out );</pre>
<b>Description</b>	Returns the symbol value as loaded in the given process. This will include any relocations that have been applied
<b>Parameters</b>	<p>s: State created by CSAPI_new</p> <p>process: Pointer to the process containing the given symbol</p> <p>symbol_name: Name of the symbol to obtain the value for (typically a static variable)</p> <p>symbol_value_out: On success this is set to the value associated with symbol_name (typically the address of a static variable given by symbol_name)</p>
<b>Returns</b>	<p>DRVErrno_success</p> <p>DRVErrno_bad_csapi_state</p> <p>DRVErrno_bad_csapi_arg</p> <p>DRVErrno_no_symbol</p>

*Table 5.48 DRVErrno CSAPI\_get\_symbol\_value\_loaded*



### 5.3.6 Utility functions

The CSAPI utility functions are described in Table 5.49 to Table 5.53.

<b>Function</b>	<pre>DRVErrno CSAPI_set_system_param(     struct CSAPIState* const s,     enum CSAPISystemParameters what,     unsigned int vi,     const char* vs );</pre>
<b>Description</b>	Sets the specified system parameter (using the CSAPISystemParameters enumeration) to a value given to either the vi or vs parameter as appropriate. See the CSAPISystemParameters enumeration to see which parameters use which input variable. The user documentation should be referred to for valid combinations of system parameters.
<b>Parameters</b>	s: State created by CSAPI_new what: Name of parameter to be set, defined by an enumeration at the top of this file vi: Integer value to set the parameter with (if applicable) vs: String value to set the parameter with (if applicable)
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg DRVErrno_error

Table 5.49 DRVErrno CSAPI\_set\_system\_param

<b>Function</b>	<pre>DRVErrno CSAPI_num_cards(     struct CSAPIState* const s,     unsigned int* const num_of_cards );</pre>
<b>Description</b>	Provides the number of boards in the current system. Note that this does not include simulators. It is still necessary to call CSAPI_new before this function to load the library of functions. It is valid to create a CSAPIState with CSAPI_new, call this function and then delete the state with CSAPI_delete.
<b>Parameters</b>	s: State created by CSAPI_new. Required to ensure the dynamic CSAPI library is loaded num_of_cards: Pointer to value to be given the number of boards in the current system
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_arg DRVErrno_lldclient_error

Table 5.50 DRVErrno CSAPI\_num\_cards

<b>Function</b>	<pre>DRVErrno CSAPI_num_processors(     struct CSAPIState* const s,     unsigned int* const num_of_processors );</pre>
<b>Description</b>	Returns the number of processors on the board we are connected to.
<b>Parameters</b>	s: State created by CSAPI_new num_of_processors: Pointer to value to be given the number of processors
<b>Returns</b>	DRVErrno_bad_csapi_state DRVErrno_bad_csapi_arg

Table 5.51 DRVErrno CSAPI\_num\_processors

<b>Function</b>	<pre>DRVErrno CSAPI_num_pes(     struct CSAPIState* const s,     unsigned int proc_inx,     unsigned int* const num_of_pes );</pre>
<b>Description</b>	Returns the number of processing elements on the specified processor on the board we are connected to.
<b>Parameters</b>	s: State created by CSAPI_new proc_inx: Index of processor to get count of processing elements on. CS_PROCESSOR0 or CS_PROCESSOR1 num_of_pes: Pointer to value to be given the number of processing elements
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_state DRVErrno_invalid_proc_inx DRVErrno_bad_csapi_arg

Table 5.52 DRVErrno CSAPI\_num\_pes

<b>Function</b>	<pre>DRVErrno CSAPI_get_error_string(     struct CSAPIState* const s,     DRVErrno error_code,     char* const error_string,     const int max_string_length );</pre>
<b>Description</b>	This function decodes an error_code and copies a short description of the error to the provided char buffer. The string will be truncated at max_string_length if necessary to avoid over-running the provided buffer.
<b>Parameters</b>	s: State created by CSAPI_new. Required to ensure the dynamic CSAPI library is loaded error_code: Error code to get the string for. Must be of type DRVErrno error_string: Pointer to pre-allocated buffer to receive error string max_string_length: Length of pre-allocated buffer to receive error string
<b>Returns</b>	DRVErrno_success DRVErrno_bad_csapi_arg

Table 5.53 DRVErrno CSAPI\_get\_error\_string

## 5.4 DMA issues

The *Advance* accelerator board uses Direct Memory Access (DMA) to perform data transfer with the host and by default DMA is used to transfer all but the smallest pieces of data. Data is normally transferred directly to and from the user buffers passed to the `Read_mono_memory` and `Write_mono_memory` functions, no intermediate buffering should be used if at all possible. This implies that, depending on host architecture characteristics, it is safer to align the host data buffers on a system page boundary. For x86 and x86\_64 the page size is 4K and for ia64 it is normally 32K but, can be larger. The reason for this is that the entire system page needs to be operated on while the DMA transfer is proceeding and reads and writes into a page that includes either the start or end of the DMA buffer may cause obscure and difficult to track problems due to race conditions between DMA operations and host memory traffic.

Also, the DMA engine can only transfer data that is at least 8-byte aligned on both the host machine and the *Advance* accelerator board. If the transfer does not satisfy these constraints, a warning will be generated and the slower data transfer mechanism will be used. The slower data transfer mechanism is typically one hundredth the speed of DMA, so should not normally be used.

DMA on some chip sets can cause problems and it is possible to disable DMA. If the environment variable `CS_DISABLE_DMA` is set, DMA will not be used. This may be useful in diagnosing problems. The variable is set as follows:

```
export CS_DISABLE_DMA=1 (on Linux/bash)
set CS_DISABLE_DMA=1 (on Windows)
```

## 5.5 Access control

Access control prevents concurrent access to *Advance* accelerator boards. The access control is not intended to be 100% secure – it is intentionally made open, with a plain text lock file that can be edited and deleted. The locking mechanism is most easily disabled by removing the write privilege from the lock file parent folder.

### 5.5.1 The lock file

The lock file, which must be writable, is created in `/var/lock/clearspeed/` on Linux. Under Windows the lock file is created in the installation directory (typically `C:\Program Files\clearspeed\csx600_m512_le`).

The lock file, `cs_lock_file.txt`, is created if it does not already exist. If it cannot be created, the following warning will be printed:

```
Warning: Not using lock file. Check rw permissions for /var/lock/clearspeed/cs_lock_file.txt.
```

The lock file contains the following header block:

```
0
ClearSpeed lock file for windrvr6 driver. Each entry starts with an asterisk.
White space is ignored. Entries are TCP/PCI, Instance, UserID, PID, Lock Time.
All entries present in this file are considered locked.
```

If the first line is 0, the file is not currently being modified. When the file is accessed the 0 is changed to a random number then the file is read. When the file is updated this number is first checked to ensure nothing has changed since the file was read, and after the changes have been written it is changed back to 0. If the line stays as the same non-zero value for too long, it is considered stale and is ignored.

The header block of information text is fixed and is regenerated each time the file is written. The lock-file entries follow this. Each entry consists of five lines following an asterisk. The following is an example for the entry for a board (2), instance 0, locked by user 'tims' running process ID 18830 at time 1134472924. The last line shows the time in text, but this is not read by the software.

```
Resource 1 *
2
0
tims
18830
1134472924
Locked by tims on Tue Dec 13 11:22:04 2005
```

All text from the time entry on line 5 to the next asterisk is ignored. So, if a board or simulator is locked, it has an entry in the lock file. If it is not locked, it does not have an entry in the lock file. The process ID is used to check that the process that locked the board is still running and allows stale entries in the lock file to be identified.

## 6 Runtime Environment Variables

This chapter contains a description of the runtime environment variables for the drivers.

### 6.1 LLDINST

Sets the board or simulator instance to use, if the application does not define it, for example, adbg. Either a number or the word 'any'.

### 6.2 LLDHOST

Sets the host name for a simulator.

### 6.3 CS\_LLD\_DEBUG\_MASK

Gets entry and exit messages for all low level functions. It also turns on warnings or turns off errors. The CS\_LLD\_DEBUG\_MASK bits are as follows:

```

#define LLD_DEBUG_CREATE_ENTRY          0x00000001
#define LLD_DEBUG_CREATE_EXIT          0x00000002
#define LLD_DEBUG_DESTROY_ENTRY        0x00000004
#define LLD_DEBUG_DESTROY_EXIT         0x00000008
#define LLD_DEBUG_CONNECT_ENTRY        0x00000010
#define LLD_DEBUG_CONNECT_EXIT         0x00000020
#define LLD_DEBUG_READREGISTER_ENTRY   0x00000100
#define LLD_DEBUG_READREGISTER_EXIT    0x00000200
#define LLD_DEBUG_WRITEREGISTER_ENTRY   0x00000400
#define LLD_DEBUG_WRITEREGISTER_EXIT   0x00000800
#define LLD_DEBUG_READMEMORY_ENTRY     0x00001000
#define LLD_DEBUG_READMEMORY_EXIT      0x00002000
#define LLD_DEBUG_WRITEMEMORY_ENTRY    0x00004000
#define LLD_DEBUG_WRITEMEMORY_EXIT     0x00008000
#define LLD_DEBUG_WAITFORINTERRUPT_ENTRY 0x00010000
#define LLD_DEBUG_WAITFORINTERRUPT_EXIT 0x00020000
#define LLD_DEBUG_ESCAPE_ENTRY         0x00040000
#define LLD_DEBUG_ESCAPE_EXIT          0x00080000
#define LLD_DEBUG_CARDLOCATION_ENTRY    0x00100000
#define LLD_DEBUG_CARDLOCATION_EXIT     0x00200000
#define LLD_DEBUG_NUMBEROFCARDS_ENTRY  0x00400000
#define LLD_DEBUG_NUMBEROFCARDS_EXIT   0x00800000
#define LLD_DEBUG_LLPCI_FUNC_ENTRY     0x01000000
#define LLD_DEBUG_LLPCI_FUNC_EXIT      0x02000000
#define LLD_DEBUG_MESSAGE               0x10000000
#define LLD_DEBUG_ERROR                 0x20000000

```

### 6.4 CS\_LLD\_NAME

Specifies the dynamic library to connect through, for use with DDK development.

## 6.5 CS\_CLEARD\_ARGS

Provides access to system parameters such as clock speed. This was previously passed in as parameters to `cleard`.

Options previously passed to the `cleard` daemon can now be passed to the direct driver by putting them in the `CS_CLEARD_ARGS` environment string. For example:

```
CS_CLEARD_ARGS="--logall --ecc off" csrcun_debug
```

This will run the debug `csrcun` application with all driver logging output to the screen. ECC errors will also be suppressed as the ecc checking is off. A full list of options can be shown using the `-h` option:

```
CS_CLEARD_ARGS="-h" csreset
```

This will print the help shown in Table 6.1, in addition to the standard output from the `csreset` application.

Long name	Short name	Valid values	Description
--logall			Enable full low-level driver log
--logmask bit mask			Enable specific low-level driver log using bit mask
--no-fpga			Used for fake FPGA targets
--no-zero-bss	(-n)		Do not initialize BSS sections to zero (faster)
--bus-little-endian	(-b)		ClearConnect bus to Little Endian. Default: same as mtap
--bus-big-endian	(-B)		ClearConnect bus to Big Endian. Default: same as mtap
--load-fuse-mask		<i>filename</i>	Load PE configuration mask from file
--save-fuse-mask		<i>filename</i>	Save PE configuration mask to file
--fuse-check		on   off	Strict PE fuse check (on may be faster). Default: off
--ccbr-clock-slow		integer	CCBR clock in MHz for FPGA to chip 1. Default: 100
--ccbr-clock-fast		integer	CCBR clock in MHz chip 1 to chip 2. Default: 200
--ddr-clock		integer	DDR clock speed in MHz. Default: 200
--icache-bypass		on   off	Instruction cache bypass mode. Default: off
--dcache-bypass		on   off	Data cache bypass mode. Default: off
--ecc		on   off	Turn on or off DRAM ECC mode. Default: on
--reset-flags		bit mask	Set Bit 1 to perform a full system reset. Default: 0
--verbose	(-v)		Switch on verbose display (use logmask instead)
--version	(-V)		Display version string
--help	(-h)		Outputs this message

Table 6.1 Help for Cleard settings environment string `CS_CLEARD_ARGS`

## 6.6 CS\_SHOW\_FPGA\_TEMP

Displays the FPGA core temperature. By default, the FPGA core temperature is not displayed. You must define this variable to display the temperature.

### 6.7 CS\_DISABLE\_DMA

Transfers the memory to the card slowly with PIO, avoiding the DMA engine.

## 7 Kernel level driver

This chapter is of interest to anyone who wants to understand what the kernel driver does. It is a particularly good source of information for people who want to modify the kernel driver so that it can be embedded in the kernel source code.

This chapter describes the various parts of the kernel driver by looking at the main stages the driver goes through:

1. Module loading and unloading
2. Device opening, closing and mmap
3. Interrupt handling
4. DMA ioctls

The driver source code is referred to so it is useful to have this source available.

Many of the terms used in this documentation can be found in *Linux Device Drivers, Third Edition* [10].

### 7.1 Overview

The ClearSpeed Linux driver is a Linux kernel module which provides the lowest level functionality to connect to an *Advance* accelerator board via the PCI. The driver is provided in source form under the terms of the GNU General Public License (GPL) in the following three files:

- `csx_driver.h`  
A header file that contains the definitions common to the user side of the driver and the kernel side. Specifically it defines the ioctls the kernel driver provides.
- `csx_driver.c`  
The source code of the driver.
- `Makefile`  
The makefile for the driver. The driver uses the kBuild mechanism for module building.

The kernel driver acts like a Linux char device, providing the standard functionality that these devices provide:

- `open`
- `close`
- `mmap` and so on.

The driver is also a PCI driver and uses the standard PCI subsystem API to register callbacks and so on. In addition, the driver provides a set of ioctls to implement the extra functionality the *Advance* accelerator board needs specifically for scatter-gather DMA operations.

The driver uses the `get_user_pages()` functionality available only in the 2.6 based kernels to implement the DMA operations hence the driver cannot be used in 2.4 based kernels where a different mechanism is used.

**Note:** The `get_user_pages` functionality became stable after the 2.6.7 release of the kernel and again the driver cannot be used with kernels before this release. `get_user_pages` is heavily used by InfiniBand drivers and these drivers first encountered the `get_user_pages` problems.

The ClearSpeed driver consists of two parts:

- A user-space driver which is provided in a shared library.
- The kernel driver which is provided as a kernel module.

An unusual feature of the ClearSpeed driver is that the kernel module contains few dependencies on the architecture of the ClearSpeed system on the PCI board. Most of the functionality that a standard driver might provide such as handling interrupts is provided by the user-space driver rather than by the kernel side driver.

For each *Advance* accelerator board on a system, the driver dynamically generates two `/dev` (and `/sys/class`) entries. For example, if two PCI *Advance* accelerator boards are discovered, four `/dev/` entries will be created by the driver. The "control device" (`csx_ctl`) (`/dev/csxc`) maps in the control and configuration spaces of the device and the main asynchronous PCI interrupt signal. The "memory device" (`csx_mem`) (`/dev/csxn`) maps the memory space on the device and handles the DMA interrupt. This two device paradigm will be encountered throughout the driver where operations are performed on the control/memory device pair.

In general, the style of the driver is to provide a teardown function which is the exact opposite of the creation function where resources are released in the exact reverse order of their allocation. This document describes the module operating as an "out-of-tree" module, that is, not compiled into the kernel. Once the driver is accepted into the Linux driver tree we hope to provide an in-tree driver.

## 7.2 Module loading and unloading

The driver provides module load and unload functions via `csx_init` and `csx_exit`. `csx_init` obtains a block of device numbers for the attached PCI boards and initialize the `/sys/class` hierarchy for `udev` and user use. `csx_init` also registers the device with the PCI subsystem via a call to `pci_register_driver()`. See *Linux Device Drivers, Third Edition* [10] for details on the PCI subsystem. The driver registers its callback function (`csx_probe`) to be called when a PCI device with the specified `<Vendor id, Device id>` pair is found.

At this point, the driver has not created any `/dev` entries or `/sys/class` entries, this only happens when the PCI subsystem detects a device with the specified `<Vendor id, Device id>` pair, not at module load time. However, without PCI hotplugging, device detection follows shortly after the kernel is booted and this step is described next.



The PCI callback function `csx_probe` is called for each ClearSpeed device found and the steps taken for each device are summarized in Table 7.1.

Steps	Substeps
1. The device is enabled as a PCI device and as a bus master.	Setup of the control device is carried out by the function <code>csx_setup_ctl</code> and consists of: <ul style="list-style-type: none"> <li>a. Establishing a kernel map for the PCI control memory space (BAR0). This is used by the kernel driver to mask interrupts, read various control registers and other control operations. The mapping is built via <code>ioremap_nocache()</code>.</li> <li>b. Initializing the interrupt queue.</li> <li>c. Creating the <code>/dev</code> device entry.</li> </ul>
2. The control device ( <code>/dev/csxcnc</code> ) is created.	
3. The memory device ( <code>/dev/csxnmc</code> ) is created.	The memory device is similar to the control device setup with the following differences: <ul style="list-style-type: none"> <li>• No kernel mapping is built for the memory aperture (BAR2). This is not needed by the kernel and would be wasteful of kernel resources. This behavior can be changed via the <code>CS_NOMAPMEM</code> define.</li> <li>• Coherent DMA space is allocated for the DMA descriptors via <code>dma_alloc_coherent()</code>. This space is used for DMA descriptors while DMA is in flight. The driver specifies the devices PCI address features via a call to <code>dma_set_mask</code> to indicate that the device is 64 bit DMA capable. Note that the default is to have only 32 bit capable PCI devices and without this call the kernel will build bounce buffers for DMA transfers.</li> </ul> <p>At this stage, coherent DMA mappings can be allocated for contiguous DMA buffers used as bounce buffers. This can be used as an alternative to scatter-gather and is enabled via the <code>CSX_BOUNCE</code> define.</p>
4. The interrupt handler is registered.	

Table 7.1 Steps taken by `csx_probe`

A side effect of Step 3 is that the `udev` daemon is called with an ADD device request. Similarly, if the remove function (`csx_remove`) is called, `udev` is called with a REMOVE request. Since the driver presents two devices, a pair of ADD requests is issued to `udev` (matched by a pair of REMOVE requests on release). `udev` will scan the `/sys/class` structure for changes and carry out operations such as setting permissions and setting device symbolic links. The current driver system does not make full use of `udev`. This is due to differences in the `udev` setup on various systems and due to bugs in `udev`. Instead, the installation script performs the device permission settings. One bug in particular caused `udev` to momentarily add a device and then remove it.

However, `udev` could be used for permission settings and the relevant rules could be added to the `udev` rule set on a specific machine. A rule like the following would correctly set the device permissions:

```
KERNEL=="csx",MODE="0666"
```

Once this step is complete, the device is ready for use.

## 7.3 Device opening, closing and mmap

The device pair for a physical PCI device present a standard set of char device operations defined in `csx_ctl_flops` for the control device and `csx_mem_fops` for the memory device. The operations provided are:

- open/release

Standard device open via file descriptor. Note that unusually perhaps, the driver does not impose any access control on open. This functionality is provided in the user-land driver. Any per-connection data structures are allocated (freed).

- poll

Poll is used to wait for an interrupt from the device. The poll on the control device is the asynchronous PCI interrupt wait. The poll on the memory device is the synchronous DMA complete interrupt. The functionality is implemented by adding the caller to an interrupt queue which the actual interrupt handler will release. This model allows the user-land device driver to wait simultaneously on DMA interrupts and PCI interrupts in separate threads.

- read/write

Read and write provide control register reads and writes for the *Advance* accelerator board via the kernel's mapping of the control aperture space. These are not in fact used by the current user-land driver, instead a mapped address space is used. However, these operations can be used if a strict sequentiality is needed by the driver or if aperture mapping is not available.

They are also useful in driver bring up.

The memory device has read and write operations as well as `lseek`. This provides a nonmemory-mapped interface to the memory on the *Advance* accelerator board. `Seek` moves to a position within the aperture and `read` or `write` is then used to access a number of bytes at this offset. These operations are not currently used and the memory mapped interface is used instead.

- mmap

Both the control and memory devices provide `mmap` operations. They are used to provide user-space mappings of the control and data apertures respectively. The data aperture mapping is in fact much smaller than the full data address range available and the aperture acts more like an address window.

This windowing functionality is not provided by the kernel driver but rather by the user-land driver. The mapping is built by `io_remap_page_range` in earlier 2.6 kernels and `remap_pfn_range` in later kernels.

The driver marks the mapping as being for IO with no caching.

The `mmap` operation is used by the user-land driver via the `mmap()` system call on the appropriate open file descriptor.

## 7.4 Interrupt Handling

All interrupts from the device are routed to `csx_intr_handler`. This uses the kernel control mapping to examine the device to determine if the interrupt is PCI or DMA. A thread waiting on these interrupt queues will be woken up. The driver must also acknowledge the interrupt to avoid it being refired once the kernel is left. This acknowledgement is handled differently for the two interrupt sources.

For PCI interrupts, the interrupt mask is closed but the interrupt itself is not cleared. Interrupt clearing is handled in the user-land driver. PCI interrupt clearing is too complex to be handled in the kernel.

For DMA interrupts, the interrupt is acknowledged by the kernel by the usual technique of writing back the interrupt source bits. The interrupt mask is not changed by the kernel driver.

**Note:** The kernel driver uses kernel memory write flushing macros to ensure that these interrupt operations are flushed before leaving the kernel.

## 7.5 DMA ioctls

The DMA engine on the PCI device is programmed by the user-land driver to perform scatter-gather DMA to and from the *Advance* accelerator board. The approach taken is to do as much as possible in the user-land driver and obtain the necessary kernel functionality via ioctls on the memory device.

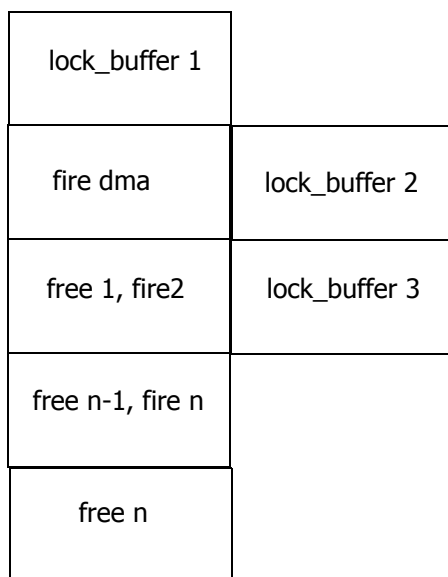
The model called "zero-copy user-space access" DMA allows DMA directly on user data avoiding any bounce buffer copying. For further information, see *get\_user\_pages usage* [11]. Using a bounce buffer can lose 30% of DMA performance.

DMA data buffers use so-called scatter-gather mappings which are a subset of "streaming DMA" mappings which are efficient but have a stringent set of rules concerning their coherency. The DMA descriptors use a "coherent DMA" mapping. The kernel driver uses the scatter-gather coherency functions to mark the buffers as being coherent for the device or coherent for the host. For a general description of DMA mapping, see *Kernel Source Tree/Documentation/DMA-mapping.txt* [12].

In outline, a DMA operation to transfer a single (virtual address) contiguous buffer from host memory to or from the ClearSpeed device is as follows:

```
Obtain physical page address mapping for user buffer: Lock_data_buffer
Construct Descriptor set and program DMA engine
Fire DMA: Fire_DMA
Poll on memory device
Unlock_data_buffer
```

The kernel driver provides a pair of resources for DMA to allow for concurrency between buffer mapping for transfer #N and the actual DMA transfer for transfer #(N-1). This allows for the overlap between DMA preparation functions and DMA traffic as follows:



Thus, only the initial setup and final teardown operations are not overlapped with DMA.

Kernel ioctls require unique numbers and a set of macros are provided by the kernel to help build the necessary unique numbers and to help characterize the ioctl use. For the ClearSpeed ioctl set, use the prefix 0xC5 as the "IOCTL MAGIC" number.

The user-land driver interfaces to the kernel ioctls via a library that provides a function for each ioctl. These wrapper functions collect the ioctl parameters into a struct specific to the ioctl and pass this struct to the kernel trap. A full specification of ioctl functionality, parameter types, return codes and so on is given in this header file. See `csx_driver.h` for more information.

Each ioctl has a checking step in the kernel where all the parameters are validated before the ioctl is executed. This is not described in this document as the intent should be clear from the source code.

The ioctls provided for DMA support are the following:

#### **`csx_lock_buffer_for_dma`**

This ioctl takes the user data buffer and locks the physical page set for the buffer into memory in preparation for DMA. `get_user_pages()` is used to pin the pages to physical addresses and to obtain the physical page addresses. If the virtual buffer has not been accessed and hence does not have any physical addresses, this step can require the kernel to generate the physical addresses. This can be a slow operation but this latency can be alleviated by overlapping the buffer lock and unlock with a DMA transfer.

The second step is to generate the "scatter-gather" list which provides the PCI bus addresses as needed for DMA. This is carried out by the call to `dma_map_sg()`. This allows the kernel to perform any architecture specific operations necessary for memory DMA. Note that `get_user_pages()` returns the number of full pages mapped (or pinned). This will include any partial pages mapped due to nonpage-aligned user buffers. Mapping to the scatter-gather set can result in a different page count such as:

```
pages_pinned by get_user_pages >= pages_mapped by dma_map_sg
```

The kernel driver has to retain this distinction in order to properly free the resources in the matching `unlock_dma` ioctl.

**Note:** Physical memory addresses are not suitable for DMA and the addresses returned by the `dma_map_sg` function must be used. For example, some 32 bit machines remap the PCI address space beyond the 4 GB boundary and on these machines, PCI addresses can be 33 bits in length.

The set of PCI bus addresses for the transfer are returned to the user-land driver where they are used to program the card DMA engine.

#### **`csx_unlock_data_buffer`**

This reverses the lock buffer operation undoing the DMA scatter-gather list mapping and undoing `get_user_pages`. As many others have noted, it is strange that the kernel does not provide a `put_user_pages` function to undo the effect of `get_user_pages`. Instead, `page_cache_release` is called on each pinned page. Note again the distinction between the pages mapped for the `dma_unmap_sg` call and the pinned set as returned by `get_user_pages`.

These two locking functions also use the DMA synch calls to mark the physical pages for coherency. On a write to device operation, `csx_lock_buffer_for_dma` calls `dma_sync_sg_for_device`. On a read from device operation, `csx_unlock_data_buffer` calls `dma_sync_for_cpu`.

#### **`copy_to_desc_buffer`**

The kernel driver reserves DMA coherent space for the DMA descriptors used by the DMA engine. The user-land driver constructs the descriptor set based on the mapped page information from the buffer lock functions and this

ioctl copies the user descriptor set into this DMA coherent space. This is generally safer than using user locked space for DMA descriptors.

#### **csx\_fire\_dma**

This is an ioctl to finally fire the DMA by setting the valid bit in the DMA engine. An ioctl ensures that all the necessary memory and control flush operations have taken place.

#### **csx\_copy\_and\_fire**

This ioctl combines the descriptor copy and DMA fire into a single ioctl call for efficiency.

#### **csx\_get\_page\_size**

This is used to provide the user space with the running kernel's physical page size. This is needed on systems like Itanium where the page size can vary.

## **7.6 Miscellaneous**

### **7.6.1 Class interface**

The device driver provides a pair of class device interfaces for the `/sys/class` hierarchy. The principal reason for this is to provide the structure needed by `udev` but the system allows for arbitrary information to be provided. The function `csx_show_status` gives an example of what can be provided. This displays the version number of the FPGA and driver build time for instance. This structure can be extended to allow the driver to provide extra information to the user space.

### **7.6.2 /proc interface**

Later versions of the kernel driver also provide a `/proc` interface for displaying performance information via `/proc/driver/csxn`. At the moment this is only experimental.

### **7.6.3 Moving functionality into kernel driver**

The split between user land operations and kernel side operations means that the kernel driver ends up knowing little about the device and for performance reasons alone it is worth considering moving some functionality into the kernel. For example, some of the interrupt handling could be moved into the kernel driver. However, there is a feature of the method to access the devices control space which makes this very difficult. Most control information on the csx device is available via the PPCI bus.

To access this bus, the following operations are needed:

1. A write to an address register
2. A read or write to obtain the result.

This makes PPCI accesses nonatomic thus making it impossible for the kernel to access the PPCI space safely.

#### 7.6.4 Resources.

The resources are described in Table 7.2

Resource	Description
csx.ko	Kernel module driver.
/dev/csxC, /dev/csxCm	Control and Memory devices for the N'th CSX device on the PCI bus. Note that N is NOT the slot number but an arbitrary discovery order.
/sys/class/csxc/csxNc	devclass entry for N'th card control device. This directory contains the status file for presenting user information.
/sys/class/csxcmem/csxNm	devclass entry for Nth card memory device.

*Table 7.2*

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