3.3V MACROM

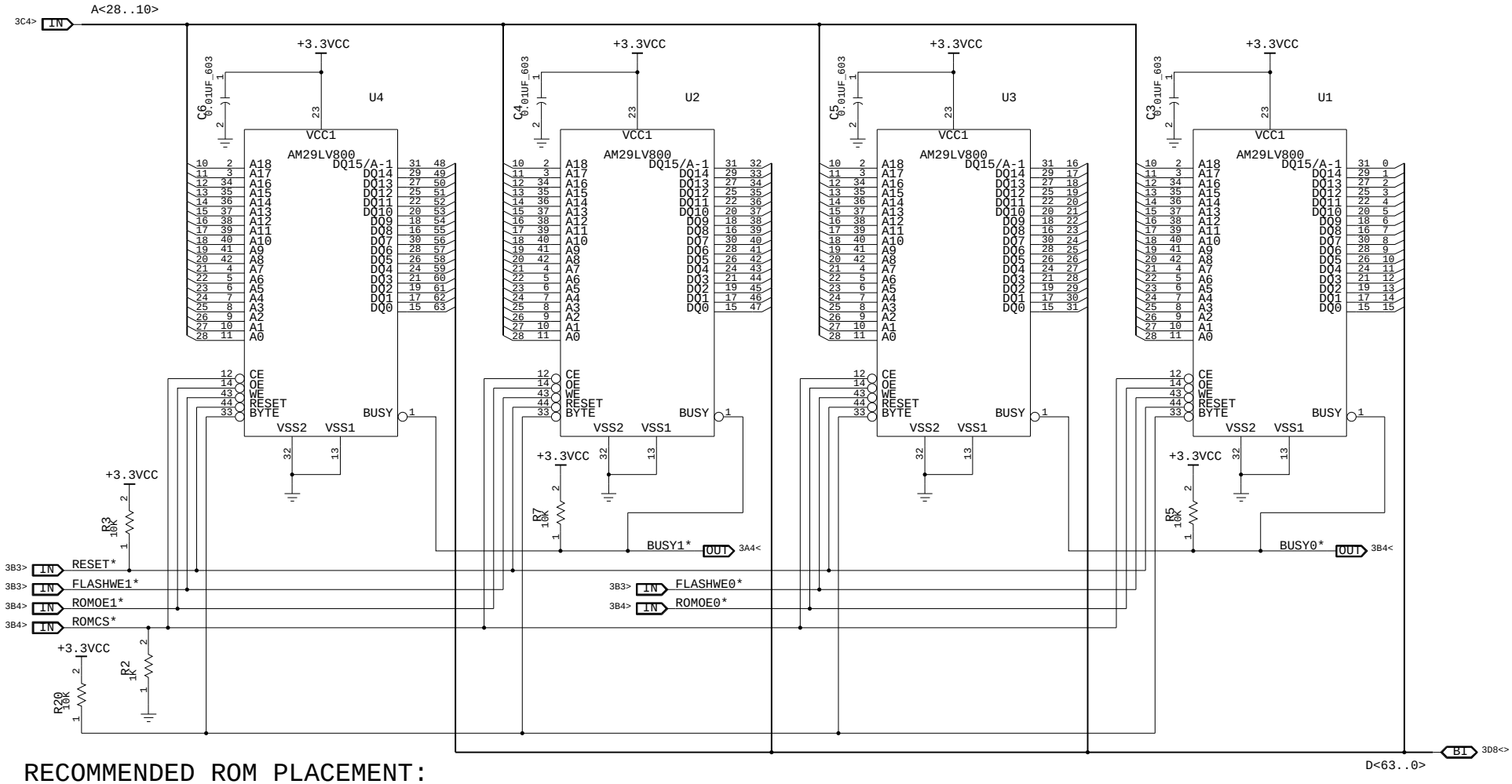
MACINTOSH TOOLBOX ROM

(VER 1.0)

FOR 3.3V APPLE AND CHRP-BASED SYSTEMS

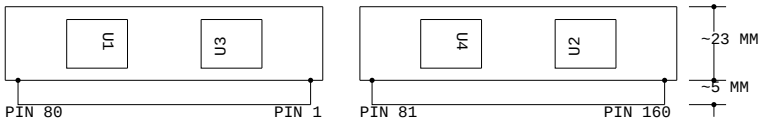
IBM	NATHAN PRITCHARD		
DRAFT	SIZE B	REVISION	DRWNG PART NUMBER
DRAWING MACROM3_3 Thu Aug 22 09:24:30 1996	SHEET 1 OF 5		

PD NOTE: DECOUPLING CAPS SHOULD BE PLACED CLOSE TO ROM VCC PINS



RECOMMENDED ROM PLACEMENT:

PD NOTE: TOTAL DIMM HEIGHT CANNOT EXCEED 28 MM



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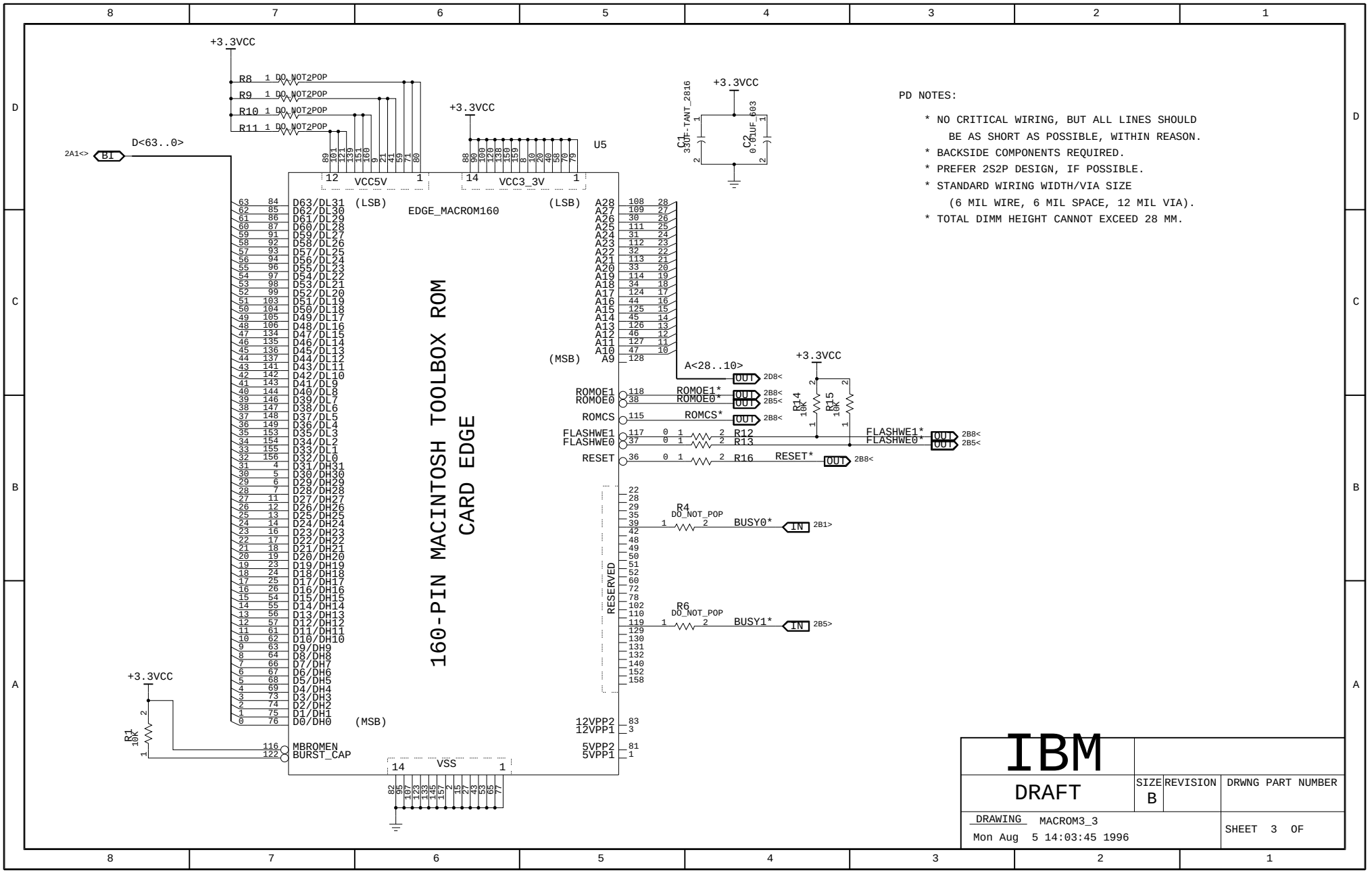
DRAWING MACROM3_3 Mon Aug 5 14:02:59 1996			SHEET 2 OF



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DRAWING MACROM3_3 Mon Aug 5 14:03:45 1996			SHEET 3 OF

*** Signal Cross-Reference ***
--- for the entire design --

A <28..10>	3C4> 2D8<
BUSY0 *	2B1> 3B4<
BUSY1 *	2B5> 3A4<
D <63..0>	2A1<> 3D8<>
FLASHWE0 *	3B3> 2B5<
FLASHWE1 *	3B3> 2B8<
RESET *	3B3> 2B8<
ROMCS *	3B4> 2B8<
ROMOE0 *	3B4> 2B5<
ROMOE1 *	3B4> 2B8<

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SIZE	REVISION	DRWNG PART NUMBER
B		

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*** Unit Cross-Reference ***
--- for the entire design --

C1	CAPACITOR 3D5
C2	CAPACITOR 3D4
C3	CAPACITOR 2D2
C4	CAPACITOR 2D6
C5	CAPACITOR 2D4
C6	CAPACITOR 2D7
R1	RESISTOR 3A8
R2	RESISTOR 2A8
R3	RESISTOR 2B8
R4	RESISTOR 3B5
R5	RESISTOR 2B2
R6	RESISTOR 3A5
R7	RESISTOR 2B6
R8	RESISTOR 3D7
R9	RESISTOR 3D7
R10	RESISTOR 3D7
R11	RESISTOR 3D7
R12	RESISTOR 3B4
R13	RESISTOR 3B4
R14	RESISTOR 3B4
R15	RESISTOR 3B4
R20	RESISTOR 2A8
U1	AM29LV800 2D2
U2	AM29LV800 2D5
U3	AM29LV800 2D3
U4	AM29LV800 2D7
U5	EDGE_MACROM160 3D5

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