

IBM posts SPEC CPU2006 scores for x3400 M3

x3400 M3 delivers competitive processor performance for compute-intensive applications

March 15, 2011 ... IBM® today announces SPEC® CPU2006 benchmark scores for the IBM System x®3400 M3 server using the Intel® Xeon® Processor X5675.

The x3400 M3 delivered competitive scores with two Intel Xeon X5675 processors (3.06GHz, 256KB L2 cache per core and 12MB L3 cache per processor—2 processors/12 cores/24 threads), 96GB of DDR3 PC3L-10600R memory, and SUSE Linux® Enterprise Server 11 x64. (1)

The scores in the following table are the first SPEC CPU2006 results published for this x3400 M3 processor model.

SPEC CPU2006 Benchmark	Six-Core Intel Xeon Processor X5675 – 3.06GHz
SPECint®2006	43.2
SPECint_base2006	40.9
SPECint_rate2006	391
SPECint_rate_base2006	364
SPECfp®2006	61.5
SPECfp_base2006	57.3
SPECfp_rate2006	262
SPECfp_rate_base2006	254

The System x3400 M3 servers are self-contained, high-performance, 5U tower (optional rack-mounted) systems designed for Web and business server applications in remote or distributed environments. These servers are highly scalable in configuration, performance, and availability. They are flexible tower models that deliver the best blend of power, manageability, expandability, and serviceability. They meet the requirements of server applications in small-to-medium businesses that need an affordable general-purpose network server. These servers fit into business environments where tower configurations are required.

Results are current as of March 15, 2011. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at www.spec.org.

(1) The x3400 M3 model using the Intel Xeon Processor X5675 is planned to be generally available March 15, 2011. The x3400 M3 as configured for this benchmark will be available March 15, 2011.

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