IBM posts SPEC CPU2006 scores for single-socket System x3100 M5

IBM System x3100 M5 delivers competitive performance for a server using Intel Xeon E3-1281 v3 processor

May 13, 2014 ... The IBM System x3100 M5 is a single-socket tower server, equipped with Intel's latest quad-core processor technology, the Intel® Xeon® Processor E3-1200 v3 series. Competitively priced in a compact tower, the x3100 M5 delivers business class performance and reliability for small to medium businesses, distributed environments, and first-time server buyers seeking a solution to improve business efficiency.

The x3100 M5 was configured with the Intel Xeon E3-1281 v3 processor (3.7GHz, 8MB cache per processor—1 processor/4 cores/2 threads),), 4x4GB of PC3L-12800 memory, and Red Hat® Enterprise Linux 6.5 x64. (1).

The scores in the following table are the first SPEC CPU2006 results published for the x3100 M5 using this processor.

SPEC CPU2006 benchmark	Quad-core Intel Xeon E3-1281 v3 (3.7GHz, 8MB Cache)
SPECint®2006	65.2
SPECint_base2006	62.6
SPECint_rate2006	228
SPECint_rate_base2006	222
SPECfp®2006	78.4
SPECfp_base2006	77.0
SPECfp_rate2006	157
SPECfp_rate_base2006	152

Results are current as of May 13, 2014. The scores have been submitted to SPEC for review and will be posted on their Web site upon successful completion of the review. View all published results at http://www.spec.org/cpu2006/results/.

(1) The x3100 M5 is planned to be generally available June 18, 2014.