



Performance Brief

Netfinity 7600 delivers powerful performance for e-business computing

September 2000

IBM Netfinity® 7600 servers are high-throughput, four-way SMP-capable Pentium® III Xeon®-based network servers that incorporate the powerful 700MHz¹ Pentium III Xeon processor with integrated 2MB of full-speed ECC L2 cache.

The Netfinity 7600 servers are designed to maximize total system throughput from processor, to memory, to bus, to disk-intensive I/O. These features, combined with four-way SMP capability, make the Netfinity 7600 an excellent choice as an advanced midrange database server or high-speed Internet e-business solution.

The SPECweb99™ benchmark was used to measure the Netfinity 7600 server's performance in 4-way, 2-way and 1-way processor configurations. The SPECweb99³ results are summarized below.

IBM Netfinity 7600 - Simultaneous Connections		
Four Processors	Two Processors	One Processor
1,570	1,182⁴	968
System Hardware		
700MHz Pentium III Xeon / 2MB L2 Cache		
8GB Memory	8GB Memory	4GB Memory
7 x 9.1GB ² 10K Ultra160 Hard Disk Drives		
Netfinity ServeRAID®-3HB SCSI Adapter		
Software		
Microsoft® Windows™ 2000 Advanced Server		
Microsoft Internet Information Server 5.0		
Network Hardware		
Alteon® ACEnic™ PCI Adapter		
Alteon ACEswitch™ GbE		

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Notes

(1) MHz only measures microprocessor internal clock speed, not application performance. Many factors affect application performance.

(2) When referring to hard disk capacity, GB, or gigabyte, means one thousand million bytes. Total user-accessible capacity may vary depending on operating environment.

(3) SPECweb99 measures the maximum number of simultaneous connections, requesting the predefined benchmark workload that a Web server is able to support while still meeting specific throughput and error rate requirements. The connections are made and sustained at a specified maximum bit rate with a maximum segment size intended to more realistically model conditions that will be seen on the Internet during the lifetime of this benchmark.

(4) Leading result for a 2-way Intel-based server when published in June 2000.

Results referenced in this document are current as of September 21, 2000.

